

6.

a) To ~~max~~ eliminate s.s. position tracking error for step input, controller must have the form  $(K_p + \frac{K_i}{s})$  in it

Thus, we have a P.I. controller.

Through testing in MATLAB, we found

$$C_{PI} = 10^{-7} + \frac{10^{-2}}{s}$$

To track a 3,000 rad/s input, yet reject  $10^5$  rad/s noise,

we set gain cross over frequency between those two values. A  $\omega_g \approx 10^4$  should accomplish this. ~~well that's~~

~~well that's~~ We chose  $\omega_g = 1.67 \times 10^4$  to be a better median point between the input frequency and noise frequency.

For the compensator, we chose a lead-lag controller.

Lead

Set zeros a decade before  $\omega_g$  and poles a decade after

$$C_{lead} = \left( \frac{1 + \frac{s}{6 \times 10^3}}{1 + \frac{s}{6 \times 10^4}} \right)^3$$

With this, the loop needs 92 dB of Gain to have the cross over frequency at  $1.67 \times 10^4$  rad/s.

$$92 = 20 \log(K)$$

$$K = 10^{92/20}$$

$$\text{so } C_{lead} = 10^{92/20} \left( \frac{1 + \frac{s}{6 \times 10^3}}{1 + \frac{s}{6 \times 10^4}} \right)^3$$

Lag

$$K_p = 100,000$$

This is set to ensure low s.s. tracking error for the step input.

$$K_{pold} = \lim_{s \rightarrow 0} s \cdot C_{lead} \cdot L(s) = 10^{92/20} \cdot -5$$

$$K_{lag} \text{ ~~K_{pold}~~} = \frac{K_p}{K_{pold}} = \frac{100,000}{K_{pold}} = -0.502377$$

$$C_{lag} \text{ takes the form } K_{lag} \frac{(1+\tau s)}{(1+\alpha\tau s)}$$

We keep the high frequency gain of the lag compensator 1 so as to not change the cross over frequency.

$$\text{So, } \frac{|K_{lag}|}{\alpha} = 1 \Rightarrow \alpha = 0.502377$$

Similarly, the zero of the lag compensator must be AT LEAST a decade below  $\omega_g$ .

$$\text{So, } \tau = \frac{1}{100}$$

$$\text{so, } C_{lag} = -5.02377 \times 10^{-1} \cdot \frac{\left(1 + \frac{s}{100}\right)}{\left(1 + 5.02377 \times 10^{-3} s\right)}$$

Thus

$$\begin{aligned} C_{mech}(s) &= C_{PI} \cdot C_{lead} \cdot C_{lag} \\ &= \left(10^{-7} + 10^{-2}/s\right) \cdot 10^{92/20} \cdot \left(\frac{1 + \frac{s}{6 \times 10^3}}{1 + \frac{s}{6 \times 10^4}}\right)^3 \cdot -5.02377 \times 10^{-1} \cdot \frac{\left(1 + \frac{s}{100}\right)}{\left(1 + 5.02377 \times 10^{-3} s\right)} \end{aligned}$$

This compensator design focuses heavily on steady-state performance. We achieved a sinusoidal tracking error under 10% while nearly eliminating noise error. Unfortunately, the noise attenuation came at the cost of the phase margin and overshoot (23%).

As mentioned earlier, the PI compensator is used to eliminate step tracking error. Then, the Lead compensator is used to increase input frequency gain and phase margin at the cross over frequency. The lag compensator lowers the gain at the noise frequency.