TUL KU115 INTRODUCTION

Config SW   User DIP SW   Reset SW   Program SW   JTAG Header   PMBus

DDR4 SDRAM

QSFP28

DDR4 SDRAM

QSFP28

USB-JTAG Connector

DDR4 SDRAM

PCIE Gen3 x16 connector

Xilinx® Kintex® Ultrascale™
XCKU115-2FLVB2104E

DDR4 SDRAM

DDR4 SDRAM

DDR4 SDRAM
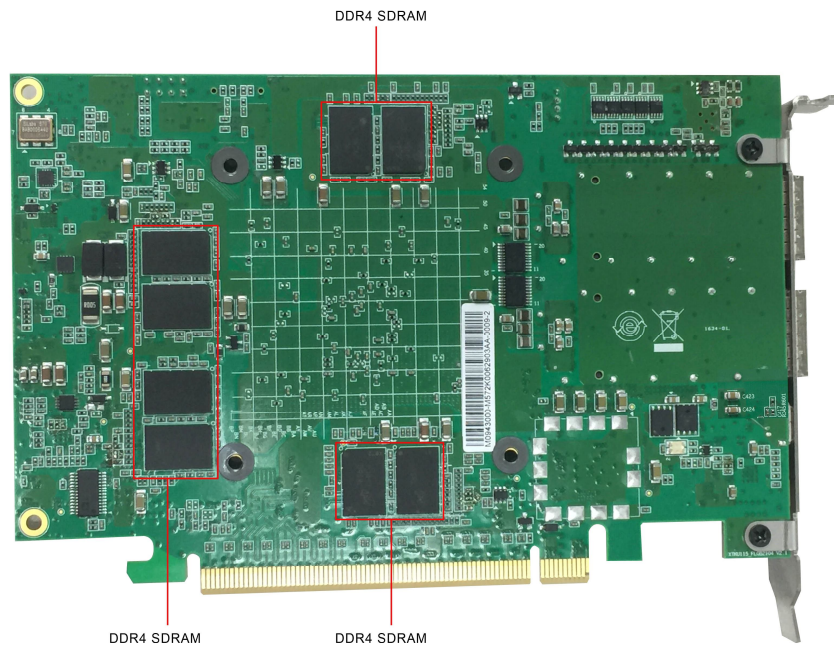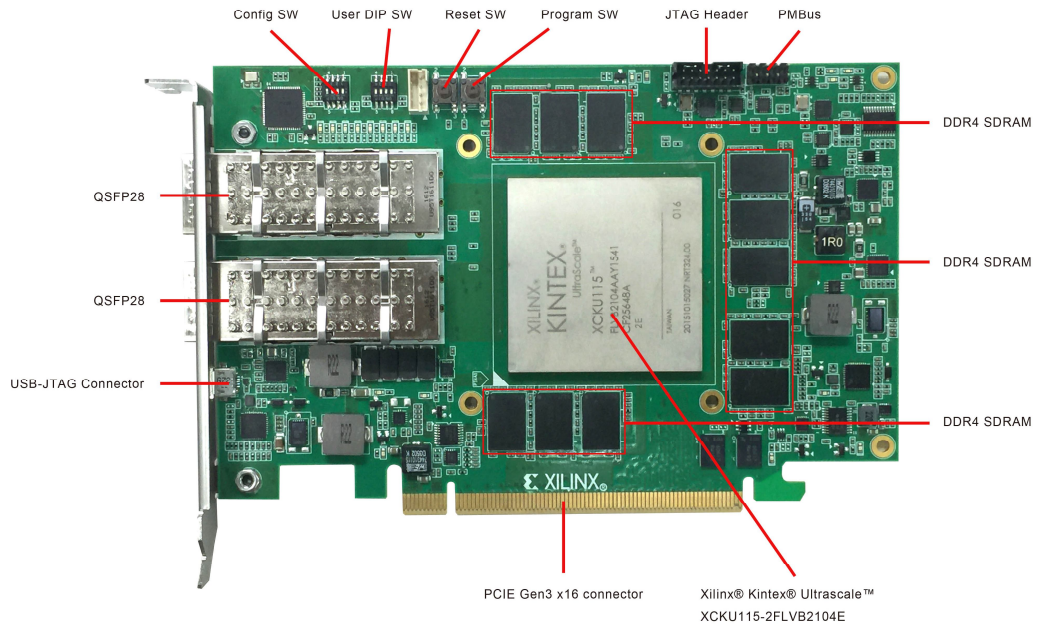
Product Specification

- ➢ **PCIe Compliant in a Half-Length, Full-Height form factor**
- ➢ **Two (2) PCIe Gen3 x16 with bifurcation to dual x8 links or single x8 link without bifurcation**
- ➢ **Xilinx® Kintex® Ultrascale™ XCKU115-2FLVB2104E**
- ➢ **Four (4) DDR4 Interfaces (soldered down devices) - three (3) 72bit and one (1) 64bit capable of operating to 2133MT/s**
- ➢ **On-board re-programmable flash memory for configuration**
- ➢ **Two (2) 40Gb Ethernet ports with QSFP28 connector**
- ➢ **JTAG (micro USB) port for programming**
- ➢ **Variable cooling fan operation for optimal ambient noise performance or passive heat sink**
- ➢ **On-board power telemetry via PMBus**

Applications

Video Processing

Machine Learning

High Performance Computing

Network Acceleration

Ordering PN

1M1-M000930XLG retail package
1M1-M000930XLB bulk package

Retail package:

1M1-M000930XLG

Bulk package:

1M1-M000930XLB