Left most edge	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	Right most edge
A Top	5V	3V3	GND	VOUT B1.diff	VOUT A1.1	VOUT A2.1	VOUT B2.diff	GND	VOUT B3.diff	VOUT A3.1	VOUT A4.1	VOUT B4.diff	GND	VOUT B5.diff	VOUT A5.1	VOUT A6.1	VOUT B6.diff	GND			GPIO 20	-5V	GND	GND	CLK 2	CLK 3	VDD IO	GND	GPIO 19	GPIO 18	GPIO 17	GPIO 16	GND	VOUT B7.diff	VOUT A7.1	VOUT A8.1	VOUT B8.diff	GND	VOUT B9.diff	VOUT A9.1	VOUT A10.1	VOUT B10. diff	GND	VOUT B11. diff	VOUT A11.1	VOUT A12.1	VOUT B12. diff	GND	3V3	5V	**A** Top
B Center	5V	3V3	GND	VOUT B1.1	VOUT A1.1	VOUT A2.1	VOUT B2.1	GND	VOUT B3.1	VOUT A3.1	VOUT A4.1	VOUT B4.1	GND	VOUT B5.1	VOUT A5.1	VOUT A6.1	VOUT B6.1	GND	SPI FLASH CS	SPI FLASH SD0	SPI FLASH SD2	UART RX	GND	JTAG TCK	JTAG TMS	nRST	BOOT sel	GND	GND	EXIT Valid	EXIT Value	GND	GND	VOUT B7.1	VOUT A7.1	VOUT A8.1	VOUT B8.1	GND	VOUT B9.1	VOUT A9.1	VOUT A10.1	VOUT B10.1	GND	VOUT B11.1	VOUT A11.1	VOUT A12.1	VOUT B12.1	GND	3V3	5V	**B** Center
C Bottom	VDD IO	-5V	GND	I2C SDA	I2C SCL			GND	GPIO 15	GPIO 14	GPIO 13	GPIO 12	GND	GPIO 11	GPIO 10	GPIO 09	GPIO 08	GND	SPI FLASH SCK	SPI FLASH SD1	SPI FLASH SD3	UART TX	GND	JTAG TDO	JTAG TDI	JTAG TRST	EXEC FLASH	GND	CLK fast	GND	VDD IO	CLK 32khz	GND	GPIO 07	GPIO 06	GPIO 05	GPIO 04	GND	GPIO 03	GPIO 02	GPIO 01	GPIO 00	GND	3V3	5V	-5V		GND	-5V	VDD IO	**C** Bottom
Left most edge	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	Right most edge

Notch: There is a dent in the PCB that you should match on the breakout board to make sure you align the connector properly.