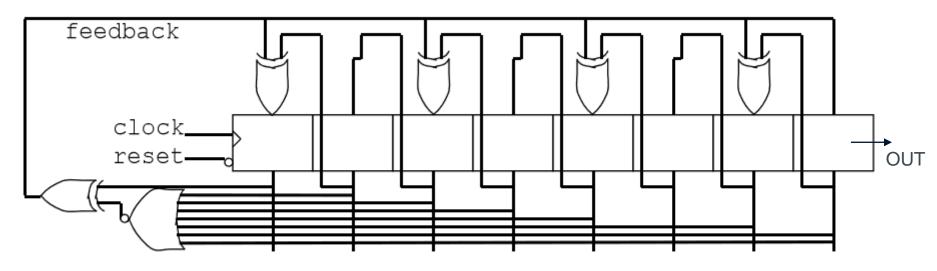
LFSR (Linear Feedback Shift Register)

Specification

- LFSR is initialized with a Seed input on reset activation
- LFSR operation enable signal is activated for 10 clock cycles, then result sequence is available in the LFSR
- IF out_enable signal is activated, LFSR result could be serially out through LFSR[0] with Valid signal



LFSR[7] LFSR[6] LFSR[5] LFSR[4] LFSR[3] LFSR[2] LFSR[1] LFSR[0]