

Function	c	C \rightarrow Verilog		Verilog		MFE	GTD
		Non-XOR	Total gates	Non-XOR	Total gates		
16384-bit Compare	1,024	51	70	16	64	0.91	219%
	4,096	15	21	4	16	0.76	275%
	16,384	6	9	1	4	0.44	500%
160-bit Hamming	1	1,264	2,142	371	1,230	0.57	241%
	32	91	146	17	35	0.24	435%
	160	45	71	10	19	0.27	350%
1024-bit Sum	1	3,067	5,115	1,023	5,114	1.00	200%
	32	195	292	32	160	0.55	509%
	1,024	9	11	1	5	0.45	800%