R3 CheatSheet - https://github.com/phreda4/ - PHREDA

Block const	ruction				
(Start block for IF or WHILE)		End block for IF or WHILE
Nameless de	efinition		,		
Г		Start nameless definition]		End nameless definitions
Control flow					
· ,		End of Word	EX	V	Run a word from address
Conditional					
0?	a a	is TOS=Zero? conditional	1?	a a	is TOS<>Zero? conditional
+?	a a	is TOS>=0?	-?	a a	is TOS<0?
</td <td> a b a</td> <td>is a<b? remove="" td="" tos<=""><td>>?</td><td> a b a</td><td>is a>b? remove TOS</td></b?></td>	a b a	is a <b? remove="" td="" tos<=""><td>>?</td><td> a b a</td><td>is a>b? remove TOS</td></b?>	>?	a b a	is a>b? remove TOS
=?	a b a	is a=b? remove TOS	>=?	a b a	is a>=b? remove TOS
<=?	a b a	is a<=b? remove TOS	<>?	a b a	is a<>b? remove TOS
AND?	a b c	is a AND b? remove TOS	NAND?	a b c	is a NAND b? remove TOS
BT?	a b c a	is a<=b<=c? remove TOS			
Stack mover		dunlicate TOC	DROP	1.0	remove TOS
OVER	a – aa ab aba	duplicate TOS duplicate Second of Stack	PICK2	a abc abca	Pick 3 element
PICK3	1	Pick 4 element	PICK2 PICK4	abcde abcdea	
SWAP	abcu abcua	swap TOS ans NOS	NIP	abcue abcuea	remove NOS
ROT	abc bca	Rotate 3 top element	2DUP	l ab abab	Duplicate 2 values of top
2DROP	ab	Remove 2 elements	3DROP	l abc	Remove 3 elements
4DROP	l abcd	Remove 4 elements	20VER	abcd abcdab	Copy 2 lower elemenst
2SWAP	abcd cdab	Swap 4 elements		, see asouth	
Return Stack	11	onap i didilicite			
>R	l a	rstack: a	R>	l a	rstack: a
R@	a	rstack: a a			
Logic operat	tors				
AND	a b c	c=a AND b	OR	a b c	c=a OR b
XOR	a b c	c=a XOR b	NOT	a b	b=NOT a
Aritmetic op	erators				
+	a b c	d=a+b	-		d=a-b
*	a b c	d=a*b	/	a b c	d=a/b
<<	a b c	d=a shift left b	>>	a b c	d=a shift rigth b
>>>	a b c	d=a shift rigth b w/o sign	MOD	a b c	d=a mod b
/MOD	a b c d	c=a/b d=a mod b	*/	a b c d	d=a*b/c - not bit loss
*>>	abcd	d=(a*b)>>c – not bit loss	< </td <td> a b c d</td> <td>d=(a<<c) b="" bit="" loss<="" not="" td="" –=""></c)></td>	a b c d	d=(a< <c) b="" bit="" loss<="" not="" td="" –=""></c)>
NEG	a b	b=-a	ABS	a b	b= a
_	a b	b=square root(a)	CLZ	a b	b=count lead zeros of a
Memory fetc		fetch dword adress	C@	l a b[a]	fatch byte from advace
@ 0@	a [a] a q[a]	fetch gword adress	<u></u>	a b[a] a b [a]	fetch byte from adress fetch value and increment 4
Q@ C@+	a q[a] a b b[a]	fetch byte and increment 1	Q# Q@+	a b [a] a b q[a]	fetch gword and increment 8
1	<u>'</u>	poton byte and increment 1	$\mathcal{A}\omega^{\perp}$	n a u ulai	poton gword and incidition
	llah	store A in adress R	<u></u>	'	store byte A in adress R
	a b a h	store A in adress B	C!	a b	store byte A in adress B
Ci+	a b	store qword A in adress B	!+	a b a b c	store A in B and inc 4
C!+	a b a b c	store qword A in adress B store byte A in B and inc 1	!+ Q!+	a b a b c a b c	store A in B and inc 4 store qword A in B and inc 8
C!+ +!	a b a b c a b	store qword A in adress B store byte A in B and inc 1 increment in mem B, A	!+	a b a b c	store A in B and inc 4
C!+ +! Q+!	a b a b c a b a b	store qword A in adress B store byte A in B and inc 1	!+ Q!+	a b a b c a b c	store A in B and inc 4 store qword A in B and inc 8
C!+ +!	a b a b c a b a b	store qword A in adress B store byte A in B and inc 1 increment in mem B, A	!+ Q!+	a b a b c a b c	store A in B and inc 4 store qword A in B and inc 8
C!+ +! Q+! Auxiliary reg	a b a b c a b a b sters	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A	C+i Ói+ i+	a b a b c a b c a b	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A
C!+ +! Q+! Auxiliary reg	a b a b c a b a b sters a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A	Q!+ C+!	a b a b c a b c a b	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A
C!+ +! Q+! Auxiliary reg >A A@	a b a b c a b a b isters a a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A fetch from A	!+ Q!+ C+! A>	a b a b c a b c a b a a	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A store in mem A
C!+ +! Q+! Auxiliary reg >A A@ A+	a b a b c a b a b sters a a a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A fetch from A add to A	!+ Q!+ C+! A> A! A@+	a b a b c a b c a b a a a	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A store in mem A fetch A and increment 4
C!+ +! Q+! Auxiliary reg >A A@ A+ A!+	a b a b c a b a b sters a a a a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A fetch from A add to A store in mem A, increment 4 push register B store in mem B	!+ Q!+ C+! A> A! A@+ >B	a b a b c a b c a b a a a a	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A store in mem A fetch A and increment 4 load register B
C!+ +! Q+! Auxiliary reg >A A@ A+ A!+ B> B! B@+	a b a b c a b a b sters a a a a a a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A fetch from A add to A store in mem A, increment 4 push register B	!+ Q!+ C+! A> A! A@+ >B B@	a b a b c a b c a b a a a a	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A store in mem A fetch A and increment 4 load register B fetch from B
C!+ +! Q+! Auxiliary reg >A A@ A+ A!+ B> B! B@+ Memory cop	a b a b c a b a b sters a a a a a a a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A fetch from A add to A store in mem A, increment 4 push register B store in mem B fetch B and increment 4	!+ Q!+ C+! A> A! A@+ >B B@ B+ B!+	a b a b c a b c a b c a b c a b a a a a a a a a a	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A store in mem A fetch A and increment 4 load register B fetch from B add to B store in mem A, increment 4
C!+ +! Q+! Auxiliary reg >A A@ A+ A!+ B> B! B@+	a b a b c a b a b sters a a a a a a	store qword A in adress B store byte A in B and inc 1 increment in mem B, A increment in mem B, A load register A fetch from A add to A store in mem A, increment 4 push register B store in mem B	!+ Q!+ C+! A> A! A@+ >B B@ B+	a b a b c a b c a b a a a a a	store A in B and inc 4 store qword A in B and inc 8 increment in mem B, byte A push register A store in mem A fetch A and increment 4 load register B fetch from B add to B

QMOVE d s c copy S to D, C qwords QMOVE> d s c copy from S to D, QFILL d v c fill D, C qwords with V copy from S to D, Operating System UPDATE update SO events REDRAW refresh graphic but MEM a start memory free VFRAME a frame buffer adress SH a screen height SW a screen width	uffer ss
Operating System UPDATE update SO events REDRAW refresh graphic but refresh gr	SS
UPDATE update SO events REDRAW refresh graphic but MEM a start memory free VFRAME a frame buffer adress	SS
MEM a start memory free VFRAME a frame buffer adres	SS
SH a screen height SW a screen width	o or non
	o or non
XYPEN x y position of mouse or pen BPEN a key state of mous	e or berr
KEY a key code CHAR a character ascii co	de
TIME a Hour(8):min(8):sec(8) DATE a Year(16):month(8):day(8)
MSEC a milisecond of system APPEND m cnt "fn" append file from N	Л, C bytes
LOAD m "fn" Im load file in M, last in LM SAVE m cnt "fn" save file from M, C	C bytes
FFIRST "f" s get first struct of folder "f" FNEXT a s next struct or 0 to	end
SYS "sys" call SO to run program	
Graphics drawing	
INK color value of pen color 'INK 'ink adress of color to	set
ALPHA a set alpha value OP x y set last point	
OPX opx last x point OPY opy last y point	
LINE x y lineto CURVE x y x y curve cuadratic be	ezier
CURVE3 x y x y x y curve qubic bezier PLINE x y lineto polygon	
PCURVE x y x y curve cuadratic bezier poly PCURVE3 x y x y x y curve qubic bezie	r polygon
POLI fill polygon	
Sound and Music	
SLOAD "fn" s Load sound, stack adr SFREE s Free sound with a	ıdr
MLOAD "fn" m Load music, stack adr MFREE m Free music with a	.dr
SPLAY s Play sound, 0 stop MPLAY m Play music, 0 stop	o
Video Playback (r3v version only)	
VIDEO "fn" w h 0 close video VIDEOSHOW w h v	
VIDEOSIZE w h	

Prefix	
:	define CODE, :: Export word
#	define DATA, ## Export word
^	Include source code in filename
'	Adress of word, code or data
	Commento to end of the line
	String to next ", "" for " character
\$	Hex numbers
%	Binary numbers, 0 can be .

Data Definition				
dword	#var 0			
dword list	#list 1 2 3 4 5			
byte list	#blist (1 2 3 4)			
memory	#buffer * 1024 1kb size			
vectors	#vector 'actionword			
list jump	#listj 'a1 'a2 'a3			

Control Flow	
REPEAT	(loop)
IF	?? (true branch)
WHILE	(while ?? loop)
MULTI WHILE	(while ?? while ?? loop)
IF-ELSE	factoring to new word
	:ifelse ?? (true ;) false ;

Comment work like option switchs		
WIN	in win, the line is not a comment	
LIN	in lin, the line is not a comment	
FULL	set fullscreen mode	
ISCR 640 480	screen or window size	
MEM 640	data memory size (in kb) min 1kb	