



		<u>DPORT addr:</u>	<u>AHB addr:</u>
		(speculative reads may not be safe)	(Safe FIFO access)
<div>  ESP32 </div> <div> Peripherals </div>	<u>0x3FF0_0000</u>		
	0x3FF0_0000	DPORT	
	0x3FF0_1000	AES	
	0x3FF0_2000	RSA	
	0x3FF0_3000	SHA	
	0x3FF1_0000	FLASH_MMU_TABLE_PRO	
	0x3FF1_2000	FLASH_MMU_TABLE_APP	
	0x3FF1_3FFC	DPORT_END	
	0x3FF4_0000	UART(UART_FIFO)	0x6000_0000 UART_FIFO
	0x3FF4_2000	SPI1	
	0x3FF4_3000	SPI0	
	0x3FF4_4000	GPIO	0x6000_4000 GPIO_REGS**
	0x3FF4_4F00	GPIO_SD	
	0x3FF4_5000	FE2	
	0x3FF4_6000	FE	
	0x3FF4_7000	FRC_TIMER	
	0x3FF4_8000	RTCCNTL	
	0x3FF4_8400	RTCIO	
	0x3FF4_8800	SENS	
	0x3FF4_8C00	RTC_I2C	
	0x3FF4_9000	IO_MUX	
	0x3FF4_B000	HINF (SDIO_SLAVE0)	
	0x3FF4_C000	UHCI1 (UDMA1)	
	0x3FF4_F000	I2S	
	0x3FF4_F004	I2S0_FIFO_RD	0x6000_F004 I2S_FIFO_RD
	0x3FF5_0000	UART1(UART1_FIFO)	0x6001_0000 UART1_FIFO
	0x3FF5_1000	BT	
	0x3FF5_3000	I2C_EXT	
	0x3FF5_4000	UHCI0	
	0x3FF5_5000	SLCHOST (SDIO_SLAVE1)	
	0x3FF5_6000	RMT	
	0x3FF5_7000	PCNT	
	0x3FF5_8000	SLC(SDIO_SLAVE2)	
	0x3FF5_9000	LEDC	
	0x3FF5_A000	EFUSE	
	0x3FF5_B000	SPI_ENCRYPT	
	0x3FF5_CC00	NRX	
	0x3FF5_D000	BB	
	0x3FF5_E000	PWM0	
	0x3FF5_F000	TIMERGROUP0	
	0x3FF6_0000	TIMERGROUP1	
	0x3FF6_1000	RTCMEM0	
	0x3FF6_2000	RTCMEM1	
	0x3FF6_3000	RTCMEM2	
	0x3FF6_4000	SPI2	
	0x3FF6_5000	SPI3	
	0x3FF6_6000	SYSCON(APB_CTRL)	
	0x3FF6_7000	I2C1_EXT	
	0x3FF6_8000	SDMMC	
	0x3FF6_9000	EMAC	
	0x3FF6_B000	TWAI	
	0x3FF6_C000	PWM1	
	0x3FF6_D000	I2S1	
	0x3FF6_D004	I2S1_FIFO_RD	0x6002_D004 I2S1_FIFO_RD
	0x3FF6_E000	UART2	0x6002_E000 USART2_FIFO
	0x3FF7_5000	RNG	

 ESP32 e-Fuses	<i>Blk#</i>	<i>Bits</i>	<i>Purpose</i>
	0	256/192/128	Depends on coding scheme
	1	256/192/128	Depends on coding scheme
	2	256/192/128	Depends on coding scheme