

6.11.2020

3.1

- 3.1 a) Show that the solution of the circuit in Fig. 3.9 (see Example 3.1) satisfies Kirchhoff's current law at junctions x and y.
b) Show that the solution of the circuit in Fig. 3.9 satisfies Kirchhoff's voltage law around every closed loop.

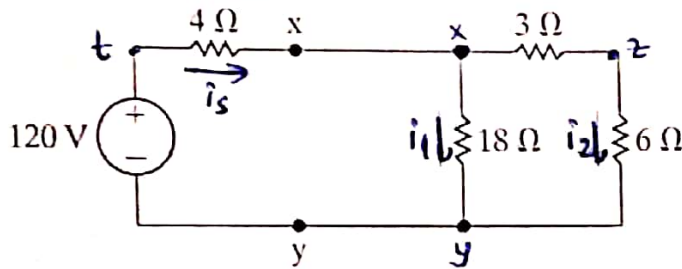


Figure 3.9 ▲ The circuit for Example 3.1.

Currents was found other example

$$i_s = 12 \text{ A}, i_1 = 4 \text{ A}, i_2 = 8 \text{ A}$$

a) For node x:

$$i_s = i_1 + i_2$$

$$12 = 4 + 8$$

For node y:

$$i_s = i_1 + i_2$$

$$12 = 4 + 8$$

(Node 'a' gives akımlar çıkımlara eşittir)



b) loop txyt

$$-120 + 48 + 72 = 0$$

loop xzyx

$$-72 + 24 + 48 = 0$$

loop txzyt

$$-120 + 48 + 24 + 48 = 0$$

3.2

- 3.2 a) Find the power dissipated in each resistor in the circuit shown in Fig. 3.9.
b) Find the power delivered by the 120 V source.
c) Show that the power delivered equals the power dissipated.

$$\text{Power} = P = i^2 \cdot R = V \cdot i$$

$$a) P_{4\Omega} = 12^2 \cdot 4 = 576 \text{ W}$$

$$P_{3\Omega} = 8^2 \cdot 3 = 192 \text{ W}$$

$$P_{18\Omega} = 4^2 \cdot 18 = 288 \text{ W}$$

$$P_{6\Omega} = 8^2 \cdot 6 = 384 \text{ W}$$

$$b) P_{120V} = 120 \cdot i = 120 \cdot 12 = 1440 \text{ W}$$

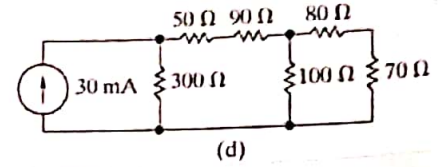
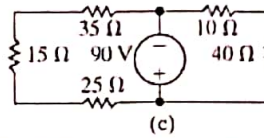
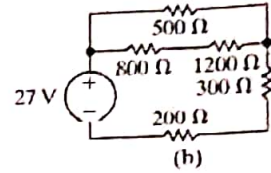
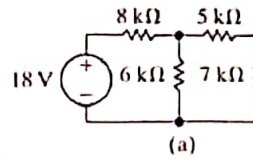
$$c) 1440 = 576 + 192 + 288 + 384$$

$$1440 = 1440 \quad \checkmark$$

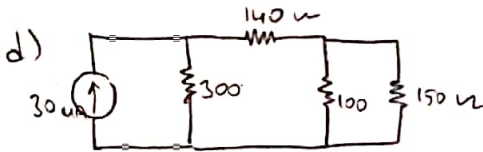
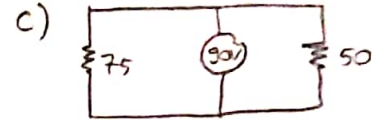
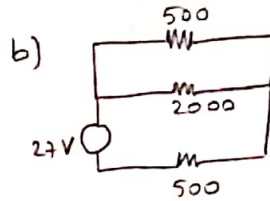
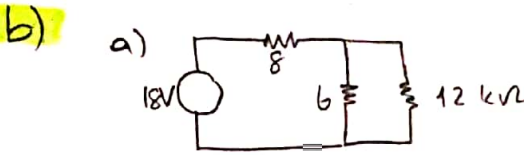
3.3

3.3 For each of the circuits shown in Fig. P3.3,
a) identify the resistors connected in series.
b) simplify the circuit by replacing the series-connected resistors with equivalent resistors.

Figure P3.3

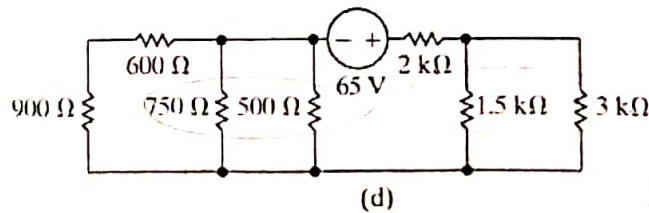
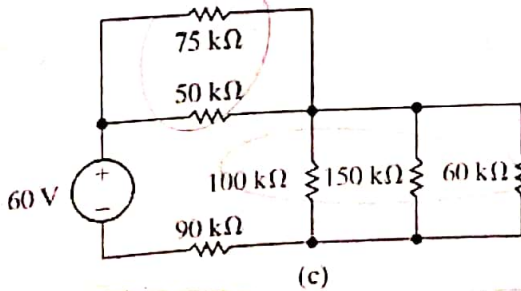
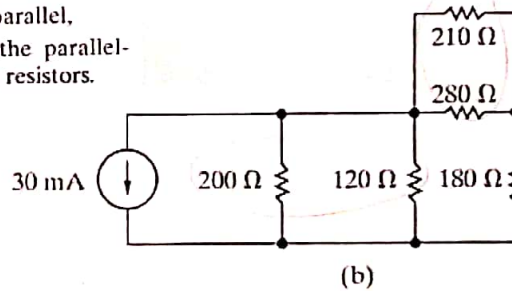
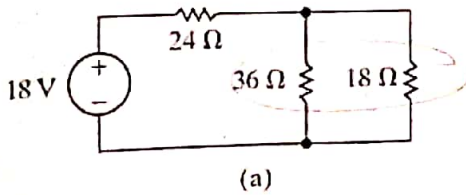


- a) circuit
- a) 5 kΩ and 7 kΩ in series
 - b) 800 Ω and 1200 Ω // and 300 Ω 200 Ω series
 - c) 35 Ω, 15 Ω and 25 Ω //
 - d) 50 Ω, 90 Ω series 80 Ω, 70 Ω series



3.4

3.4 For each of the circuits shown in Fig. P3.4,
a) identify the resistors connected in parallel.
b) simplify the circuit by replacing the parallel-connected resistors with equivalent resistors.



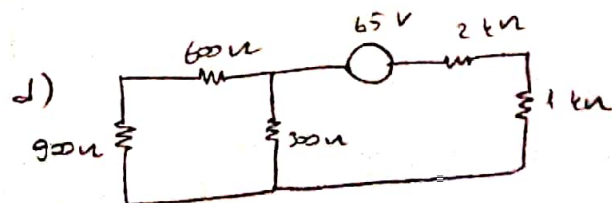
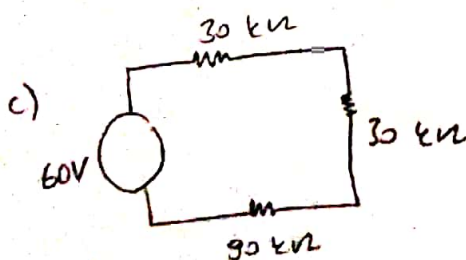
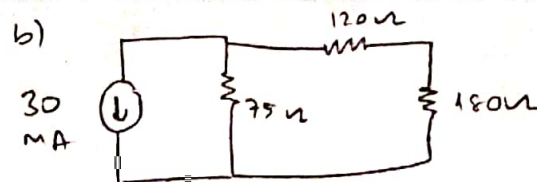
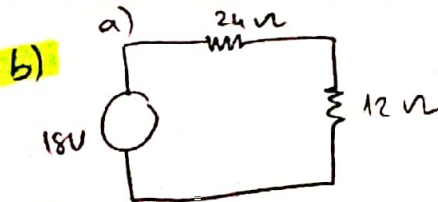
a) circuit

a) 36 Ω // 18 Ω

b) 200 Ω // 120 Ω
210 Ω // 280 Ω

c) 75 kΩ // 50 kΩ
100 // 150 // 60

d) 750 Ω // 500 Ω
1.5 kΩ // 3 kΩ



3.5

3.5 For each of the circuits shown in Fig. P3.3,

a) find the equivalent resistance seen by the source,

b) find the power developed by the source.

a) Circuit a $\rightarrow R_{eq} = [(7000 + 5000) \parallel 6000] + 8000 = 4000 + 8000 = 12 \text{ k}\Omega$

Circuit b $\rightarrow R_{eq} = [(800 + 1200) \parallel 500] + 300 + 200 = (400 + 300 + 200) = 900 \Omega$

Circuit c $\rightarrow R_{eq} = (35 + 15 + 25) \parallel (10 + 40) = 30 \Omega$

Circuit d $\rightarrow R_{eq} = \left[\left[(70 + 80) \parallel 100 \right] + (50 + 90) \right] \parallel 300 = (60 + 50 + 90) \parallel 300 = 120 \Omega$

b) $P = i^2 \cdot R = V \cdot i = \frac{V^2}{R}$

Circuit a $\rightarrow P = \frac{18^2}{12000} = 0.027 \text{ W}$

Circuit c $\rightarrow P = \frac{30^2}{30} = 270 \text{ W}$

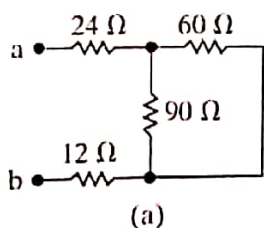
Circuit b $\rightarrow P = \frac{27^2}{900} = 0.81 \text{ W}$

Circuit d $\rightarrow P = (0.03)^2 \cdot 120 = 0.108 \text{ W}$

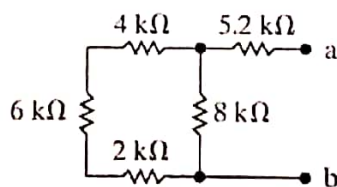
3.8

3.8 Find the equivalent resistance R_{ab} for each of the circuits in Fig. P3.8.

Figure P3.8



(a)



(b)

Circuit a

$$R_{ab} = 24 + (60 \parallel 90) + 12 = 24 + 36 + 12 = 72 \Omega$$

Circuit b

$$R_{ab} = [(4k + 6k + 2k) \parallel 8k] + 5.2k = (12k \parallel 8k) + 5.2k = 10 \text{ k}\Omega$$

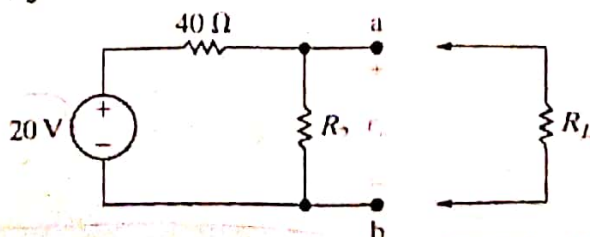
Circuit c

$$R_{ab} = 1200 \parallel 720 \parallel (320 + 480) = 288 \Omega$$

3.13

3.13 In the voltage-divider circuit shown in Fig. P3.13, the no-load value of v_o is 4 V. When the load resistance R_L is attached across the terminals a and b, v_o drops to 3 V. Find R_L .

Figure P3.13



$$V_o \rightarrow \begin{matrix} 4V \\ 3V \end{matrix}$$

$$\frac{20 \cdot R_2}{40 + R_{eq}} = 4V \rightarrow R_2 = 10 \Omega$$

$$\frac{20 \cdot R_{eq}}{40 + R_{eq}} = 3V \rightarrow R_{eq} = \frac{120}{17} \Omega$$

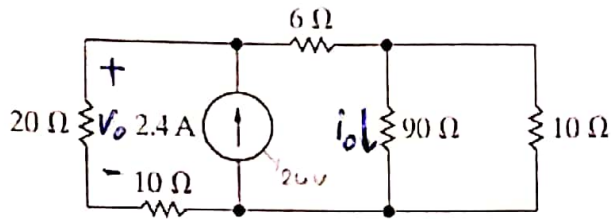
$$\frac{120}{17} = R_2 \parallel R_L$$

$$\frac{120}{17} = 10 \Omega \parallel R_L \rightarrow R_L = 24 \Omega$$

3.17**3.17** For the current divider circuit in Fig. P3.17 calculatePSICE
MULTISIM

- i_o and v_o .
- the power dissipated in the $6\ \Omega$ resistor.
- the power developed by the current source.

Figure P3.17



$$b) P_{6\Omega} = \frac{V^2}{R} = \frac{(24 - 14.4)^2}{6} = 15.3\text{ W}$$

$$c) P_{2.4A} = V \cdot i = (2.4) \cdot 24 = 57.6\text{ W}$$

$$a) \text{ First find } R_{eq} = (10 + 20) \parallel [6 + (90 \parallel 10)]$$

$$R_{eq} = 10\ \Omega$$

$$V = i \cdot R \Rightarrow V_{2.4A} = 2.4 \cdot 10 = 24\text{ V}$$

$$V_{20\Omega} = \frac{20}{20 + 10} \cdot 24 = 16\text{ V} = V_o$$

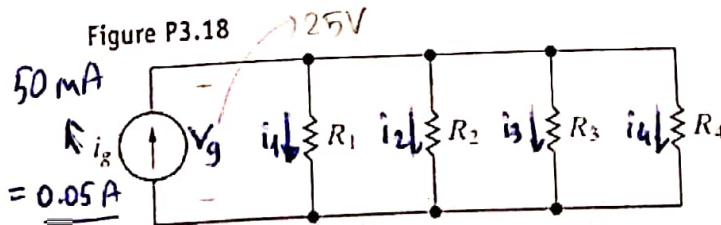
$$V_{90\Omega} = \frac{90 \cdot 10 / 100}{6 + 90 \cdot 10 / 100} \cdot 24 = \frac{9}{15} \cdot 24 = 14.4\text{ V} \rightarrow i_o = \frac{14.4}{90\ \Omega} = 0.16\text{ A}$$

3.18**3.18** Specify the resistors in the current divider circuit in Fig. P3.18 to meet the following design criteria:DESIGN
PROBLEM

$$i_g = 50\text{ mA}; v_g = 25\text{ V}; i_1 = 0.6i_2;$$

$$i_3 = 2i_2; \text{ and } i_4 = 4i_1.$$

Figure P3.18



$$i_g = i_1 + i_2 + i_3 + i_4$$

$$0.05 = 0.6i_2 + i_2 + 2i_2 + 4i_1$$

$$0.05 = 0.6i_2 + 3i_2 + 4 \cdot 0.6i_2$$

$$0.05 = 6i_2 \rightarrow i_2 = 0.0083\text{ A}$$

$$i_2 = 8.3\text{ mA}$$

$$i_1 = 0.6i_2 = 0.005\text{ A}$$

$$i_3 = 2i_2 = 0.0166\text{ A}$$

$$i_4 = 4i_1 = 0.02\text{ A}$$

25 V affects all

$$R_1 = \frac{V}{i} = \frac{25}{i_1} = \frac{25}{0.005} = 5000\ \Omega$$

$$R_2 = \frac{25}{i_2} = \frac{25}{0.0083} = 3000\ \Omega$$

$$R_3 = \frac{25}{i_3} = \frac{25}{0.0166} = 1500\ \Omega$$

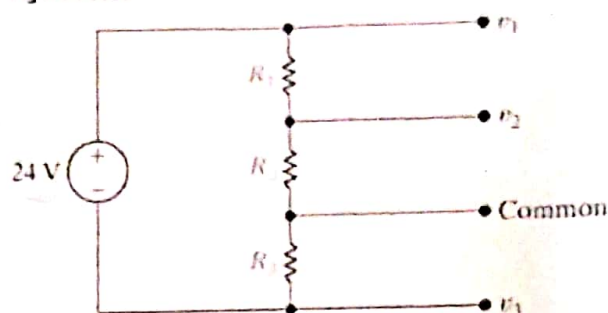
$$R_4 = \frac{25}{i_4} = \frac{25}{0.02} = 1250\ \Omega$$

3.19

3.19
DESIGN
PROBLEM

There is often a need to produce more than one voltage using a voltage divider. For example, the memory components of many personal computers require voltages of -12 V , 5 V , and $+12\text{ V}$, all with respect to a common reference terminal. Select the values of R_1 , R_2 , and R_3 in the circuit in Fig. P3.19 to meet the following design requirements:

Figure P3.19



- The total power supplied to the divider circuit by the 24 V source is 80 W when the divider is unloaded.
- The three voltages, all measured with respect to the common reference terminal, are $v_1 = 12\text{ V}$, $v_2 = 5\text{ V}$, and $v_3 = -12\text{ V}$.

$$\text{Power} = \frac{V^2}{R} = 80 = \frac{24^2}{R_1 + R_2 + R_3} \rightarrow R_1 + R_2 + R_3 = 7.2\ \Omega$$

$$\text{Voltage division} \rightarrow 24 \cdot \frac{(R_1 + R_2)}{R_1 + R_2 + R_3} = 12 \rightarrow R_1 + R_2 = 3.6\ \Omega$$

$$\text{Voltage division} \rightarrow 24 \cdot \frac{R_2}{R_1 + R_2 + R_3} = 5 \rightarrow R_2 = 3.6 / 24 = 1.5\ \Omega$$

$$R_1 = 3.6 - 1.5 = 2.1\ \Omega$$

3.22

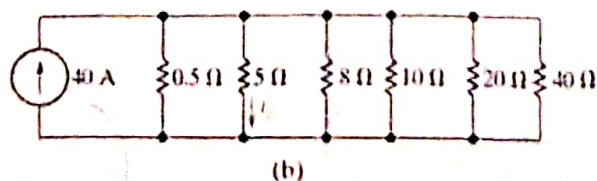
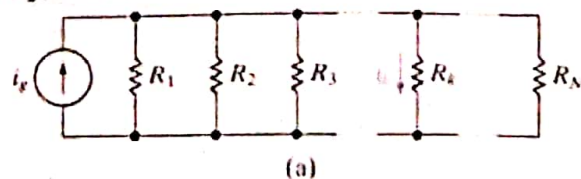
3.22
PROBLEM
ANALYSIS

- Show that the current in the k th branch of the circuit in Fig. P3.22(a) is equal to the source current i_g times the conductance of the k th branch divided by the sum of the conductances, that is,

$$i_k = \frac{i_g G_k}{G_1 + G_2 + G_3 + \dots + G_k + \dots + G_N}$$

- Use the result derived in (a) to calculate the current in the $5\ \Omega$ resistor in the circuit in Fig. P3.22(b).

Figure P3.22



$$b) i_{5\ \Omega} = \frac{40 \cdot (\frac{1}{5})}{\frac{1}{0.5} + \frac{1}{5} + \frac{1}{8} + \frac{1}{10} + \frac{1}{20} + \frac{1}{40}} = 3.2\text{ A}$$

$$a) V = i \cdot R \quad G = \frac{1}{R} \rightarrow R = \frac{1}{G}$$

$$i = \frac{V}{R} = \frac{V}{\frac{1}{G}} = V \cdot G$$

Assume voltage be V

$$i_g = V \cdot G_1 + V \cdot G_2 + \dots + V \cdot G_N$$

$$i_g = V \cdot (G_1 + G_2 + G_3 + \dots + G_N)$$

Current on the k th branch is

$$i_k = V \cdot G_k \rightarrow V = \frac{i_k}{G_k}$$

$$i_g = \frac{i_k}{G_k} \cdot (G_1 + G_2 + \dots + G_N)$$

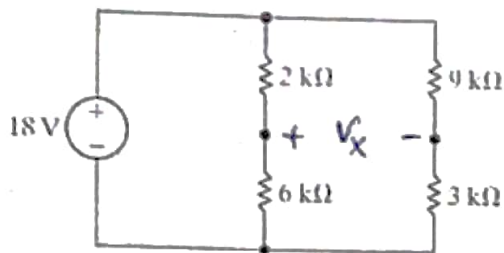
$$i_k = \frac{i_g \cdot G_k}{(G_1 + G_2 + \dots + G_N)}$$

3.28

3.28 a) Find the voltage v_x in the circuit in Fig. P3.28 using voltage and/or current division.

b) Replace the 18 V source with a general voltage source equal to V_s . Assume V_s is positive at the upper terminal. Find v_x as a function of V_s .

Figure P3.28



$$a) V_{2k} = \frac{2}{2+6} \cdot 18 = 4.5 \text{ V}$$

$$V_{9k} = \frac{9}{9+3} \cdot 18 = 13.5 \text{ V}$$

$$V_x = 13.5 - 4.5 = \underline{9 \text{ V}}$$

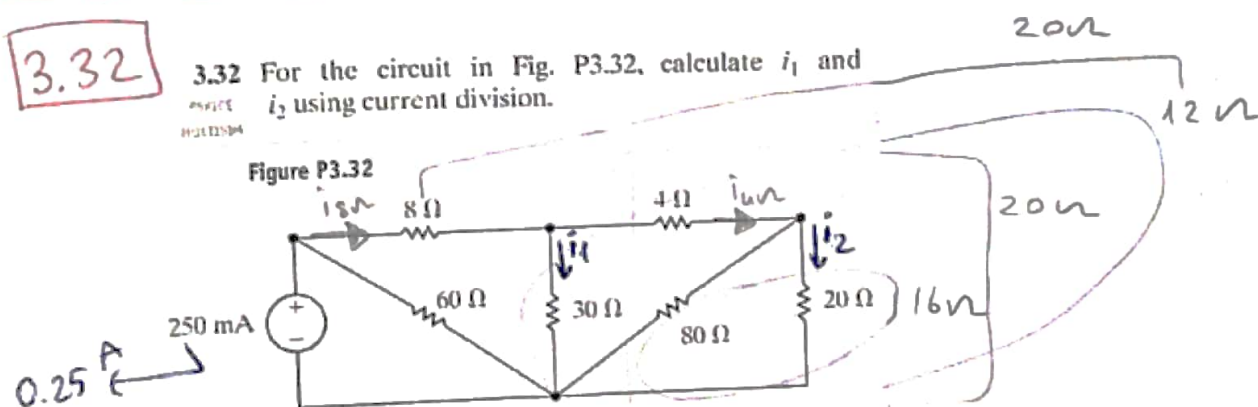
$$b) V_{2k} = \frac{2}{8} V_s \quad V_{9k} = \frac{9}{12} V_s$$

$$V_x = \frac{9}{12} V_s - \frac{2}{8} V_s = \frac{2V_s}{4} = \underline{\underline{\frac{V_s}{2}}}$$

3.32

3.32 For the circuit in Fig. P3.32, calculate i_1 and i_2 using current division.

Figure P3.32



$$i_{8\Omega} = \frac{60 \parallel 20}{20} \cdot (0.25) = 0.1875 \text{ A}$$

$$i_1 = \frac{30 \parallel 20}{30} \cdot i_{8\Omega} = 0.075 \text{ A}$$

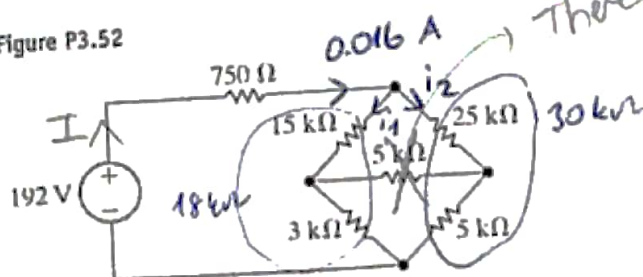
$$i_{4\Omega} = \frac{30 \parallel 20}{20} \cdot i_{8\Omega} = 0.1125 \text{ A}$$

$$i_2 = \frac{80 \parallel 20}{20} \cdot i_{4\Omega} = 0.09 \text{ A}$$

3.52

3.52 Find the power dissipated in the 3 kΩ resistor in the circuit in Fig. P3.52.

Figure P3.52



$$V = I \cdot R$$

$$192 = I \cdot 12000$$

$$I = 0.016 \text{ A}$$

$$i_1 = \frac{11250 \Omega}{18000 \Omega} \cdot (0.016) \rightarrow i_1 = 0.01 \text{ A}$$

$$P_{3k\Omega} = i^2 \cdot R = (0.01)^2 \cdot 3000 = \underline{0.3 \text{ W}}$$

There is no current through 15.5 = 25.3 ✓

$$R_{eq} = [(25k + 5k) \parallel (3k + 15k)] + 750$$

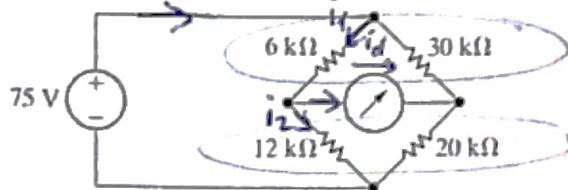
$$R_{eq} = 11.25k\Omega + 750\Omega$$

$$= 12k\Omega = 12000\Omega$$

3.53

3.53 Find the detector current i_d in the unbalanced bridge in Fig. P3.53 if the voltage drop across the detector is negligible.

Figure P3.53



$R_{eq} = 12500\Omega$

$I = \frac{75}{12500} \rightarrow I = 0.006 A$

$i_1 = i_d + i_2$

$i_1 = \frac{V}{R} = \frac{30V}{6000} = 0.005 A$

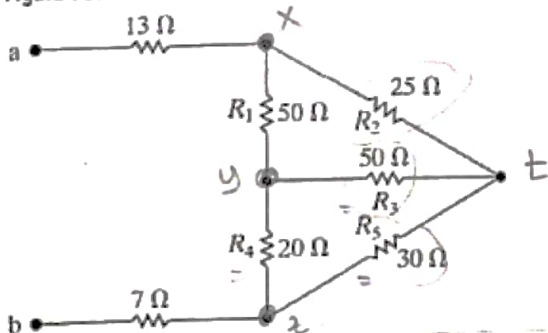
$i_d = i_1 - i_2$
 $= 0.005 - 0.00375$
 $= 0.00125 A$

$i_2 = \frac{V}{R} = \frac{45V}{12000} = 0.00375 A$

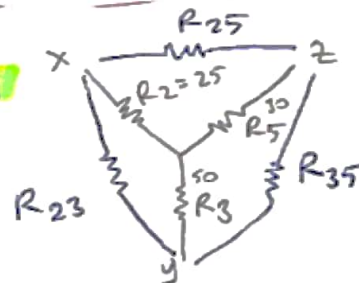
3.58

- 3.58 a) Find the equivalent resistance R_{ab} in the circuit in Fig. P3.58 by using a Y-to-Δ transformation involving resistors R_2, R_3 , and R_5 .
 b) Repeat (a) using a Δ-to-Y transformation involving resistors R_3, R_4 , and R_5 .
 c) Give two additional Δ-to-Y or Y-to-Δ transformations that could be used to find R_{ab} .

Figure P3.58



a)

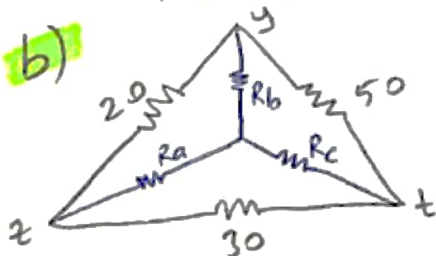


$R_{23} = \frac{25 \cdot 50 + 25 \cdot 30 + 50 \cdot 30}{30} = 116.6\Omega$

$R_{25} = \frac{25 \cdot 50 + 25 \cdot 30 + 50 \cdot 30}{50} = 70\Omega$

$R_{35} = \frac{25 \cdot 50 + 25 \cdot 30 + 50 \cdot 30}{25} = 140\Omega$

b)



$R_a = \frac{20 \cdot 30}{20 + 30 + 50} = 6\Omega$

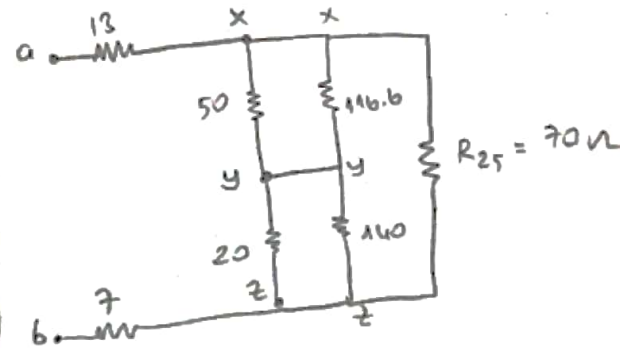
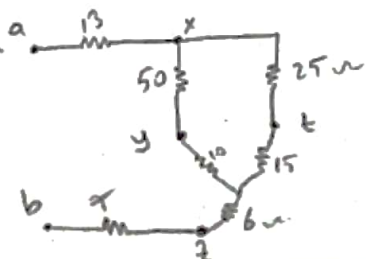
$R_b = \frac{20 \cdot 50}{20 + 30 + 50} = 10\Omega$

$R_c = \frac{30 \cdot 50}{20 + 30 + 50} = 15\Omega$

$R_{eq} = (50 + 10) \parallel (25 + 5) + 6 + 13 + 7$
 $= 50\Omega$

c)

Δ-Y $\rightarrow R_1, R_3, R_2$
 Y-Δ $\rightarrow R_1, R_4, R_3$



$R_{eq} = 70 \parallel [(50 \parallel 116.6) + (20 \parallel 140)] + 13 + 7$

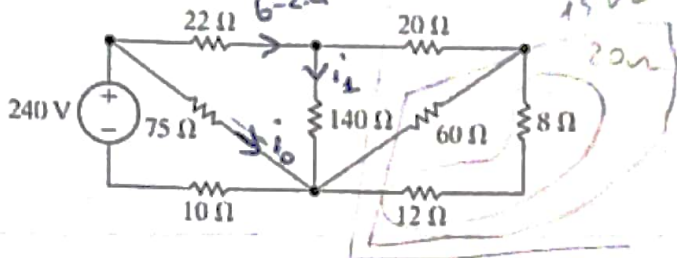
$R_{eq} = 50\Omega$

3.62

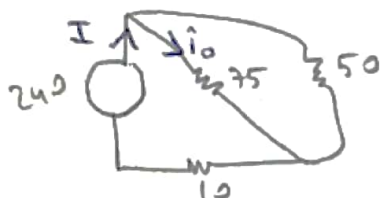
3.62
PSICE
MULTISIM

Find i_o and the power dissipated in the $140\ \Omega$ resistor in the circuit in Fig. P3.62.

Figure P3.62



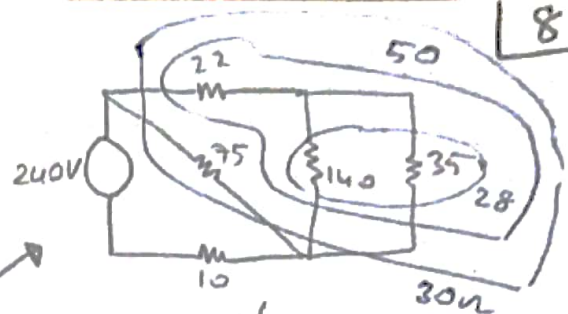
$$V = I \cdot R \rightarrow 240 = I \cdot 40 \rightarrow I = 6A$$



$$i_o = \frac{50 \cdot 6}{(50+75)} = 2.4A$$

$$i_1 = \frac{(3.6) \cdot 35}{(35+140)} = 0.72A$$

$$P_{140\Omega} = i^2 \cdot R = (0.72)^2 \cdot 140 = 72.57W$$

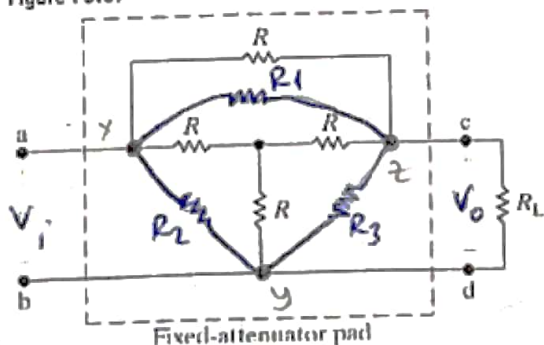


3.67

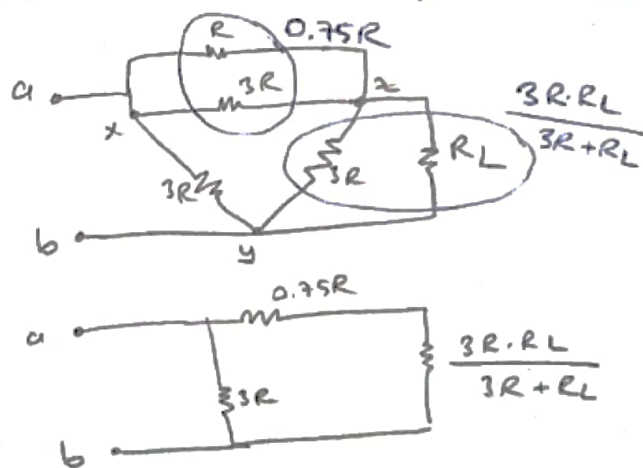
3.67
DESIGN
PROBLEM

- a) The fixed-attenuator pad shown in Fig. P3.67 is called a *bridged tee*. Use a Y-to- Δ transformation to show that $R_{ab} = R_L$ if $R = R_L$.
- b) Show that when $R = R_L$, the voltage ratio v_o/v_i equals 0.50.

Figure P3.67



$$a) R_1 = R_2 = R_3 = \frac{R \cdot R + R \cdot R + R \cdot R}{R} = 3R$$

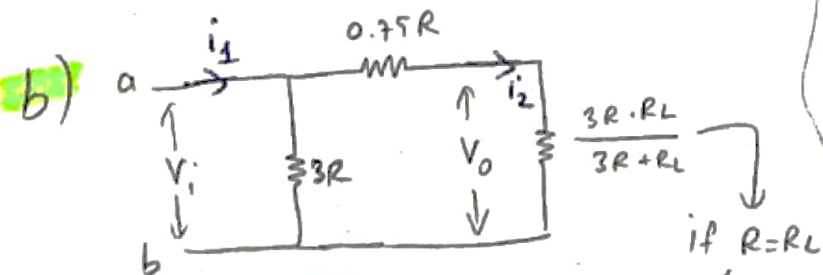


$$R_{ab} = 3R \parallel \left(0.75R + \frac{3R \cdot R_L}{3R + R_L} \right)$$

$$R_{ab} = \frac{3R \cdot \left(\frac{2.25R^2 + 3.75R \cdot R_L}{3R + R_L} \right)}{3R + \left(\frac{2.25R^2 + 3.75R \cdot R_L}{3R + R_L} \right)}$$

$$R_{ab} = \frac{3R_L \cdot 8R_L}{24R_L} = R_L$$

$$\text{if } R_L = R \rightarrow R_{ab} = R_L \checkmark$$



$$i_2 = \frac{i_1 \cdot 3R}{4.5R} = \frac{i_1}{1.5}$$

$$V_o = i_2 \cdot 0.75R = \frac{i_1}{1.5} \cdot 0.75R$$

$$\frac{V_o}{V_i} = \frac{i_1 \cdot 0.75R}{i_1 \cdot R} = \frac{1}{2}$$

$$= 0.5 \checkmark$$

3.73

3.73 A resistive touch screen has 5 V applied to the grid in the x-direction and in the y-direction. The screen has 480 pixels in the x-direction and 800 pixels in the y-direction.

$$V_x = 1 \quad V_y = 3.75$$

the y-direction. When the screen is touched, the voltage in the x-grid is 1 V and the voltage in the y-grid is 3.75 V.

- Calculate the values of α and β .
- Calculate the x- and y-coordinates of the pixel at the point where the screen was touched.

a)

3.72. question formula $\Rightarrow V_x = \alpha V_s$

$$V_y = \beta V_s$$

$$\alpha = \frac{1}{5} = 0.2$$

$$\beta = \frac{3.75}{5} = 0.75$$

b)

3.72. question formula $\Rightarrow x = P_x(1-\alpha)$

$$y = P_y(1-\beta)$$

$$x = 480 \cdot (1-0.2) = 384$$

$$y = 800 \cdot (1-0.75) = 200$$

3.74

3.74 A resistive touch screen has 640 pixels in the x-direction and 1024 pixels in the y-direction. The resistive grid has 8 V applied in both the x- and y-directions. The pixel coordinates at the touch point are (480, 192). Calculate the voltages V_x and V_y .

$$V_s = 8$$

$$x = 480$$

$$P_x = 640$$

$$P_y = 1024$$

$$y = 192$$

3.72. question formula \Rightarrow

$$x = P_x(1-\alpha)$$

$$y = P_y(1-\beta)$$

$$V_x = \alpha V_s$$

$$V_y = \beta V_s$$

$$480 = 640 \cdot (1-\alpha)$$

$$\alpha = 0.25$$

$$192 = 1024(1-\beta)$$

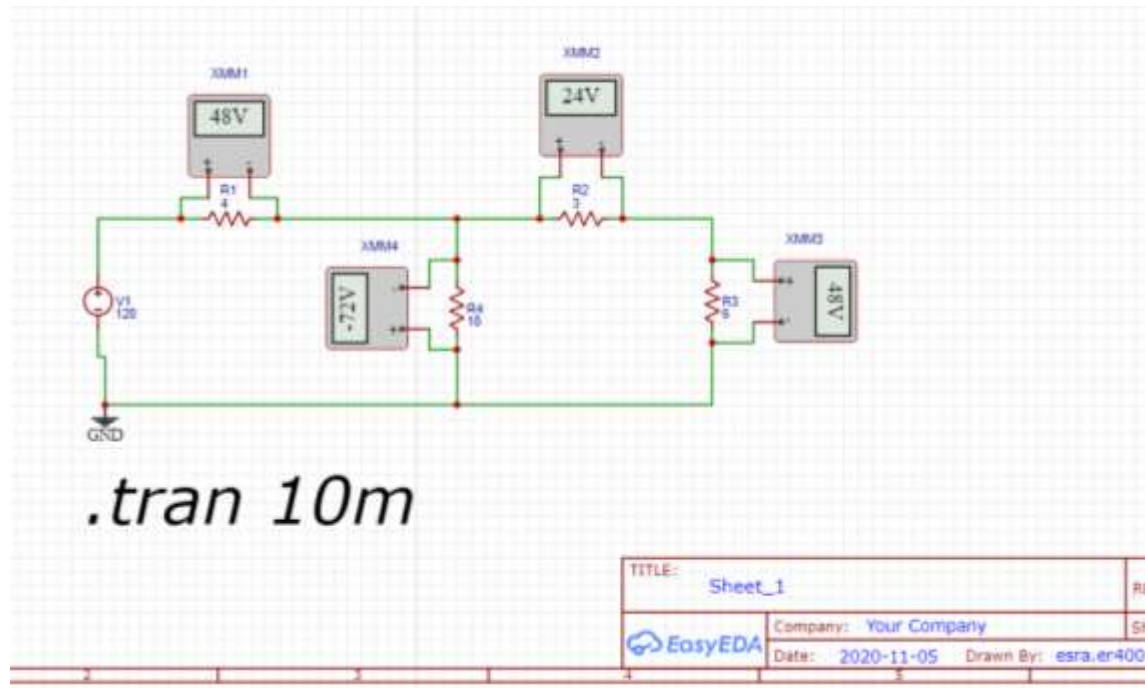
$$\beta = 0.8125$$

$$V_x = \alpha V_s = 0.25 \cdot 8 = 2 \text{ V}$$

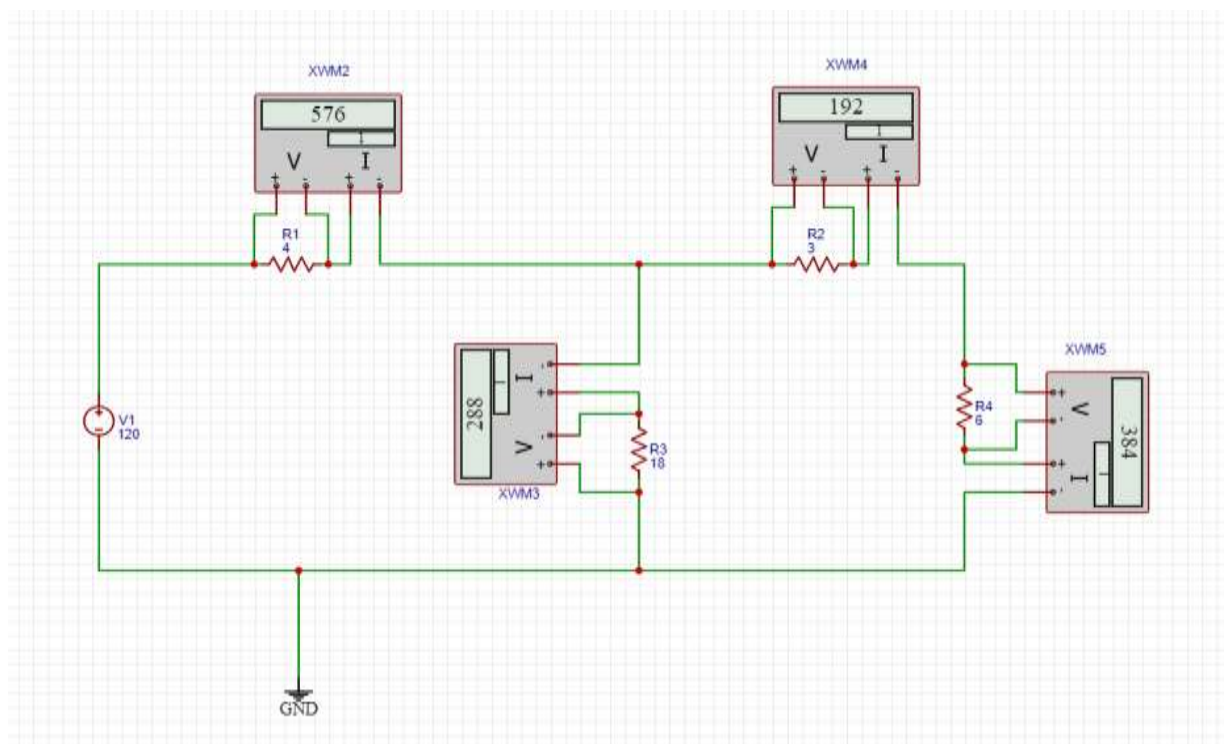
$$V_y = \beta V_s = (0.8125) \cdot 8 = 6.5 \text{ V}$$

EasyEDA simulations

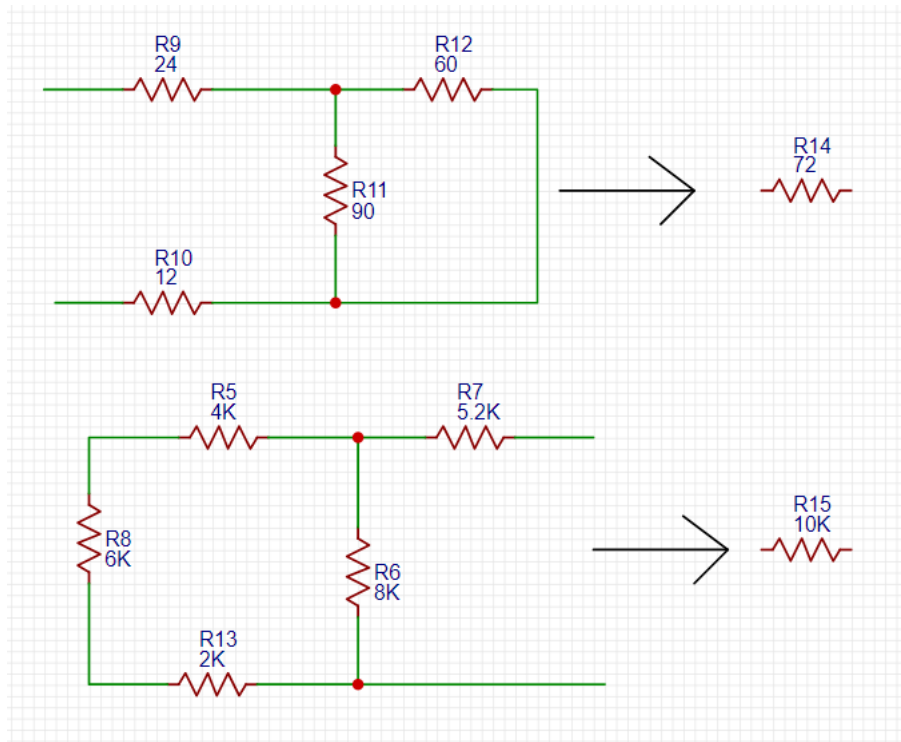
• 3.1



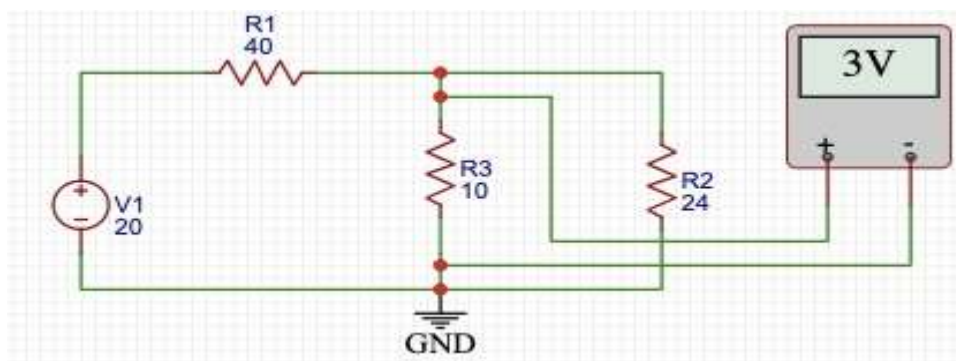
• 3.2



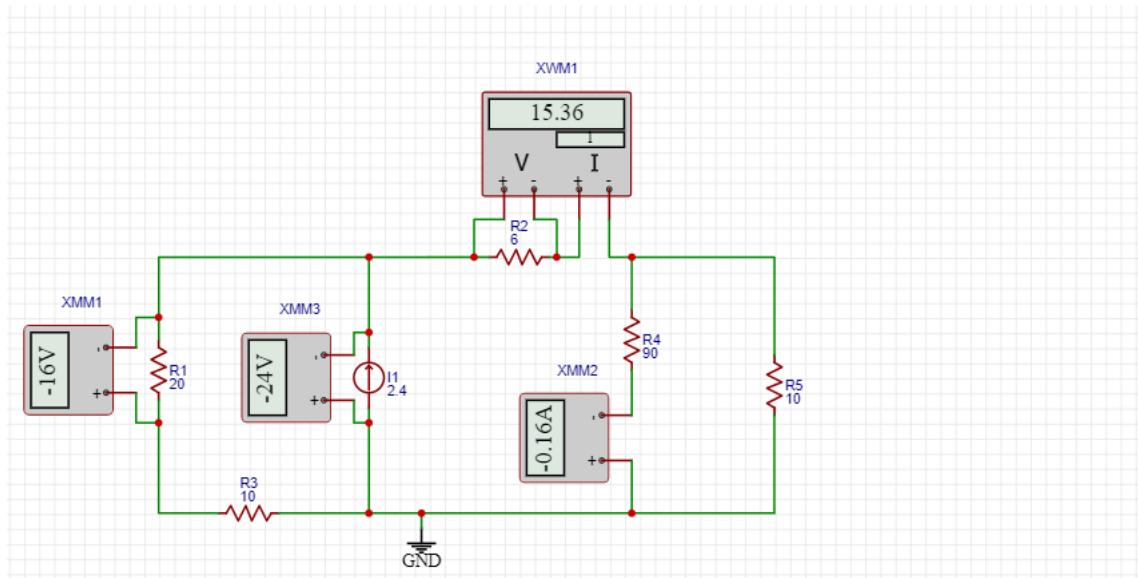
- **3.8**



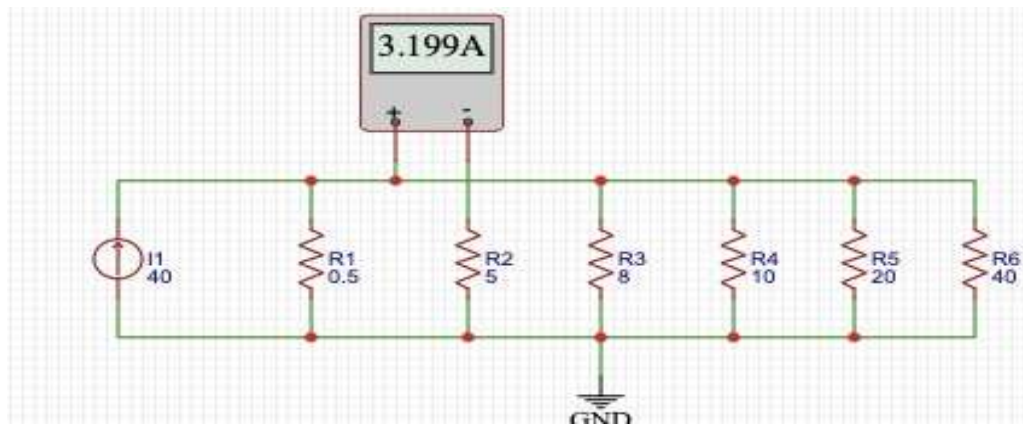
- **3.13**



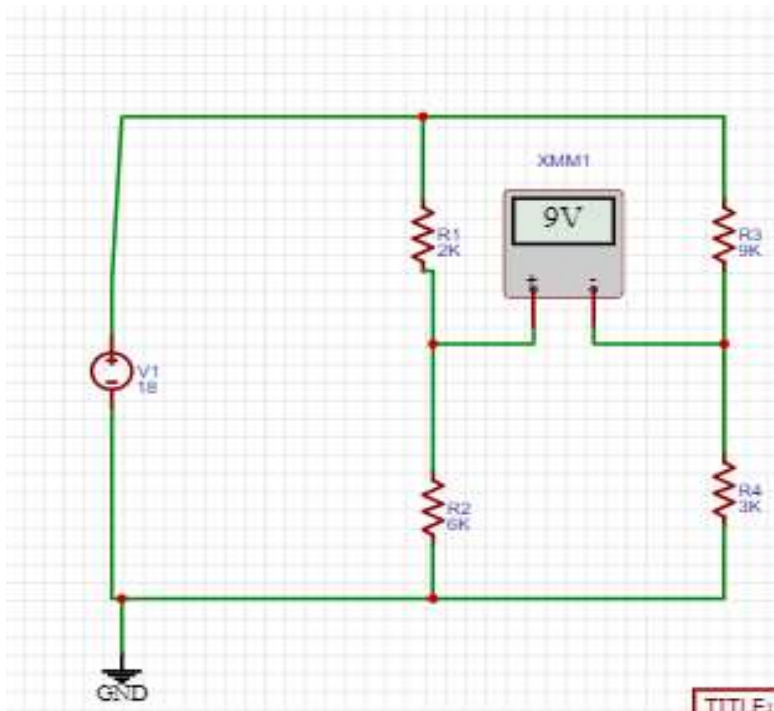
- **3.17**



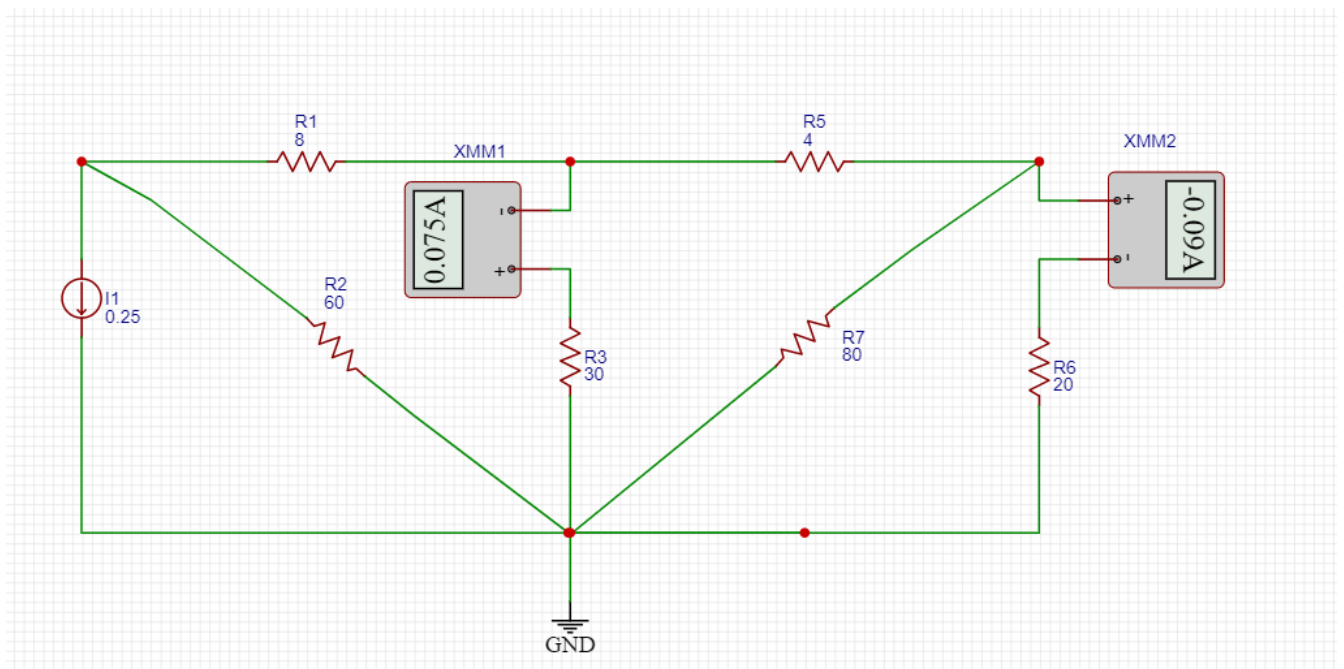
- **3.22**



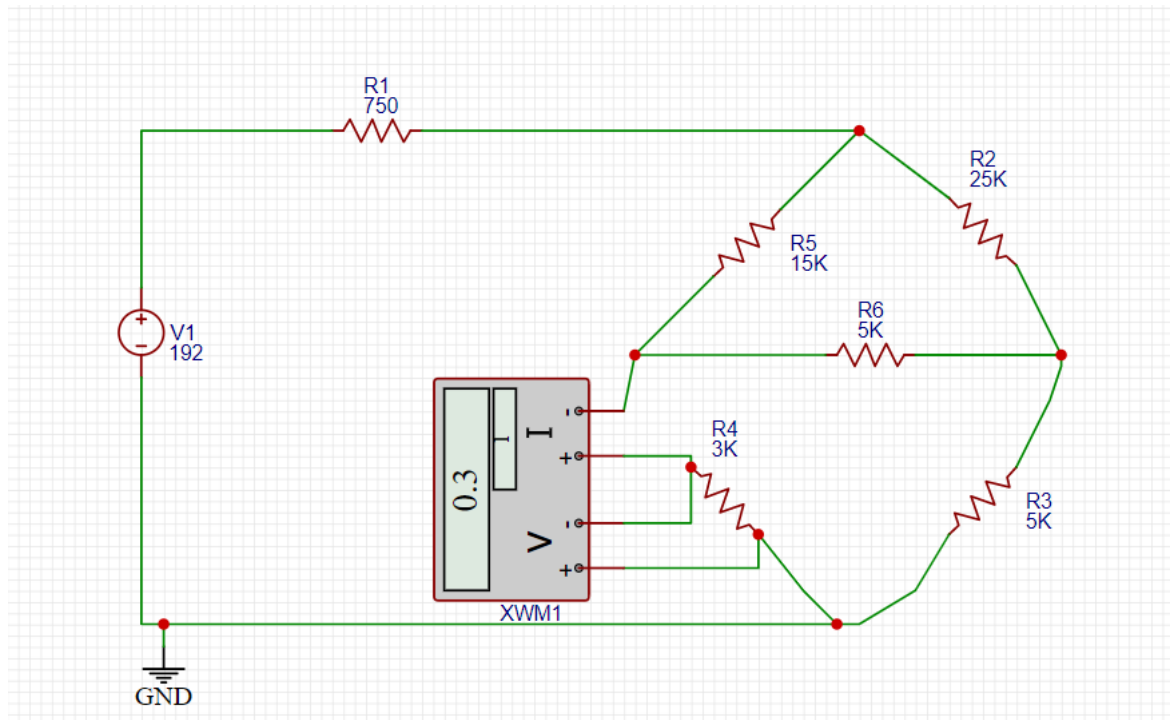
• 3.28



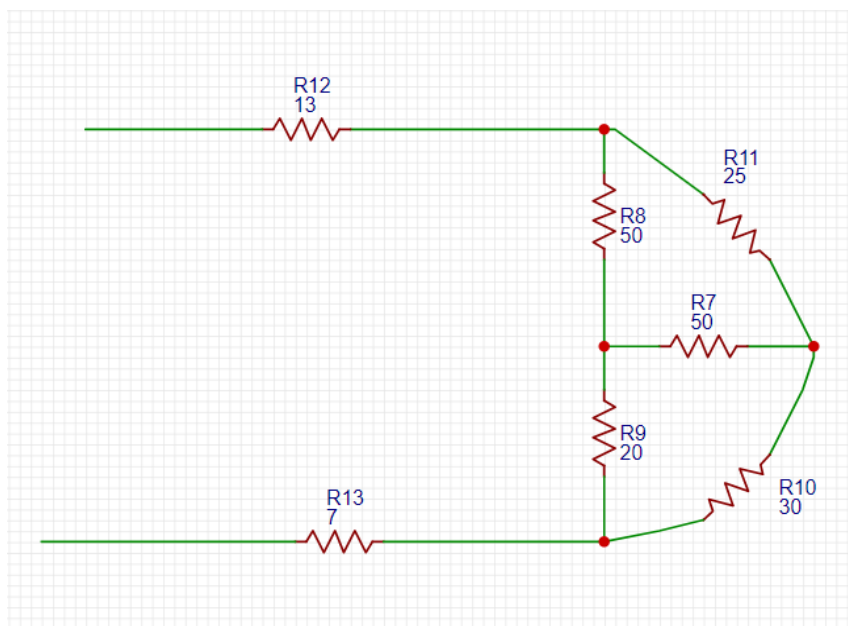
• 3.32



• 3.52



• 3.58



- 3.62

