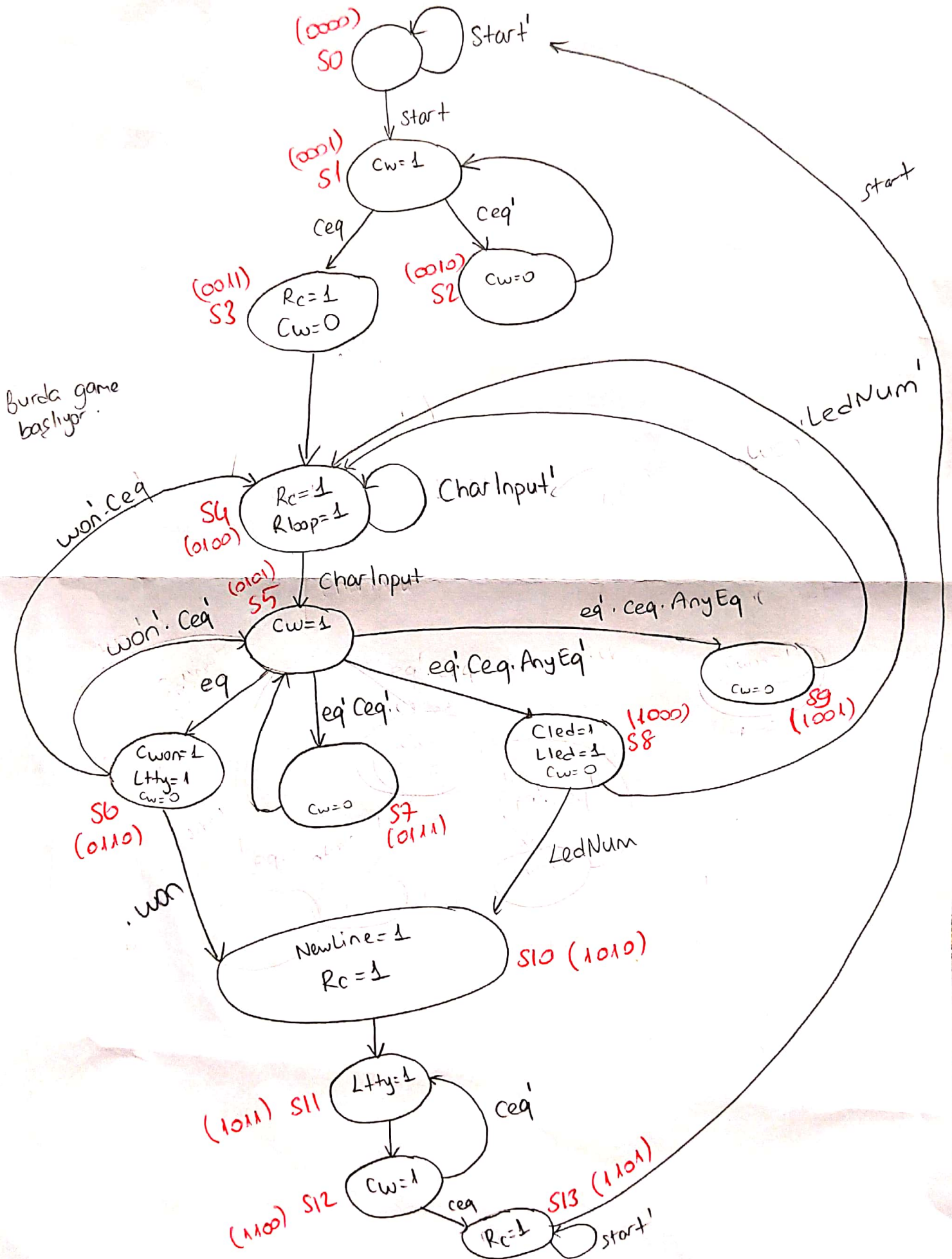


(FSM) | Hagnon.c

Inputs: Start



(PS)		(Inputs)							(NS)			
s3	s2s1s0	Start	Ceq	eq	Lwon	LedNm	AnyEq	charInput	n3	n2n1n0		
S0	0000	0 1	- -	- -	- -	- -	- -	- -	0000 0001			
S1	0001	- -	0 1	- -	- -	- -	- -	- -	0010 0011			
S2	0010	-	-	-	-	-	-	-	0001			
S3	0011	-	-	-	-	-	-	-	0100			
S4	0100	- -	- -	- -	- -	- -	- -	0 1	0100 0101			
S5	* 0101	- - - -	- 0 1 1	1 0 0 0	- - - -	- - - -	- - 0 1	- - - -	0110 0111 1000 1001			
S6	0110	- - -	1 0 -	- - -	0 0 1	- - -	- - -	- - -	0100 0101 1010			
S7	0111	-	-	-	-	-	-	-	0101			
S8	1000	- -	- -	- -	- -	0 1	- -	- -	0100 1010			
S9	1001	-	-	-	-	-	-	-	0100			
S10	1010	-	-	-	-	-	-	-	1011			
S11	1011	-	-	-	-	-	-	-	1100			
S12	1100	- -	0 1	- -	- -	- -	- -	- -	1011 1101			
S13	1101	0 1	- -	- -	- -	- -	- -	- -	1101 0000			

Truth Table for Outputs

(PS)	Cw	Rc	Rloop	Cwon	Ltty	Cled	Lled	NewLine
S0	0	0	0	0	0	0	0	0
S1	1	0	0	0	0	0	0	0
S2	0	0	0	0	0	0	0	0
S3	0	1	0	0	0	0	0	0
S4	0	1	1	0	0	0	0	0
S5	1	0	0	0	0	0	0	0
S6	0	0	0	1	1	0	0	0
S7	0	0	0	0	0	0	0	0
S8	0	0	0	0	0	1	1	0
S9	0	0	0	0	0	0	0	0
S10	0	1	0	0	0	0	0	1
S11	0	0	0	0	1	0	0	0
S12	1	0	0	0	0	0	0	0
S13	0	1	0	0	0	0	0	0

$$Cw = S1 + S5 + S12$$

$$Rc = S3 + S4 + S10 + S13$$

$$Rloop = S4$$

$$Cwon = S6$$

$$Ltty = S6 + S11$$

$$Cled = S8$$

$$Lled = S8$$

$$NewLine = S10$$