1.1 Memory Map of Development Board

4.0 GB		
		OxFFFF FFFF
3.75 GB	RESERVED ADDRESS SPACE	0xF000 0000
		OxEFFF FFFF
	PERIPHERALS	
	TEMITIEMAES	
3.5 GB		0xE000 0000
		0xDFFF FFFF
	RESERVED ADDRESS SPACE	
	NESERVED NODRESS STACE	
		0x4000 8000
		0x4000 7FFF
1.0 GB	32 kB ON-CHIP STATIC RAM	0x4000 0000
		0x3FFF FFFF
	RESERVED ADDRESS SPACE	
		0x0008 0000
		0x0007 FFFF
	512 kB ON-CHIP NON-VOLATILE MEMORY	
	312 KD OIN-CHIP INOIN-VOLATILE IVIEIVIORY	
		0x0000 0000
0.0 GB		

1.2 Detail View of Peripheral Memory Map

3.75 GB

ŝΒ		
		OxEFFF FFFF
	NOT USED	
		0xE020 0000
	System Control Block	
		0xE01F C000 0xE01F 8000
		OXEOTE 8000
	NOT USED	
		0xE001 4000
	Interrupt Controller	
		0xE001 0000
	LCD	
		0xE000 C000
	LEDO-8	
		0xE000 8000
	UART1	05000 4000
-		0xE000 4000
	UART0	0xE000 0000
:R		0XL000 0000

3.5 GB

1.3 Memory-Mapped Hardware Registers

1.3.1 UARTO

The UARTs are 64-byte versions of the *standard* National Semiconductor UARTs. The model is 16750. For an in-depth explanation of the various registers and how to configure them, check out <u>this article</u>.

U0RBR	UARTO Receiver Buffer	0xE000	0000
U0THR	UARTO Transmitter Holding Buffer	0xE000	0000
U0DLL	UARTO Devisor Latch Low Byte	0xE000	0000
U0DLH	UARTO Devisor Latch High Byte	0xE000	0004
U0IER	UARTO Interrupt Enable Register	0xE000	0004
U0IIR	UARTO Interrupt Identification Register	0xE000	8000
U0FCR	UARTO FIFO Control Register	0xE000	0008
U0LCR	UARTO Line Control Register	0xE000	000C
UOLSR	UARTO Line Status Register	0xE000	0014
UOSCR	UARTO Scratch Register	0xE000	001C

1.3.2 UART1

The UARTs are 64-byte versions of the *standard* National Semiconductor UARTs. The model is 16750. For an in-depth explanation of the various registers and how to configure them, check out this article.

U1RBR	UART1 Receiver Buffer	0xE000	4000
U1THR	UART1 Transmitter Holding Buffer	0xE000	4000
U1DLL	UART1 Devisor Latch Low Byte	0xE000	4000
U1DLH	UART1 Devisor Latch High Byte	0xE000	4004
U1IER	UART1 Interrupt Enable Register	0xE000	4004
U1IIR	UART1 Interrupt Identification Register	0xE000	4008
U1FCR	UART1 FIFO Control Register	0xE000	4008
U1LCR	UART1 Line Control Register	0xE000	400C
U1LSR	UART1 Line Status Register	0xE000	4014
U1SCR	UART1 Scratch Register	0xE000	401C