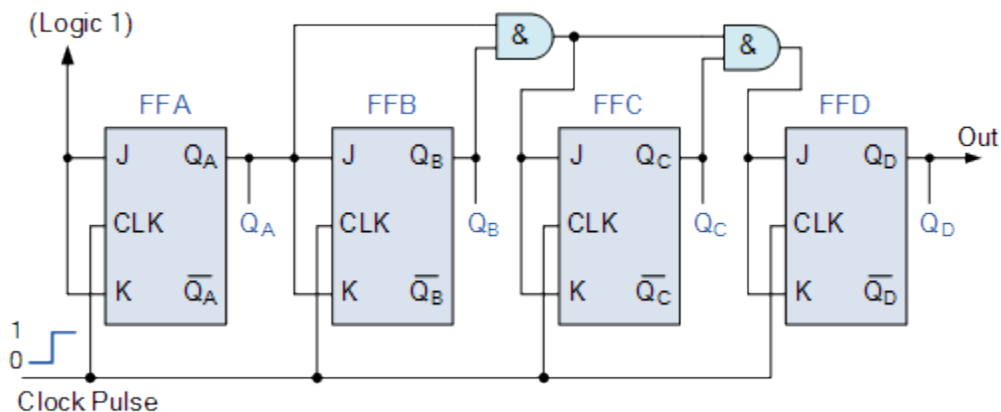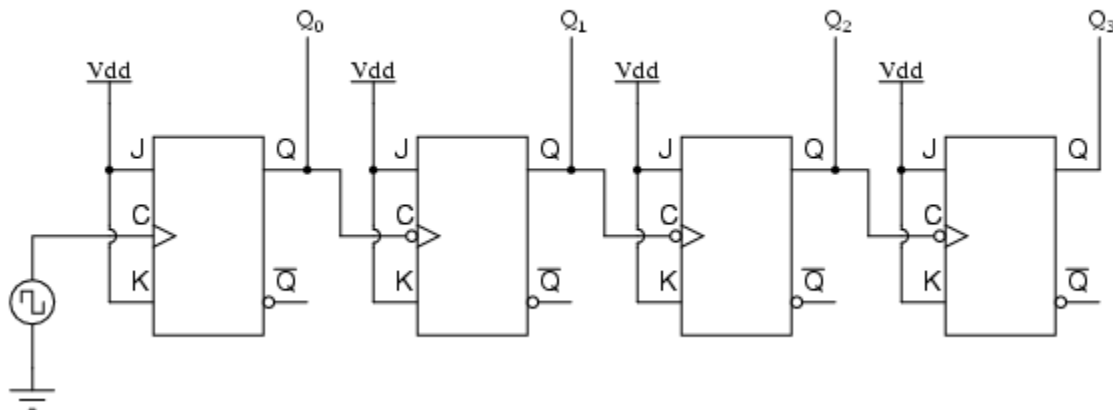# Assignment 5 and 6

Deadline: Thursday 16/5/2017 12 pm

Question 1 and 2 is Assignment-5 while Question-3 is Assignment 6

1. Draw the waveform of Following 2 Designs (.5 + .5 = 1 Weightage)
   Note: V_{dd} in 1st figure means 5 V (high) and "Logic 1" in 2nd figure means 5 V (HIGH)
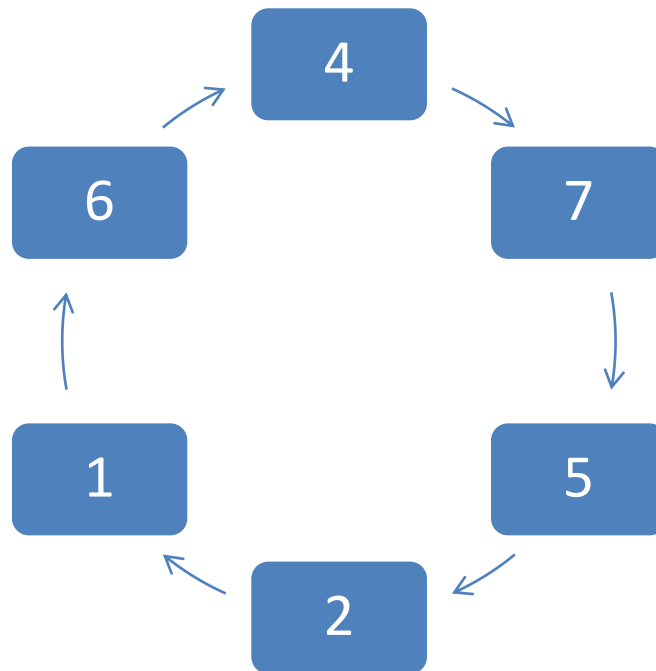
A four-bit "up" counter

2. Design synchronized counter for Following Sequence. (.5 + .5 = 1 Weightage)
    a. Using J-K Flip Flops (.5 weightage)
    b. Using D Flip Flops (.5 weightage)

```
        4
    6       7

    1       5
        2
```

3. Design UP/DOWN synchronized counter for Sequence given in Q2. (2 Weightage)