

SI 941 C

A

A

B

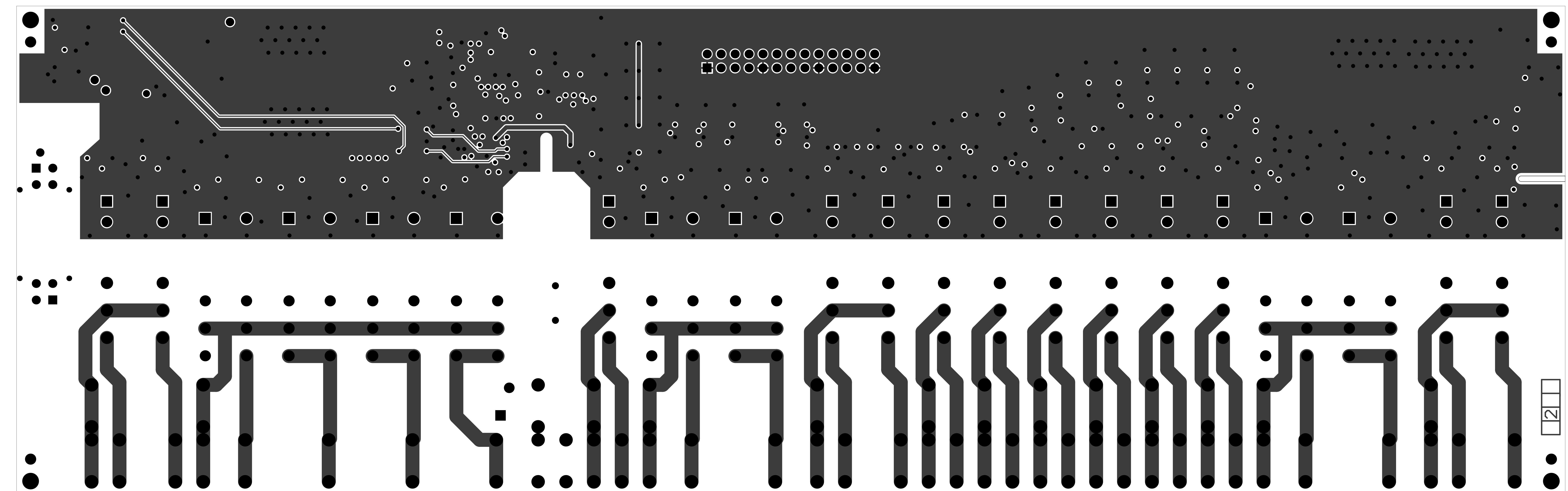
B

C

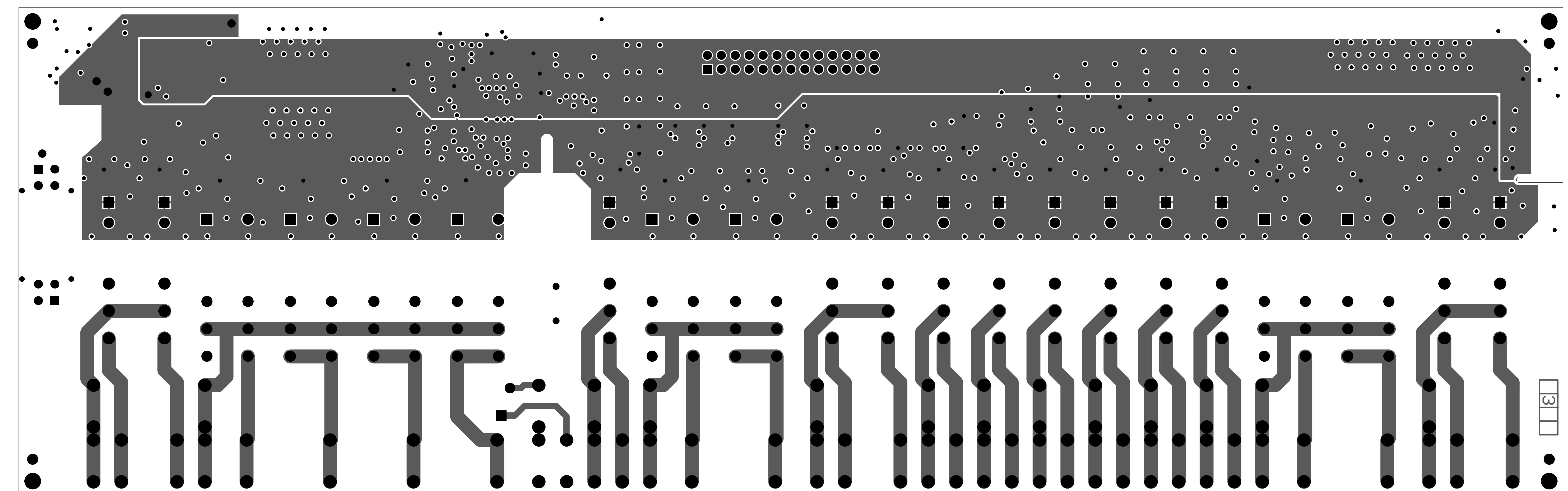
C

D

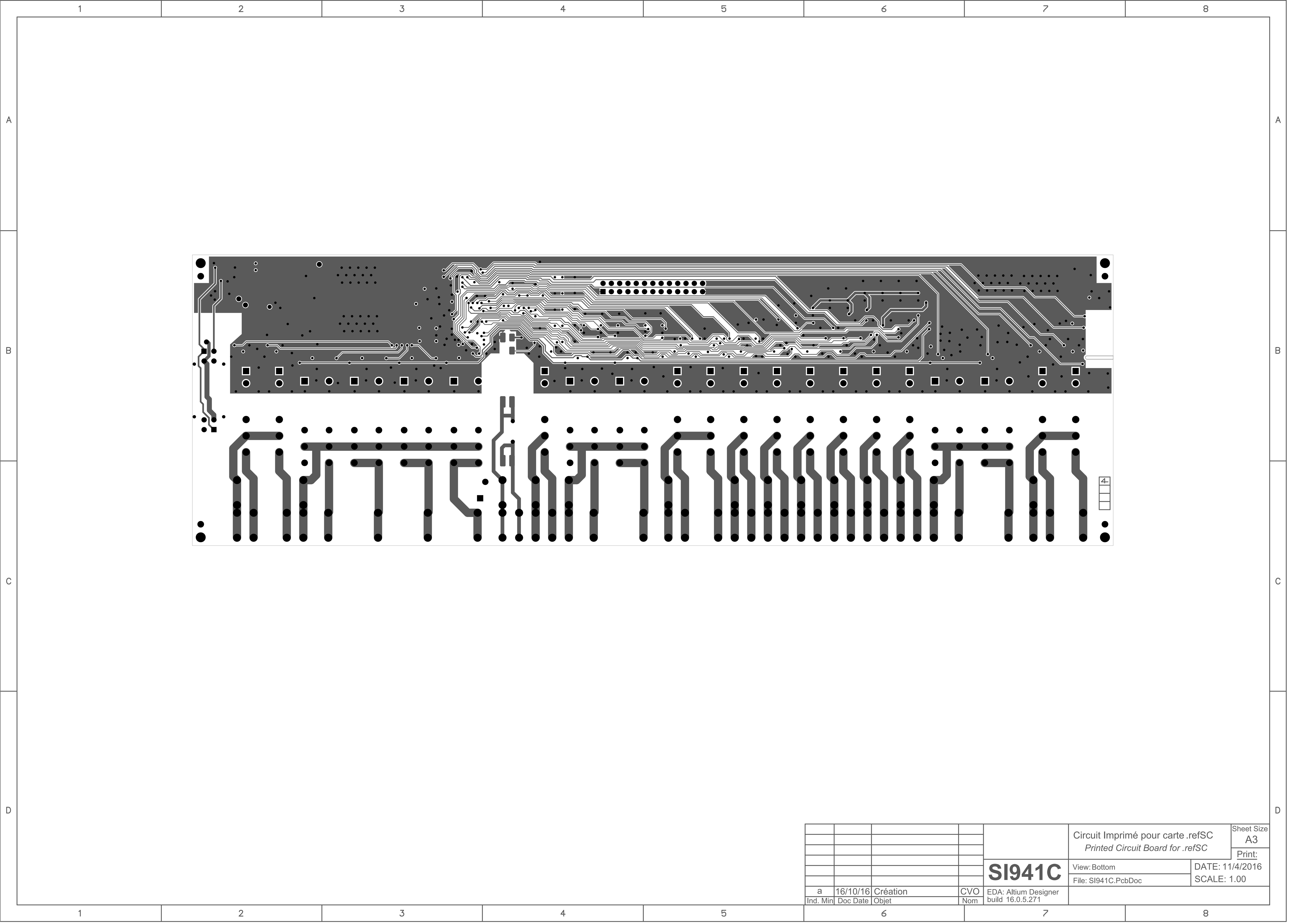
D



								Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC	Sheet Size A3
								SI941C	Print:
a	16/10/16	Création	CVO	EDA: Altium Designer build 16.0.5.271	View: Mid-Layer1	DATE: 11/4/2016			
Ind. Min	Doc Date	Objet	Nom	File: SI941C.PcbDoc	SCALE: 1.00				



Circuit Imprimé pour carte .refSC <i>Printed Circuit Board for .refSC</i>	Sheet Size A3
	Print:
View: Mid-Layer2	DATE: 11/4/2016
File: SI941C.PcbDoc	SCALE: 1.00



A

A

B

B

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6

D

D

A

A

B

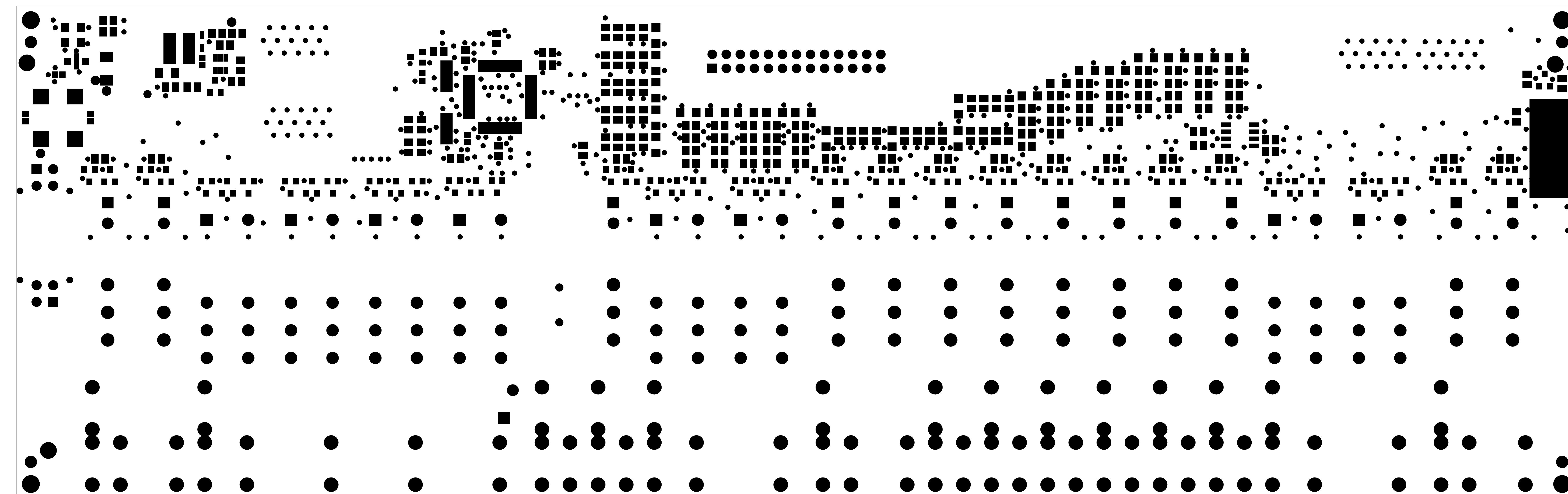
B

6

c

D

D



A

A

B

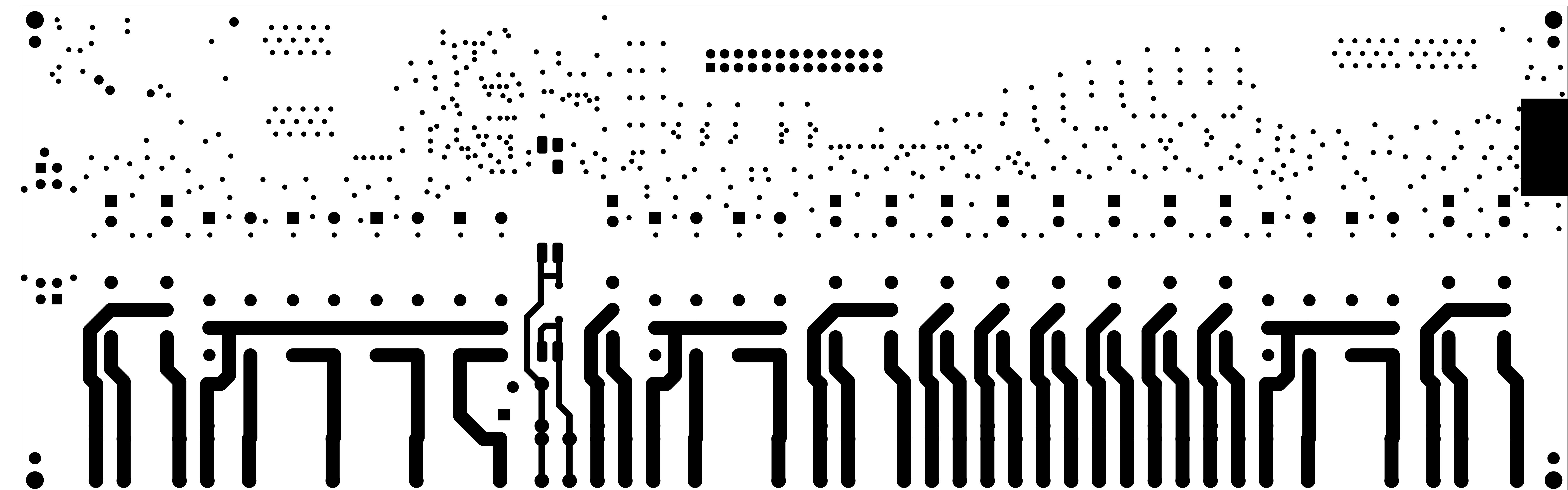
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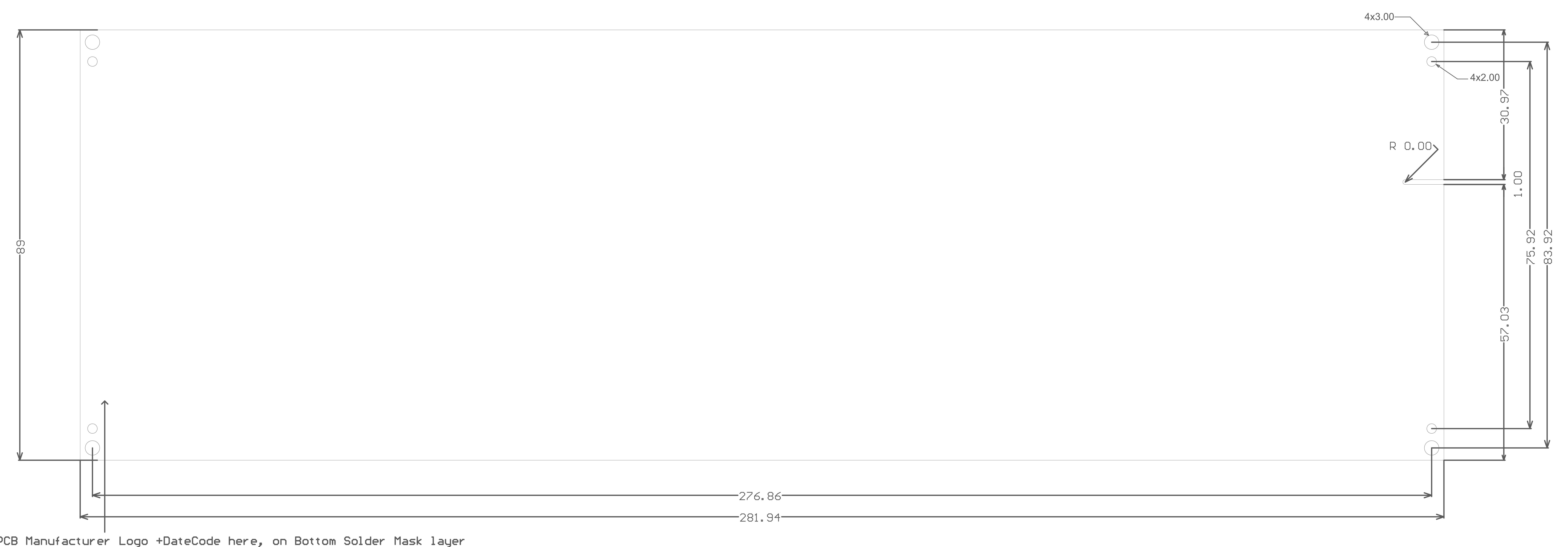
6

c

D

D





PCB Manufacturer Logo +DateCode here, on Bottom Solder Mask layer

A

A

B

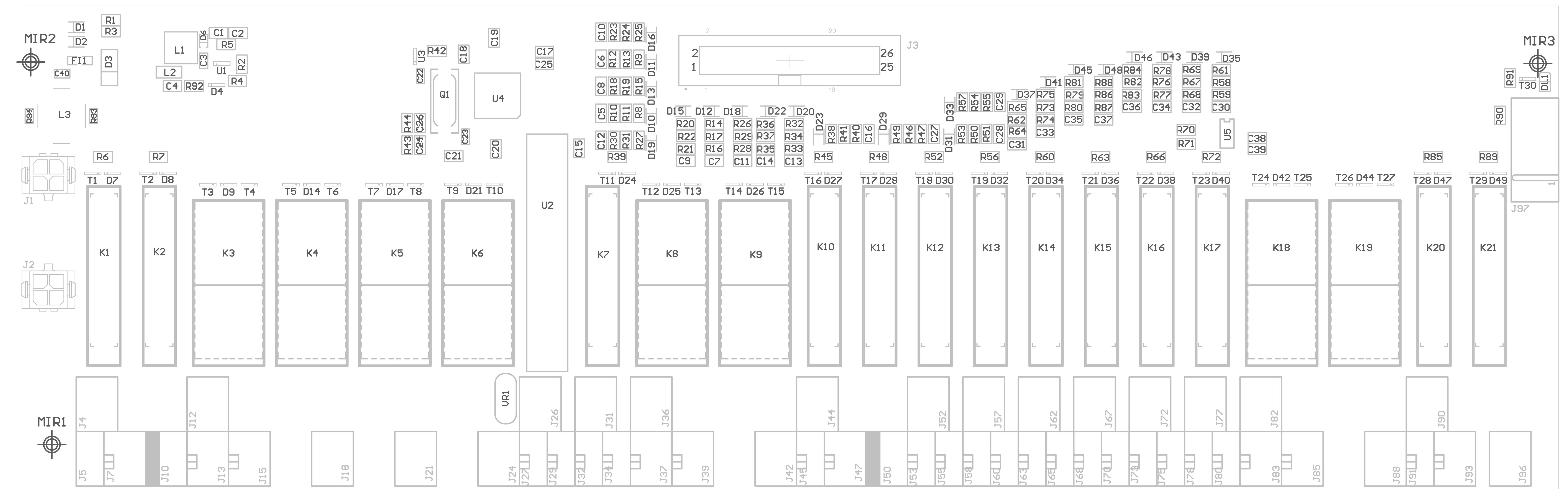
B

C

C

D

D



				Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC		Sheet Size A3		
						Print:		
SI941C	View: Top Assy		DATE: 11/4/2016					
a	16/10/16	Création	CVO	File: SI941C.PcbDoc				
Ind. Min	Doc Date	Objet	Nom	EDA: Altium Designer build 16.0.5.271				

A

A

B

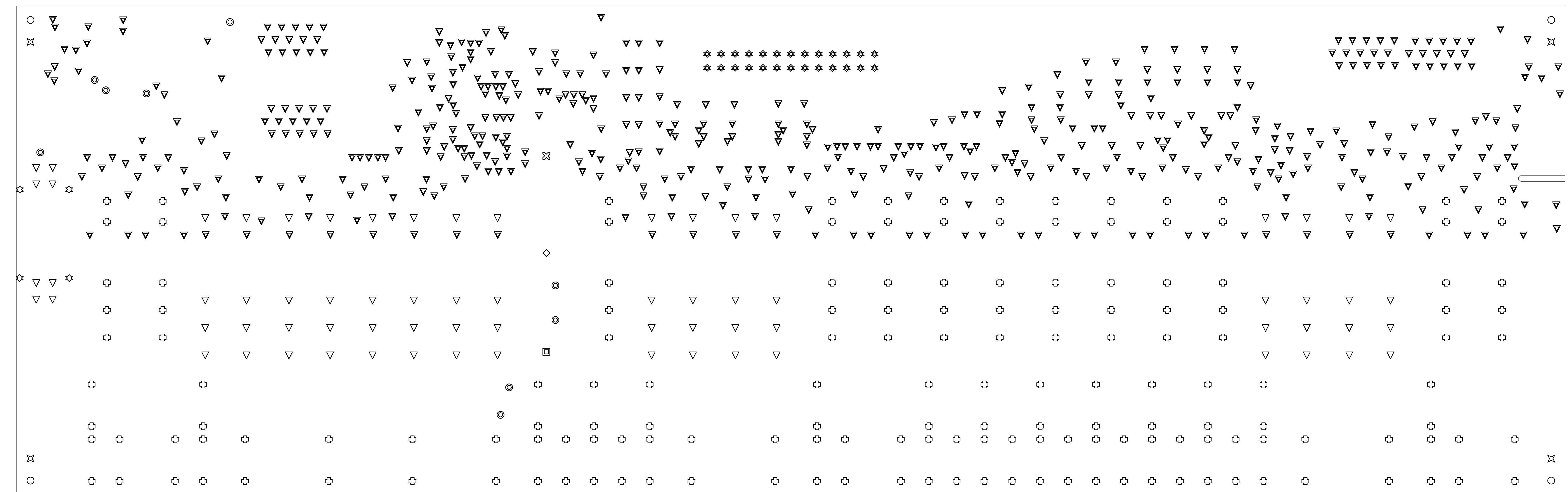
B

C

C

D

D



Symbol	Hit Count	Finished Hole Size	Plated	Hole Type	Hole Length	Routed Path Length
▽	503	0.432mm (17.00mil)	PTH	Round	-	-
○	9	0.800mm (31.50mil)	PTH	Round	-	-
✖	4	1.000mm (39.37mil)	NPTH	Round	-	-
▽	72	1.000mm (39.37mil)	PTH	Round	-	-
✖	26	1.100mm (43.31mil)	PTH	Round	-	-
✚	165	1.300mm (51.18mil)	PTH	Round	-	-
◊	1	1.800mm (70.87mil)	NPTH	Slot	5.000mm (196.85mil)	3.200mm (125.98mil)
▣	1	1.800mm (70.87mil)	NPTH	Slot	6.000mm (236.22mil)	4.200mm (165.35mil)
✖	1	1.800mm (70.87mil)	NPTH	Slot	8.100mm (318.90mil)	6.300mm (248.03mil)
✖	4	2.000mm (78.74mil)	NPTH	Round	-	-
○	4	3.000mm (118.11mil)	NPTH	Round	-	-
790 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

				Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC	Sheet Size A3
					Print:
SI941C				View: Drill Drawing For (Bottom,Top)	DATE: 11/4/2016
a	16/10/16	Création	CVO	EDA: Altium Designer	
Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271	SCALE: 1.00