



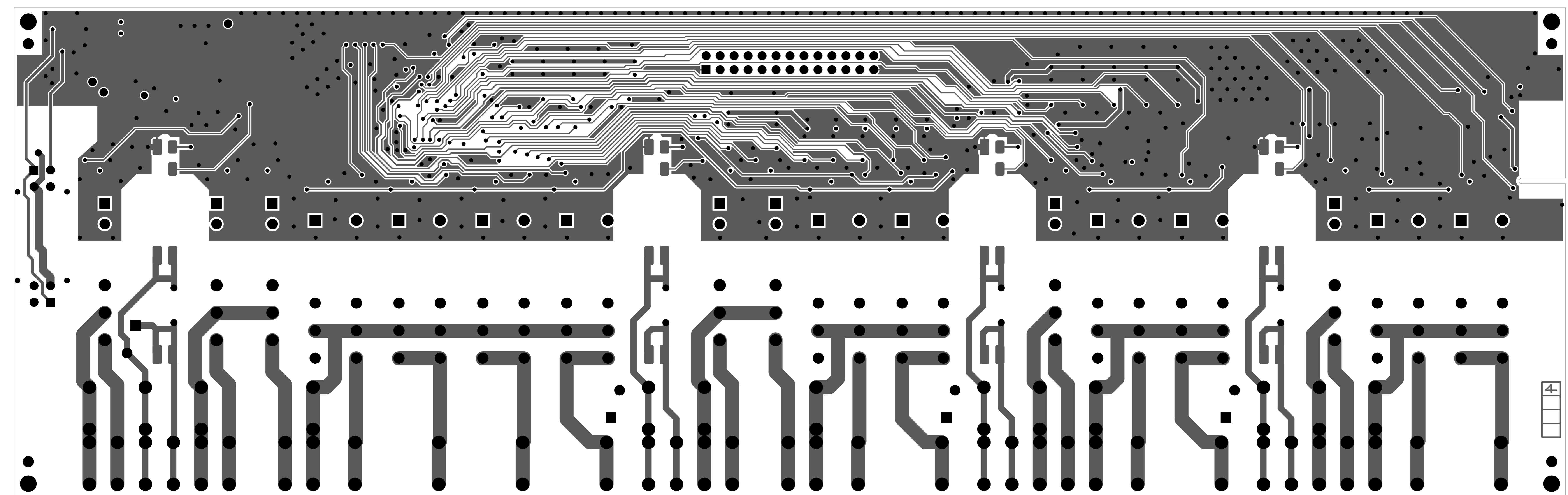
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Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				




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


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							Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC	Sheet Size A3
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Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				



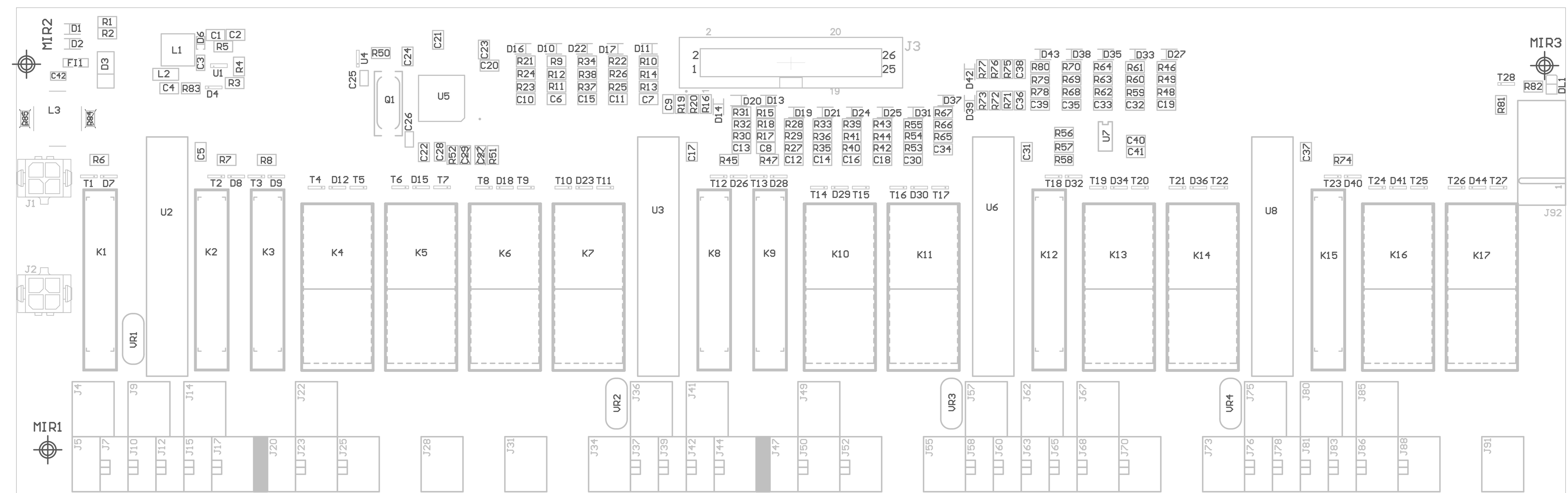
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A

A



B

B

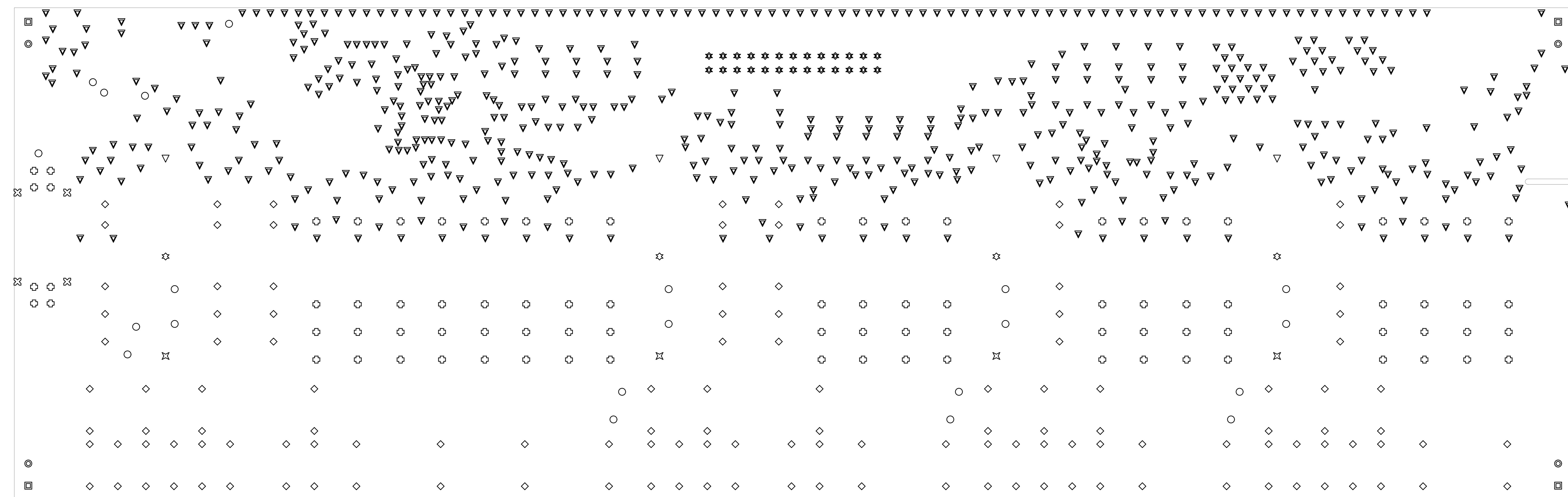
C

C

D

D

							Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC	Sheet Size A3
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Ind. Min	Doc Date	Objet	Nom					



Symbol	Hit Count	Finished Hole Size	Plated	Hole Type	Hole Length	Routed Path Length
▼	581	0.432mm (17.00mil)	PTH	Round	-	-
○	21	0.800mm (31.50mil)	PTH	Round	-	-
✗	4	1.000mm (39.37mil)	NPTH	Round	-	-
¤	88	1.000mm (39.37mil)	PTH	Round	-	-
>*	26	1.100mm (43.31mil)	PTH	Round	-	-
◊	129	1.300mm (51.18mil)	PTH	Round	-	-
✿	4	1.800mm (70.87mil)	NPTH	Slot	5.000mm (196.85mil)	3.200mm (125.98mil)
✗	4	1.800mm (70.87mil)	NPTH	Slot	6.000mm (236.22mil)	4.200mm (165.35mil)
▽	4	1.800mm (70.87mil)	NPTH	Slot	8.100mm (318.90mil)	6.300mm (248.03mil)
◎	4	2.000mm (78.74mil)	NPTH	Round	-	-
■	4	3.000mm (118.11mil)	NPTH	Round	-	-
869 Total						

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

					Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC	Sheet Size A3
						Print:
					SI942C	View: Drill Drawing For (Bottom,Top)
						DATE: 11/4/2016
a	16/10/16	Création	CVO		File: SI942C.PcbDoc	SCALE: 1.00
Ind. Min	Doc Date	Objet	Nom		EDA: Altium Designer build 16.0.5.271	