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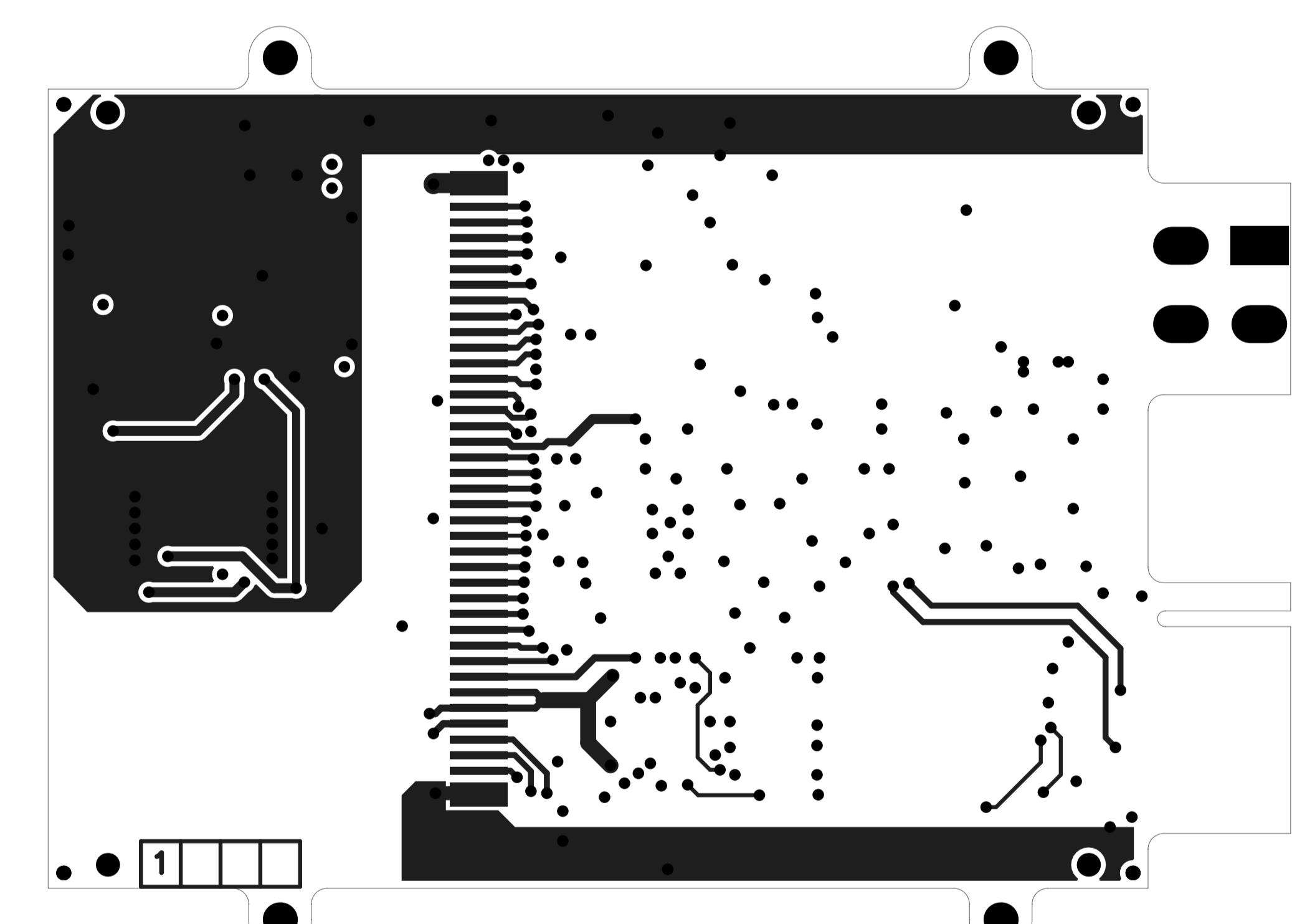
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								File: SI945D.PCBDOC SCALE: 1.00
a	16/10/16	Création	CVO	EDA: Altium Designer				
Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				

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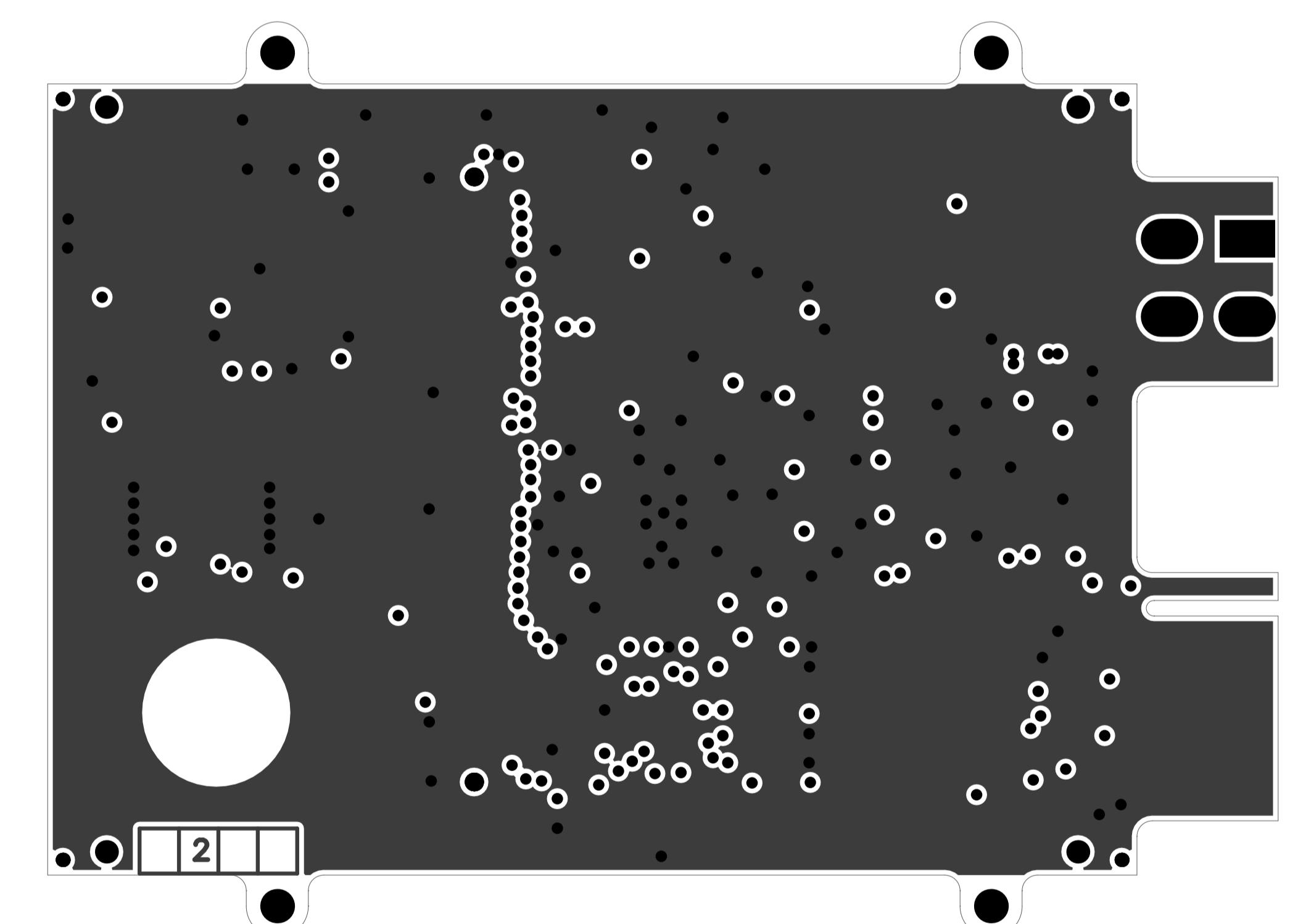
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Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				

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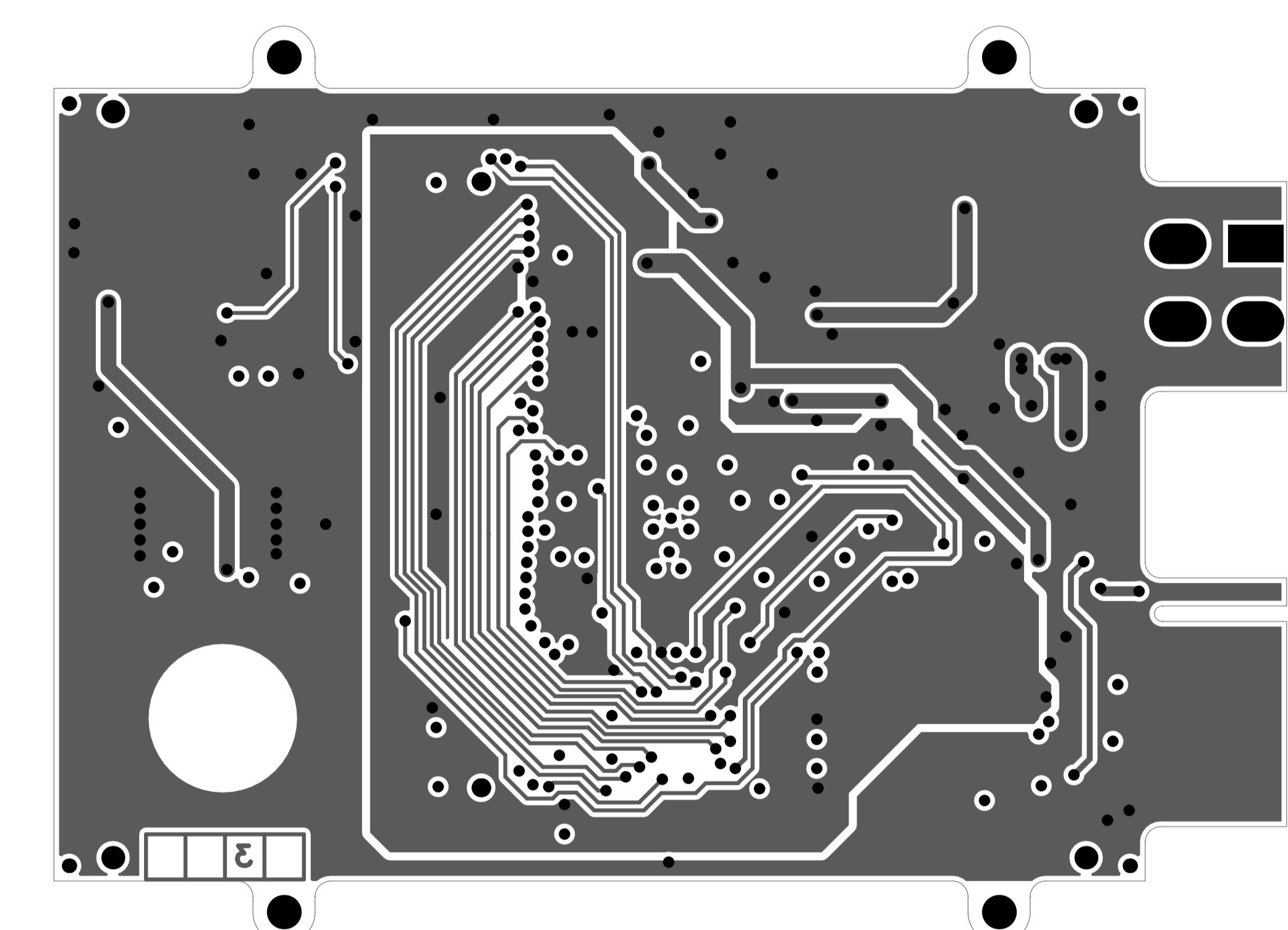
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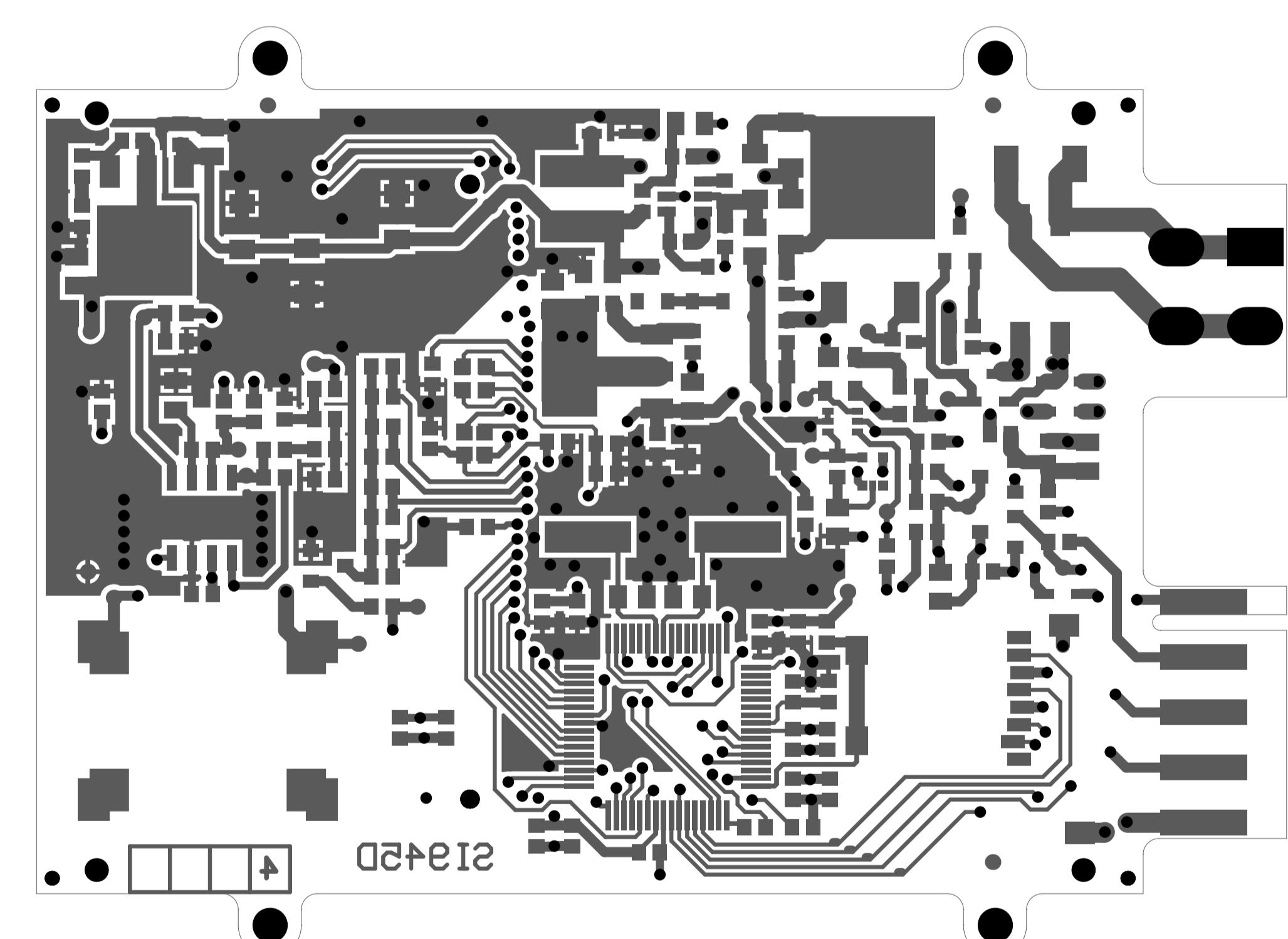
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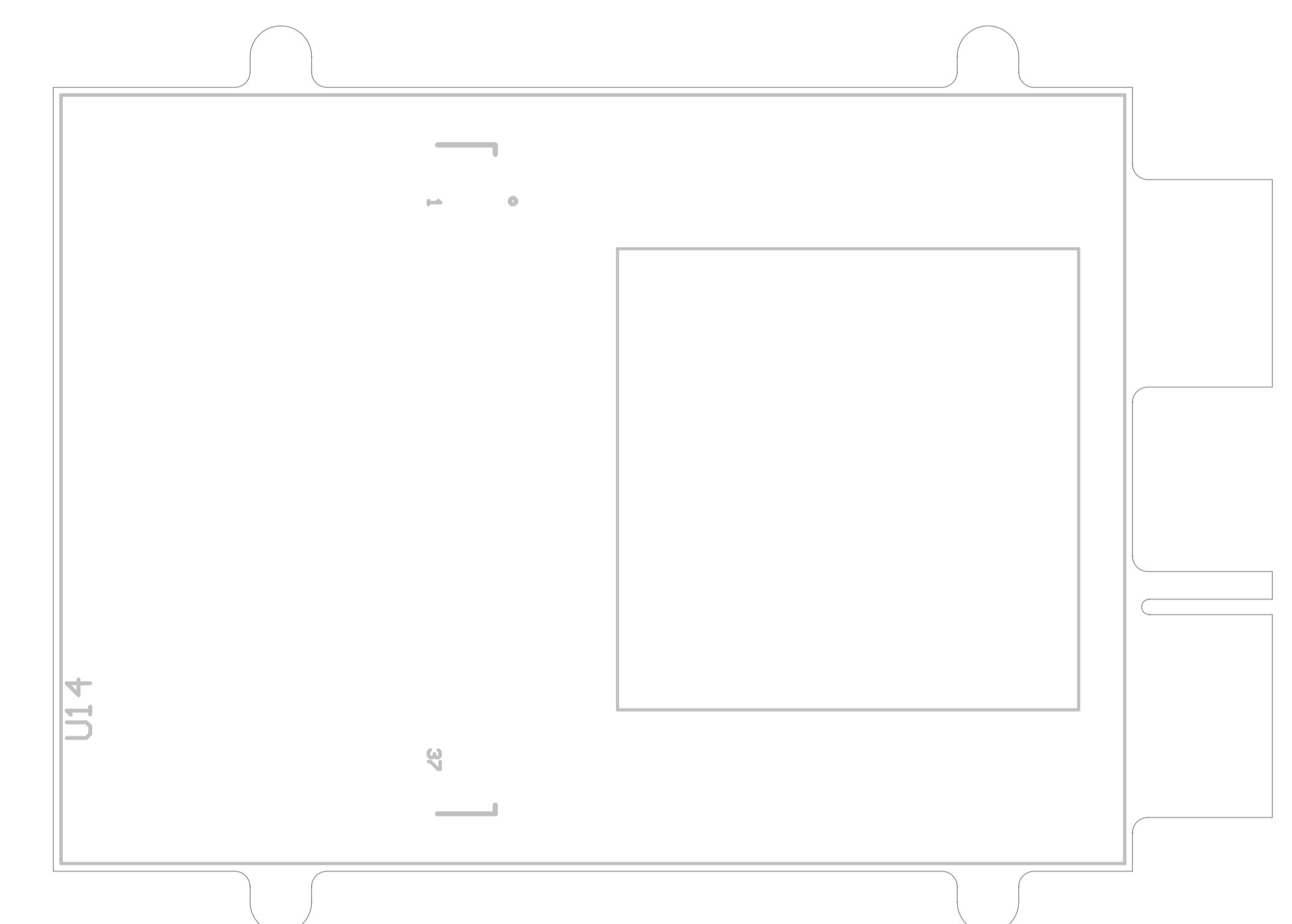
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		Objet	Nom	build 16.0.5.271				

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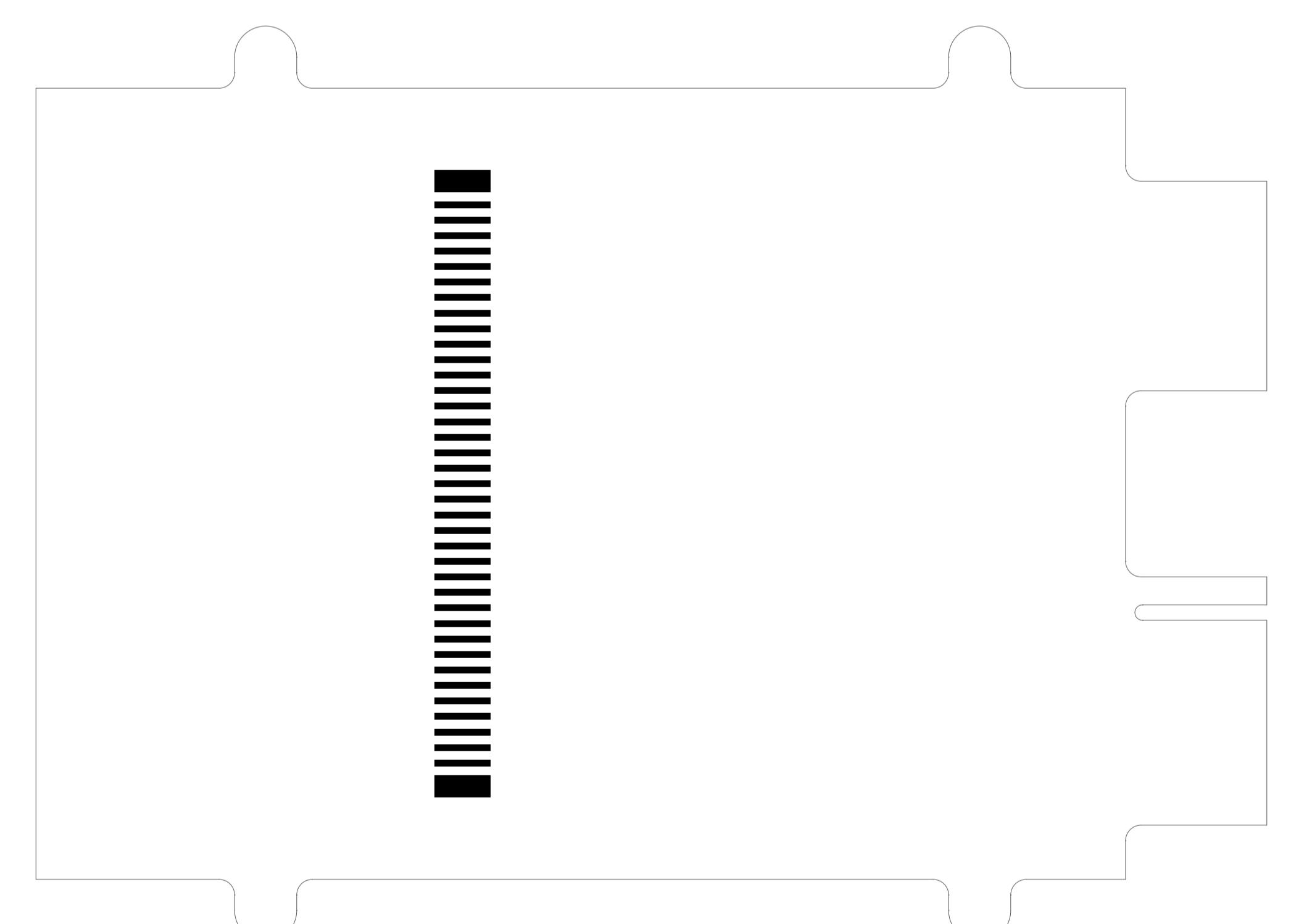
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Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				

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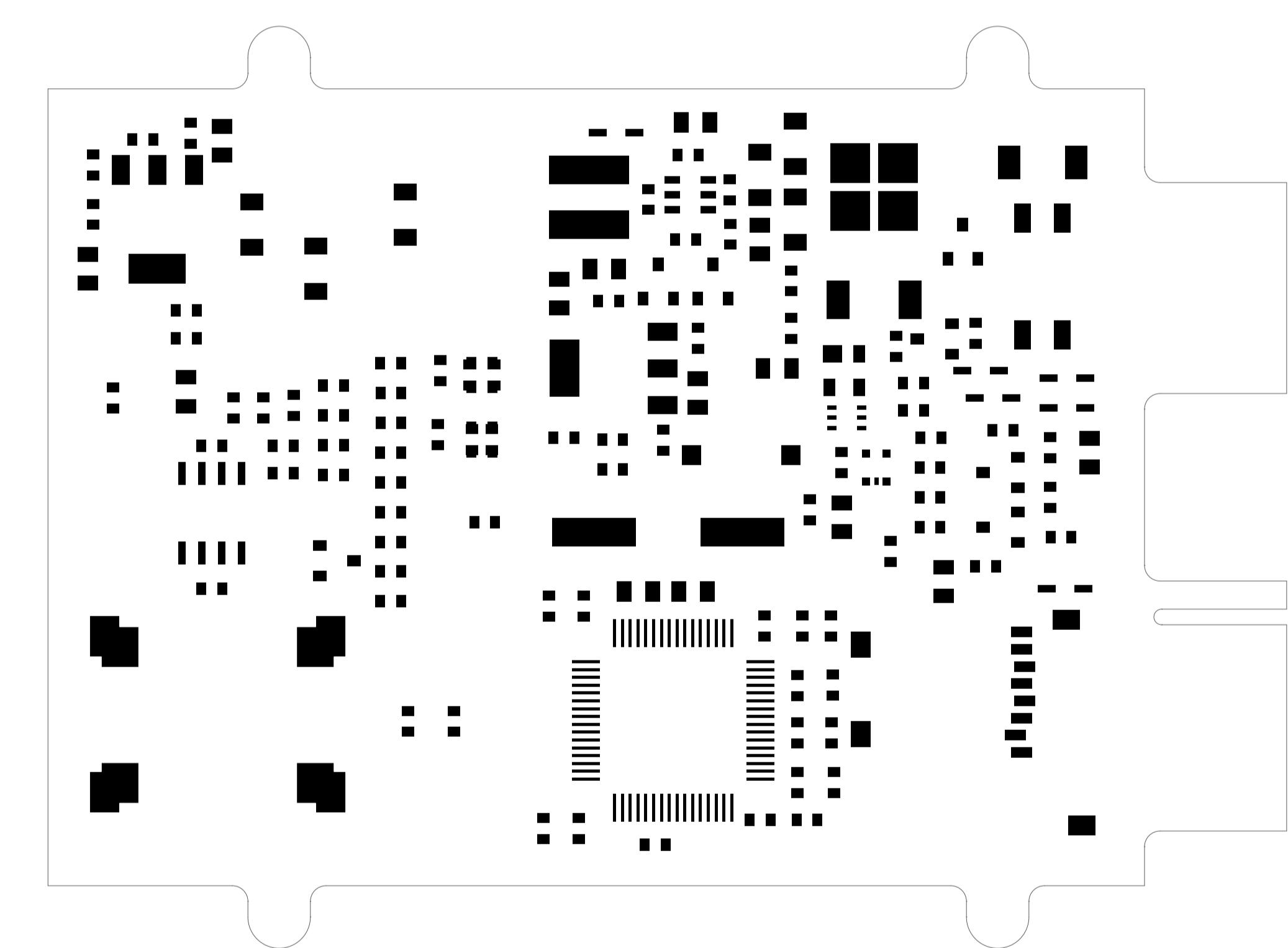
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Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				

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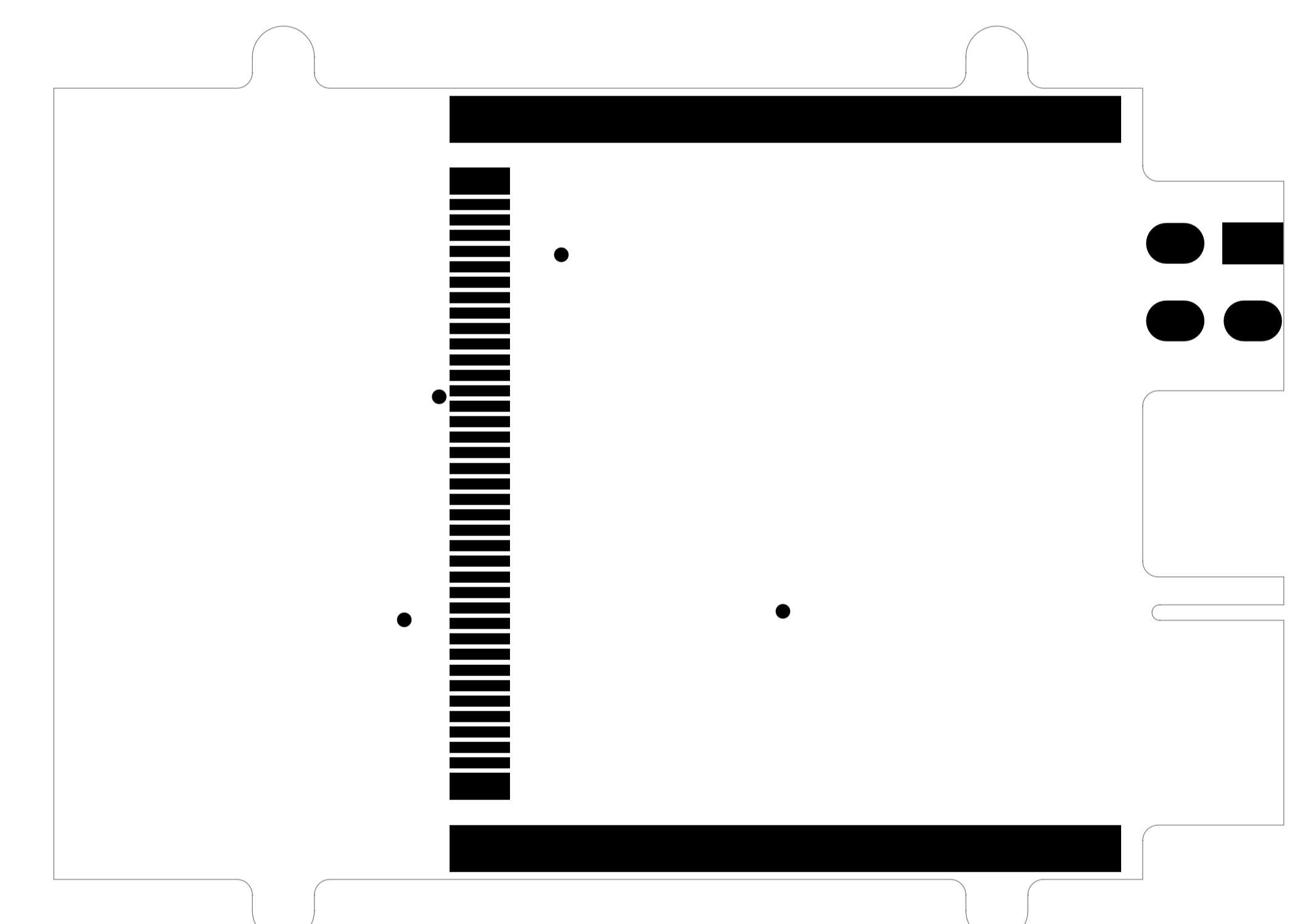
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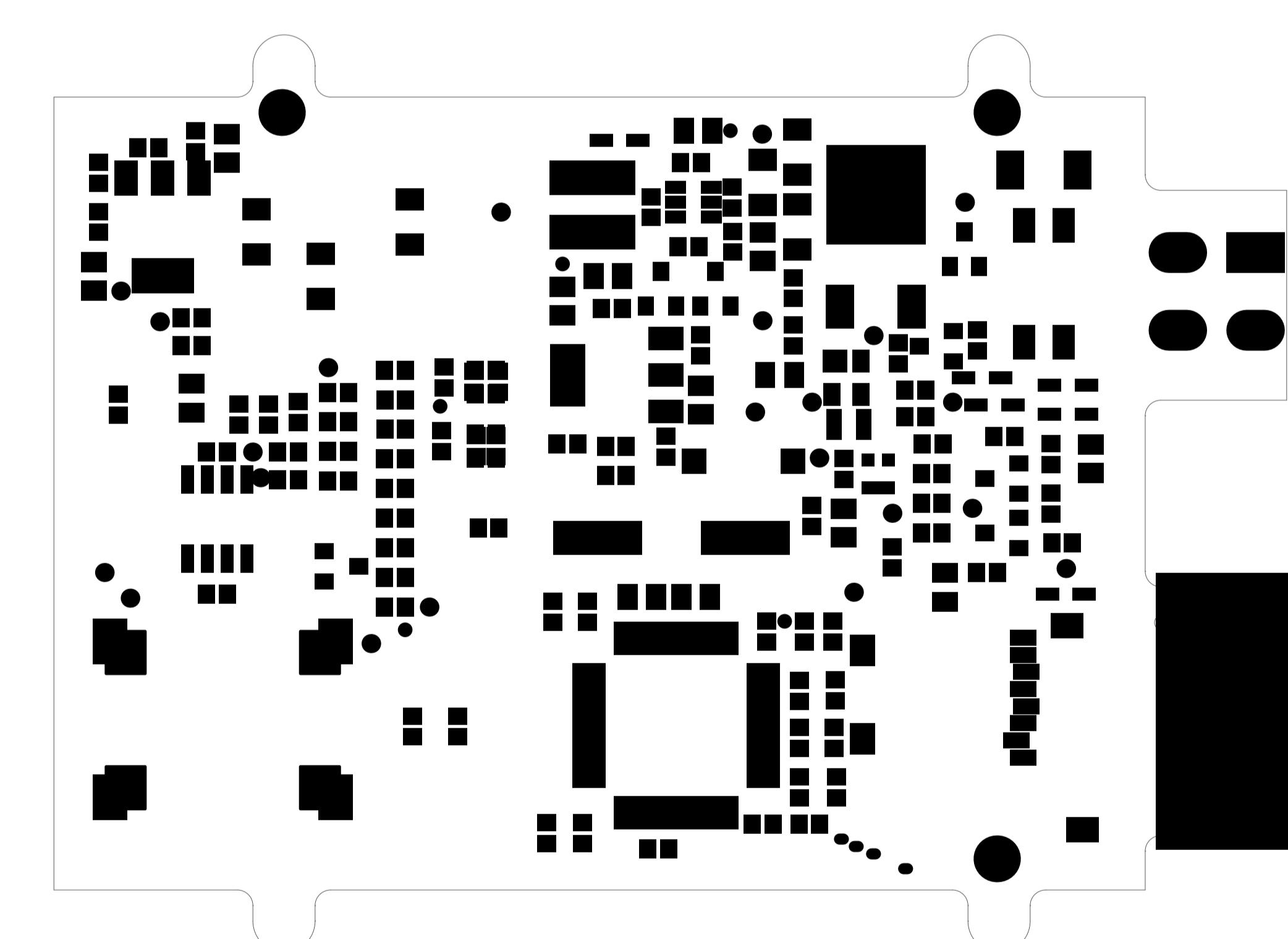
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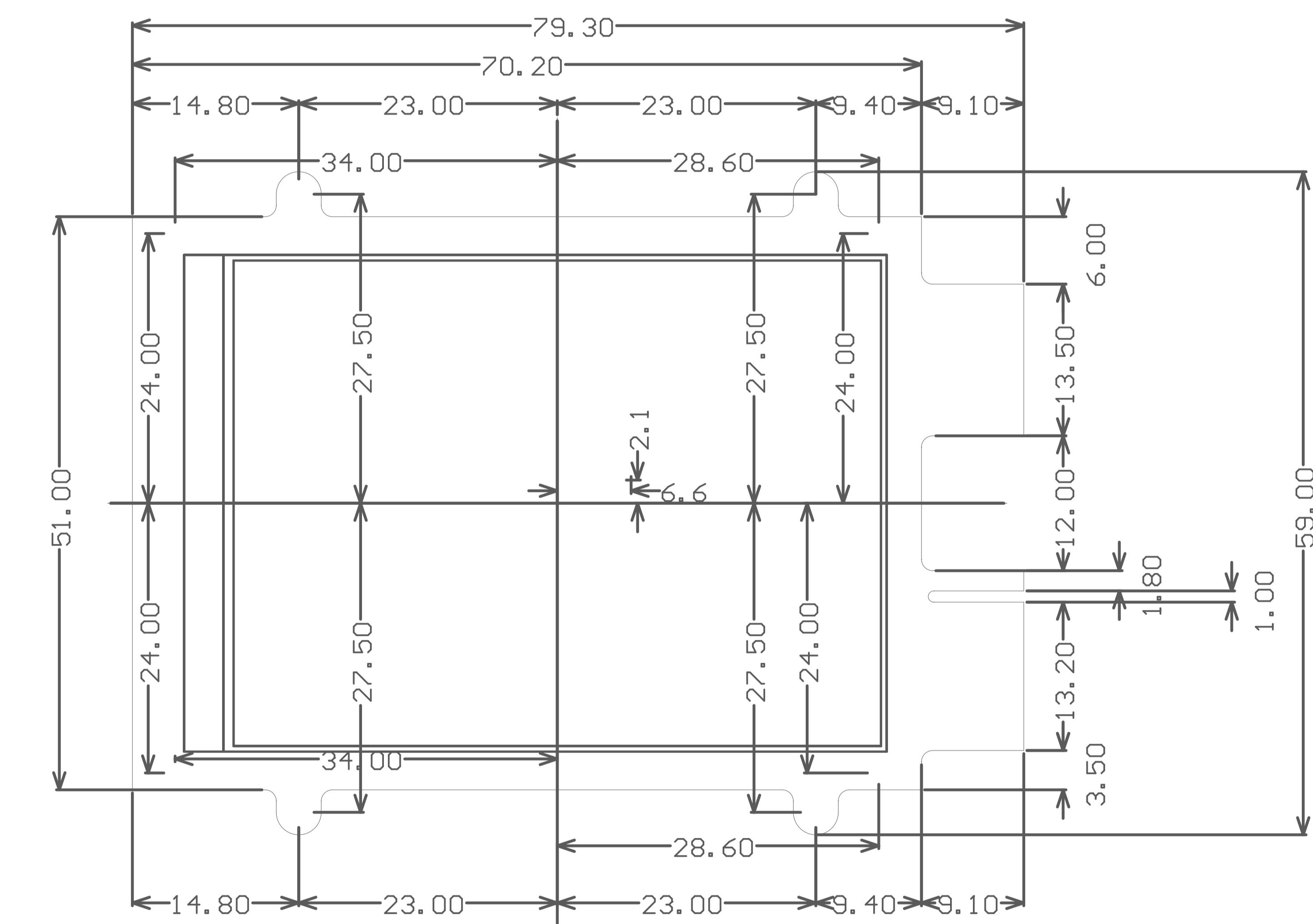
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a	16/10/16	Création	CVO				File: SI945D.PCBDOC	SCALE: 1.00
Ind. Min	Doc Date	Objet	Nom				EDA: Altium Designer	
							build 16.0.5.271	

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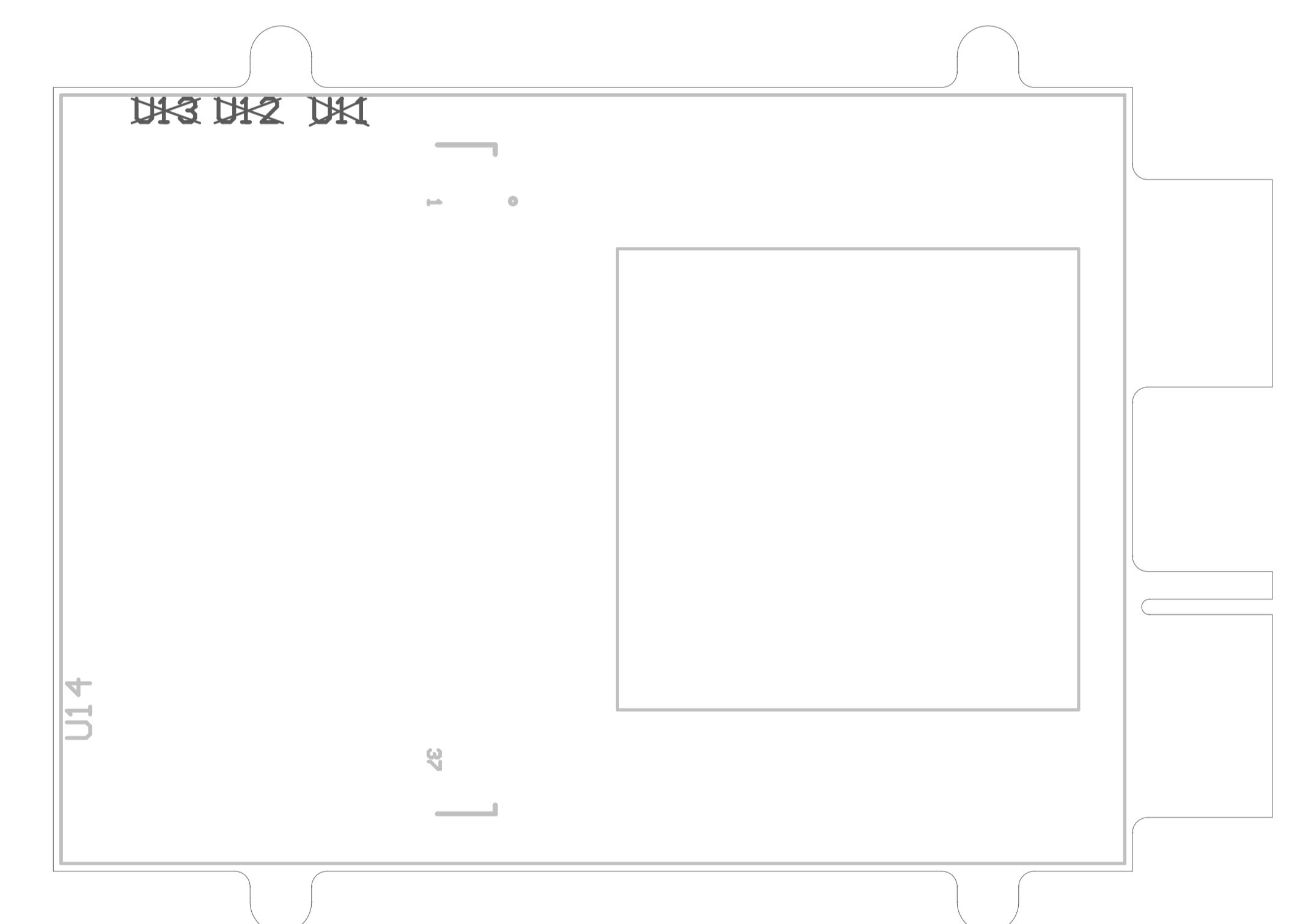
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Ind. Min	Doc Date	Objet	Nom	build 16.0.5.271				

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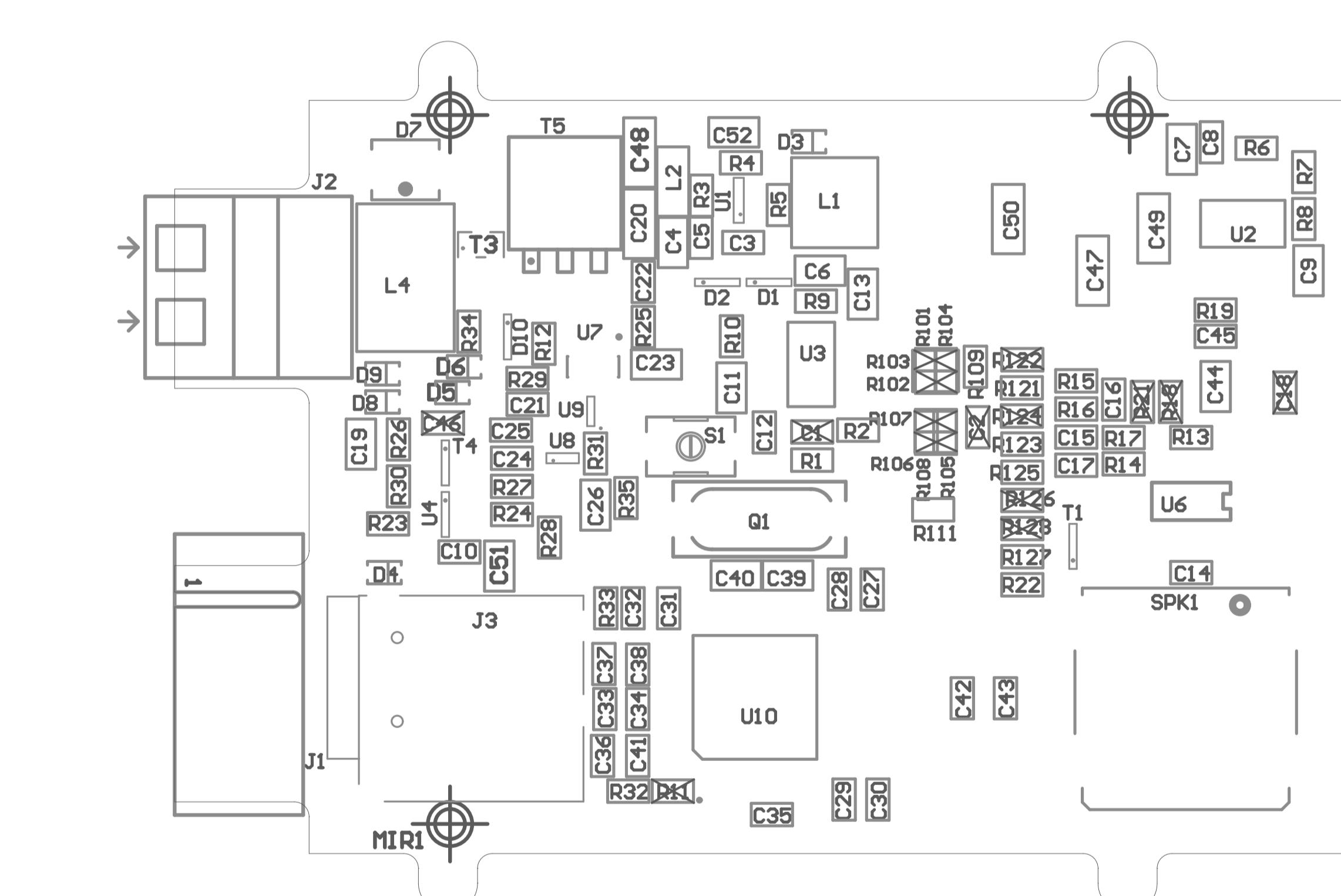
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	Circuit Imprimé pour carte .refSC Printed Circuit Board for .refSC	Sheet Size A3
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<b>SI945D</b>	View: Bottom Assy File: SI945D.PCBDOC	DATE: 11/2/2016 SCALE: 1.00
	EDA: Altium Designer build 16.0.5.271	

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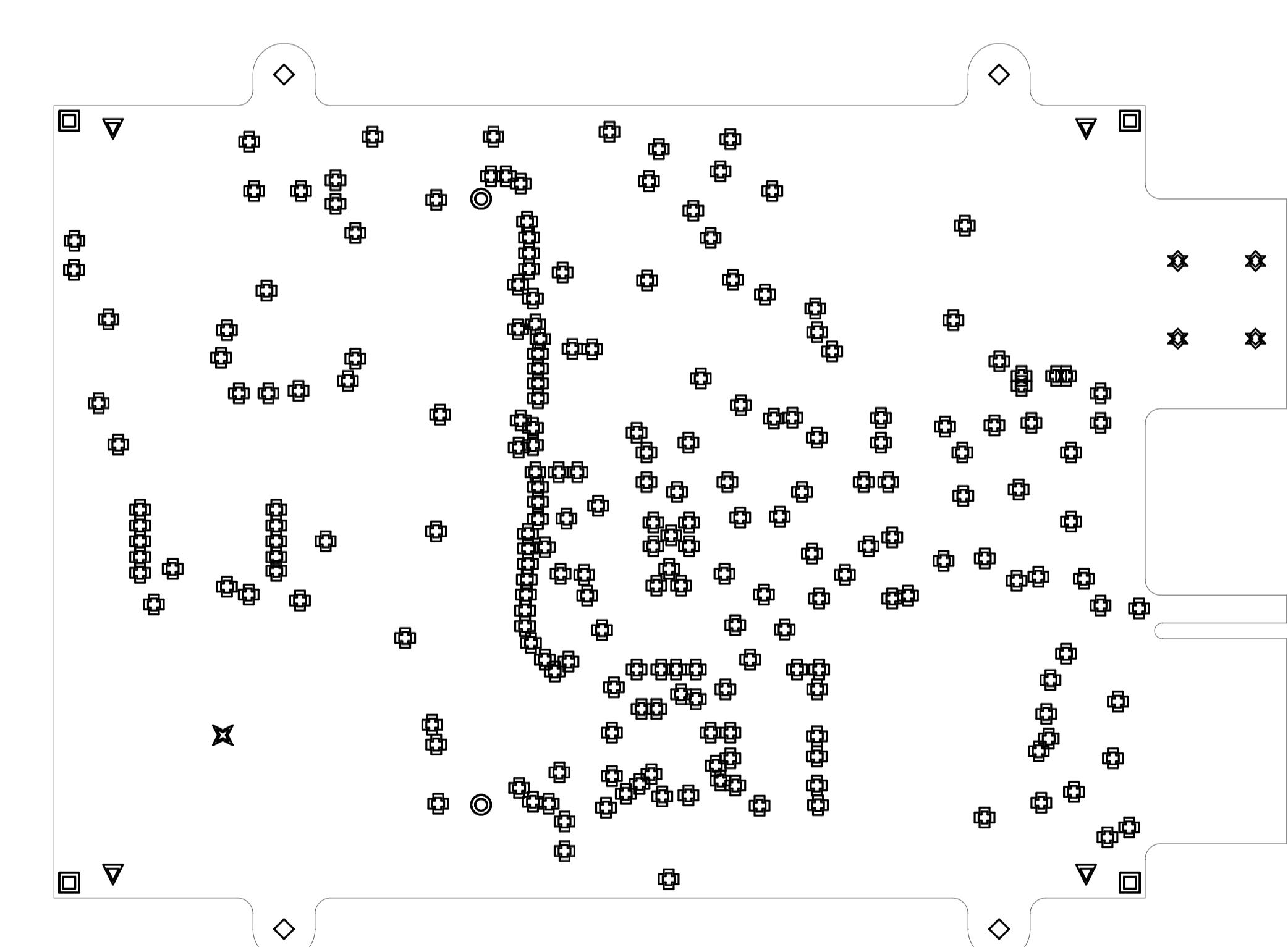
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Symbol	Hit Count	Finished Hole Size	Plated	Hole Type
+	218	0.432mm (17.00mil)	PTH	Round
□	16	0.920mm (36.22mil)	NPTH	Round
*	4	1.100mm (43.31mil)	PTH	Round
○	8	1.200mm (47.24mil)	PTH	Round
▽	4	1.500mm (59.06mil)	NPTH	Round
◇	4	2.200mm (86.61mil)	NPTH	Round
✗	1	8.900mm (350.39mil)	NPTH	Round
	255 Total			