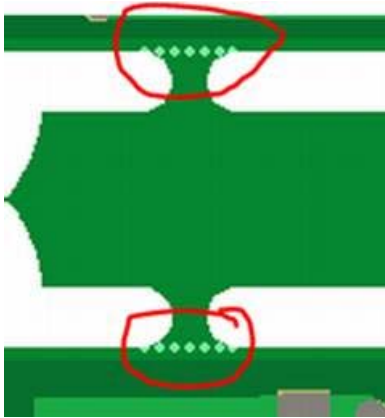


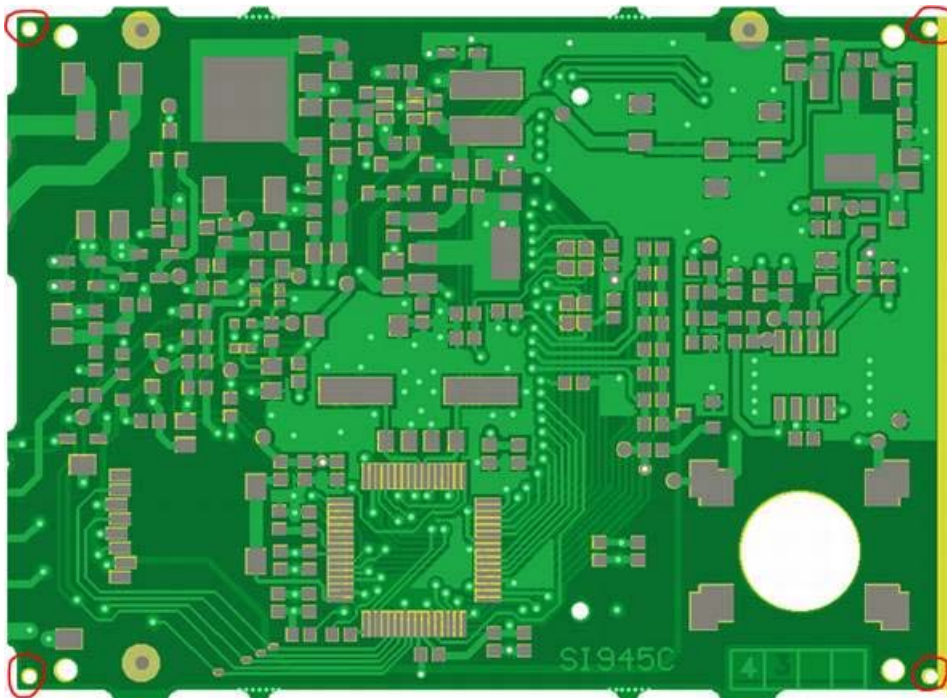
Some points to check :

- Is there some little holes to help breaking the holds ? I can't see them in the drl file :



- Why do you use a 2nd file ? Is it a drl file ? If yes, why not putting them in the drl file ?

- Why are those 4 holes not in the drl file ? :



- Can you generate the panel bottom paste please ? See attached the unitary board bottom paste.

- Can you make the pdf of the panelization same as 943D (see attached).