Portland State
UNIVERSITY

Esteban Gonzalez <esteban3@pdx.edu>

Test Cases

6 messages

Jennifer Carr <jencarr@pdx.edu>   Sat, Nov 22, 2014 at 4:11 PM
To: Esteban Gonzalez <esteban3@pdx.edu>, Evan Sprecher <sprecher@pdx.edu>, Chi King Wong
<chiking@pdx.edu>, Tim Gulzow <tgulzow@gmail.com>

I am working on document for test cases. I will send out when finished.

Jennifer

Esteban Gonzalez <esteban3@pdx.edu>   Sat, Nov 22, 2014 at 4:24 PM
To: Jennifer Carr <jencarr@pdx.edu>
Cc: Evan Sprecher <sprecher@pdx.edu>, Chi King Wong <chiking@pdx.edu>, Tim Gulzow <tgulzow@gmail.com>

Hey Jennifer,

I did start on a basic outline for a test case yesterday. I have a list of some of the specs for input and output
levels. I am a bit busy right now, but could forward what I have by about 10 tonight.

Esteban.
[Quoted text hidden]

Jennifer Carr <jencarr@pdx.edu>   Sat, Nov 22, 2014 at 4:39 PM
To: Esteban Gonzalez <esteban3@pdx.edu>

Sounds good. Thanks.
[Quoted text hidden]

Jennifer Carr <jencarr@pdx.edu>   Sat, Nov 22, 2014 at 6:03 PM
To: Esteban Gonzalez <esteban3@pdx.edu>
Cc: Evan Sprecher <sprecher@pdx.edu>, Chi King Wong <chiking@pdx.edu>, Tim Gulzow <tgulzow@gmail.com>

The test case template is uploaded to Git. Jennifer

On Sat, Nov 22, 2014 at 4:24 PM, Esteban Gonzalez <esteban3@pdx.edu> wrote:
[Quoted text hidden]

Esteban Gonzalez <esteban3@pdx.edu>   Sun, Nov 23, 2014 at 12:33 AM
To: Jennifer Carr <jencarr@pdx.edu>
Cc: Evan Sprecher <sprecher@pdx.edu>, Chi King Wong <chiking@pdx.edu>, Tim Gulzow <tgulzow@gmail.com>

Hi all,

Test plan is in the Google drive folder. Maybe we can all take a section and translate into a test case. The
detailed spec sheets are in the BOM folder if needed. I will start by tackling the parametric tests.

Esteban

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Esteban Gonzalez <esteban3@pdx.edu>   Sun, Nov 23, 2014 at 10:59 PM
To: Jennifer Carr <jencarr@pdx.edu>
Cc: Evan Sprecher <sprecher@pdx.edu>, Chi King Wong <chiking@pdx.edu>, Tim Gulzow <tgulzow@gmail.com>

Hi There,

I spent some time this weekend looking at the test plan and trying to make sense of our concept and execution. Read through some of the AVR 328P spec sheet which I uploaded to the google drive. Created a folder called HW6 as well and put my stuff in it since GITHUB hates me. One of the thing we need to at least identify is how to put the 328P into sleep mode in the absence of a trigger. Seems to be section 26 and 27 of spec sheet, but I am not sure how to translate it into program specs. It would be nice to have actual numbers to base our test plan on instead of the "back of the envelope" calculations I used based on my cursory knowledge.

Esteban
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