Project Objective

In this project, you will

- 1. learn how to implement a video system on FPGA
- 2. get introduced to video hardware functions
- 3. develop an application that runs on the video system

Hardware Requirements

- a. Remove the 7-segments components (Seg0-Seg7), as well as rotation_inputs, system_modes and the onchip_memory components.
- Make sure that your system includes the clock source, nios processor and JTAG-UART components.
- c. Rename the clock source as *clock_source_0*, nios processor as *processo*r and JTAG-UART as *jtag* components.
- d. Make sure that your processor is on the fast mode (niosII/f) and the instruction and data cache is 4KB.
- e. You are required to add the following components and setup its parameters as shown below. You may also use the diagram in L10-slide5 as a reference.
 - 1. Clock_source_1

name	Conduit name
clock_source_1	sdram_clk

2. System and sdram clock

name	Setting
sys_sdram_pll	DE-Series Board: DE2-115

3. Dual-Clock FIFO

name	Color bits	Color planes
dual_clock_fifo	10	3

4. RGB Resampler

name	Incoming format	Outgoing format
rgb_resampler	24-bit RGB	30-bit RGB

5. SDRAM controller

board device	name	Conduit name	Bit Width	Architecture	Address width	Generic memory
DRAM	sdram_controller	sdram	32	Chip select: 1, Banks:4	Row: 13, Col: 10	Enable

6. SRAM Controller

Name	conduit	Setting	Enable
sram_controller	sram	DE2-115	Use as pixel buffer for video out

7. Pixel Buffer DMA Controller

name	Addressing Mode	Frame Resolution	Pixel Format
dma_buffer	Consecutive	Width 320, Height 240	24-bit RGB

8. Scaler

name	Width	Height	width	Height	Data bits	Symbol
	factor	factor			per symbol	per beat
video_scaler	2	2	320	240	10	3

9. VGA Controller

name	conduit	DE-series Board	Video out device	VGA Resolution
vga	vga	DE2-115	VGA connector	VGA 640 x 480

f. Video clocks for DE-series Boards

Input settings	Video out setting	
50 MHz	Enable vga clock	

g. <u>pio_0</u>

Name	Conduit name	Width	Direction
key1	key1	1	input

h. <u>pio_1</u>

Name	Conduit name	Width	Direction
key2	key2	1	input

i. <u>pio_2</u>

Name	Name Conduit name		Direction
key3	key3	1	input

- j. Set reset and exception vectors of NIOS processor to sdram_controller (instead of onchip memory as we had in previous projects).
- k. In Blackboard, along with the assignment, you will find video system_connections.pdf attached which can be used as a reference to connect the components.
- I. **All resets must be connected to the Reset Output (clk_reset) of clock_source_0

Software Requirements

You are required to write a C/C++ application code to execute the functions shown in the following table. Several things are explained below as a guidance you use to write the code.

Function	KEY1	KEY2	KEY3	Function
0	0	1		Display the gamecock image
	"push			on the screen
	button"			
1	1	0		Resize the image by factor of
		"push		0.5 (both width and height)
		button"		
2	1	1	0	Resize the image by factor of
			"push	2 (both width and height)
			button"	
3	1	1	1	Print message "Video project"

- Remove (or comment) any code that belongs to the functions (7-segments and system mode)
 that we used for previous projects.
- 2. A software template (ctemplate.c) is attached which you can use it as your reference.
- Two attached documents (file.c and file.h) include the source code of gamecock image and the header file respectively.

- 4. You should add these two files (file.c and file.h) into your application project by doing the following:
 - a. Download the two files into any directory (for example download folder).
 - b. Then, open the folder containing the files (the download folder)
 - c. Drag these two files into your application project.
 - d. The instructor will also explain this during the lecture.
- 5. To access the pixel buffer and dma functions, call the following the header in your application C/C++ code:

```
#include <altera up avalon video pixel buffer dma.h>
```

6. To read the pixel_buffer (or the sram content), use the following code:

```
alt_up_pixel_buffer_dma_dev *my_pixel_buffer;
my_pixel_buffer =
alt_up_pixel_buffer_dma_open_dev("/dev/dma_buffer");
```

7. After that you need to check if the pixel buffer array (my_pixel_buffer) contains the image pixel to do so, add the following code:

```
if(!my pixel buffer) printf("Error opening pixel buffer\n");
```

8. Anytime you want to display an image on the screen, try first to clear the screen by calling the following function:

```
alt_up_pixel_buffer_dma_clear_screen(my_pixel_buffer,0);
```

9. To display an image, you need to use the following function and include the following header file:

10. To resize the image by any factor f (where f is an integer) and display the new image. For example, to double the image size, f=2. You may use the following code:

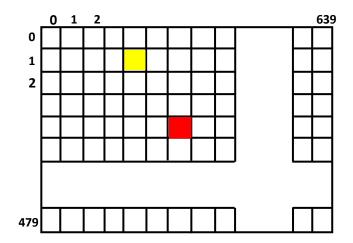
Project Report (60%)

The project report will be graded out of 100, and the points will be distributed as following:

a. **Professional preparation** (10 points):

You are required to submit a typed document with text of the paragraphs in Times New Roman 11 pt font, clear and grammatically well-formed explanations, page numbering and document heading numbering (1.0, 2.0, 3.0, etc to identify the required sections listed below).

- b. Report Content (90 points):
 - **1.0** (30 points total, each value is 10 points) Run the application project and take a picture for the screen that shows the execution of each function: display image, resize image by 0.5 and resize image by 2.
 - **2.0** (70 points) Answer the following questions:
 - a. You are given the following frame
 - 1. (15 points) Find its resolution
 - 2. (15 points) The coordinates of the red pixel.
 - b. Assume you are required to map the given frame as 24-bit RGB into a pixel memory which its base address is 0x08000000.
 - 1. (20 points) Find the pixel address of the yellow pixel.
 - 2. (20 points) Find the frame size



Project Demo (40%)

- ♣ The main purpose of the demo is to test your project functionality and execution.
- Demos will be checked and graded by the TA
- ♣ Demos will be graded out of 100, but worth 40% of total project grade
- ♣ Both partners must show up in that day. If a member didn't show up, he/she receives 0 unless an excused absence was provided.
- Demos will be conducted during the lab time on the following dates:
 - o Section 001: Wed. March 23rd or Wed. March 30th
 - o Section 002: Fri March 25th or Fri. March 27th
 - o Demo dates will be decided by the groups
- Below are how the demo points will be distributed

Tasks	Point
Display image when KEY1 ="0" (press push button)	/30
Resize image by 0.5 when KEY2="0" (press push button)	/30
Resize image by 2 when KEY2="0" (press push button)	/30
Print message "Video project" when no key is pressed	/10

Project Submission

- Save the project report as r4_username1_username2.pdf, username of both students in the group.
- 2. For this project, you are required to submit only the project report (No project submission is required). Submission date is <u>Sunday March 27th by midnight.</u>
- 3. Only one attempt is allowed
- 4. Only one group member can submit the report
- 5. Remember: Any grade dispute must be raised within one week of the grade posting.