

## CS 118 HW 2

- 1a) The solution that uses the minimal amount of padding necessary to make sure we can use Alyssa's rules and yet decode correctly is to always add a single zero byte as padding regardless of whether the original frame ends with a zero byte. For example:

- ① You want to send "X0"
- ② Pad it so it becomes "X00"
- ③ Encode the padded message as "2X1"
- ④ Add the framing characters around it

↳ Note: This is ignored in the computation of overhead

- 1b) If the input data contains all 0's, then the length of the coded data would be the number of digits needed to represent the length of the input data. For example:

"000" → three 0's → 3, Coded data is one digit long,  
to represent '3'.

In general, the coded data may only be a couple bytes longer than the original input data. These extra bytes come from the length field at the beginning. For instance:

"a b c d e f g h i j" → 10 a b c d e f g h i j

- The encoded data is 2 bytes longer due to the addition of the length field.
- A similar message of length 200 would have encoded data that is 3 bytes longer due to the 3-digit length field.

It depends on the length of the message and whether there are 0's in the message, but in general the coded data may only be 2 or 3 bytes longer than the input data.

1c) The question mentions "your scheme" - I'm assuming this is referring to my answer for 1a.

- $L \ll 254$

Say we have a very small message :  $m = "A"$

We would then pad it with the zero byte :  $m = "AO"$

Then the message gets encoded as :  $m = "1A"$

Our final encoded message (2 bytes) is twice as long as our original input (1 byte).  $\frac{1}{2}$  of the bits in the encoded message are overhead (50%).

- $L \gg 254$

Say we have a very large message :  $m = "AOAOAO..."$  [pretend this repeats many times]

The message would be split into many blocks: "AO"

Each of these blocks will then be padded: "AOO"

Each of these blocks will be encoded : "ZAI"

The final encoded message would be : "ZAI ZAI ZAI..."

Our final encoded message will have 33% more bits as our original input.  $\frac{1}{3}$  bits in the encoded message are overhead (33%).

1d) def decode(msg):

    output = ""

    while (msg && msg[0] is a number):

        x = msg[0]

        temp = the x characters following msg[0]

        output += temp

        delete msg[0:x]

    iterate through output :

        restore all occurrences of 0

    return output

[I'm sorry if anything seems off - this question was incredibly ambiguous and when I asked my TA we gave even more convoluted responses. I tried my best to decipher the question but I'm not very confident.]

$$2. \quad x^4 + x + 1 \rightarrow 10011$$

A) 100101

|  |  |
|--|--|
| $\begin{array}{r} 100011 \\ 10011   1001010000 \\ 10011 \downarrow \end{array}$          | CRC is 101.<br>Message is sent as :<br>$100101101$ |
| $\begin{array}{r} 000011 \\ 0000000 \downarrow \end{array}$                              |  |
| $\begin{array}{r} 110 \\ 000 \downarrow \end{array}$                                     |  |
| $\begin{array}{r} 1100 \\ 0000 \downarrow \end{array}$                                   |  |
| $\begin{array}{r} 11000 \\ 10011 \downarrow \end{array}$                                 |  |
| $\begin{array}{r} 010110 \\ 1001 \downarrow \\ 000101 \leftarrow \text{CRC} \end{array}$ |  |

B) Message + CRC 100101101

$$\text{Generator} + 10011 \quad \boxed{\text{The resulting message is : } 100111101}$$

C) This generator does detect all 1-bit errors. It can detect any burst errors up to the highest degree of the generator (4-bit, 3-bit, 2-bit, 1-bit burst errors). A 1-bit burst error is the same as a 1-bit error, thus we know the generator can detect all 1-bit errors.

|                           |  |
|---------------------------|--|
| D) 111011<br>$11   10011$ | There will be a remainder of 1, odd bit error.<br>This means that this generator odd number of bits will be flipped in the result. |
|                           | If the generator was divisible by $x+1$ (11), then it would catch all odd-bit errors.  |

[odd # of bits is even & bits in generator - generator never divisible by  $x+1$ , can detect odd bit errors]

$$E) (x^5 + 1) \cdot (x^n + x + 1)$$

$$x^9 + x^6 + x^5$$

$$x^4 + x + 1$$

$$x^9 + x^6 + x^5 + x^4 + x + 1$$

List of Polynomials

$$x^5 + 1$$

$$x^5 + x + 1$$

$$x^5 + x^2 + 1$$

$$x^5 + x^2 + x + 1$$

$$x^3 + x^3 + 1$$

$$x^5 + x^3 + x + 1$$

$$x^5 + x^3 + x^2 + 1$$

$$x^5 + x^3 + x^2 + x + 1$$

$$x^5 + x^4 + 1$$

$$x^5 + x^4 + x + 1$$

$$x^5 + x^4 + x^2 + 1$$

$$x^5 + x^4 + x^3 + 1$$

$$x^5 + x^4 + x^3 + x^2 + 1$$

$$x^5 + x^4 + x^3 + x^2 + x + 1$$

$$x^5 + x^4 + x^3 + x^2 + x + 1$$

There are 16 polynomials we could multiply our generator by in order to get an undetected burst error of length 10. Multiplying the generator by any of the polynomials on the right would yield a polynomial that starts with  $x^1$  and ends with 1 and has optional terms for the remaining powers between 8 and 1. There are 16 polynomials for any degree 4 CRC. This is because the

number of polynomials whose highest power is  $x^5$  and whose lowest power is 1 is the same as the number of distinct combinations for the middle terms. The middle terms are  $x^4, x^3, x^2$ , and  $x$ . The number of combinations would be given by  $2^4 = 16$ . Every degree 4 CRC has 16 of these polynomials, and they are the same 16 listed above.

F) There are  $2^{n-2}$  total possible burst errors. This comes from the number of distinct combinations that can be made where the lowest and highest terms are both set. There are  $2^{n-k-2}$  undetected burst errors. This comes from the number of polynomials that can be multiplied by the generator to create a polynomial that has a lowest degree of 1 and highest degree of  $n$ .

Having a generator with degree  $K$  and following our established logic, we get  $2^{n-1-k-2}$ .

$$P(\text{Not Detecting}) = \frac{\# \text{ undetected}}{\text{total errors}} = \frac{2^{n-k-2}}{2^{n-2}} = \frac{1}{2^k} \quad (\text{CRC-32 fails to detect burst errors greater than } 32 \text{ times. This is very rare!})$$

### 3. Error Recovery

- A) The STATUS packet is necessary because there may be instances where the sender is unaware that an error has occurred. An example of this is if the final packet from the sender gets lost, then the sender will not receive a Nack in return. Even worse, every single packet from the sender could get lost, and the sender would never even see a Nack since the receiver never got any of the packets. It is also possible that the Nack itself may get lost from the receiver to the sender, which would be problematic. The STATUS packet is also useful because the sender can periodically check with the receiver to see if they are properly receiving messages.
- B) The sender will periodically send STATUS packets as long as there are still unacknowledged data packets. This means that the sender will keep trying to send the data packets to the receiver over and over until the receiver acknowledges that they have received the packets. Thus, any data packet given to the sender will eventually reach the receiver (as long as the link delivers most packets without errors).
- C) The timer should be started if there are packets remaining in the sender's queue. If there are no packers in the sender's queue, then the timer can be stopped.
- D) In the worst-case scenario, the latency would be equivalent to the length of two status timers + the length of the round-trip time. See the sketch below for a visualization of what happens between the sender and the receiver. After the events shown in the sketch, the sender's queue is empty so the timer stops and the process is complete.

