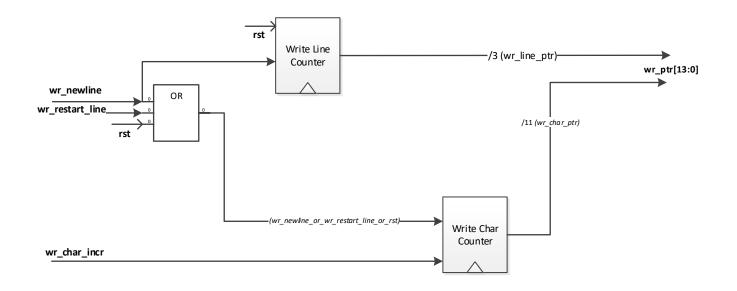
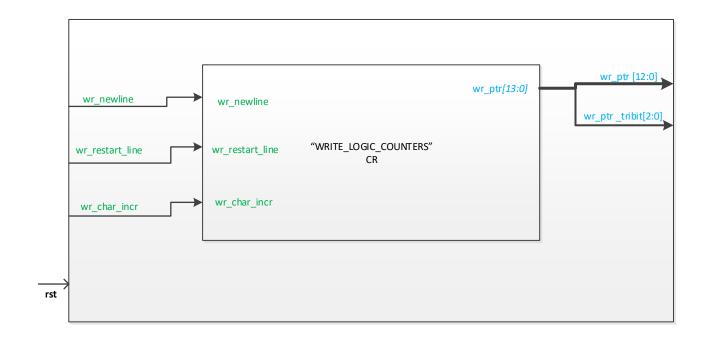
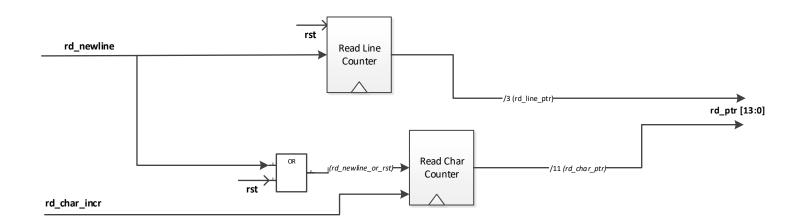
# "WRITE\_LOGIC\_COUNTERS" module



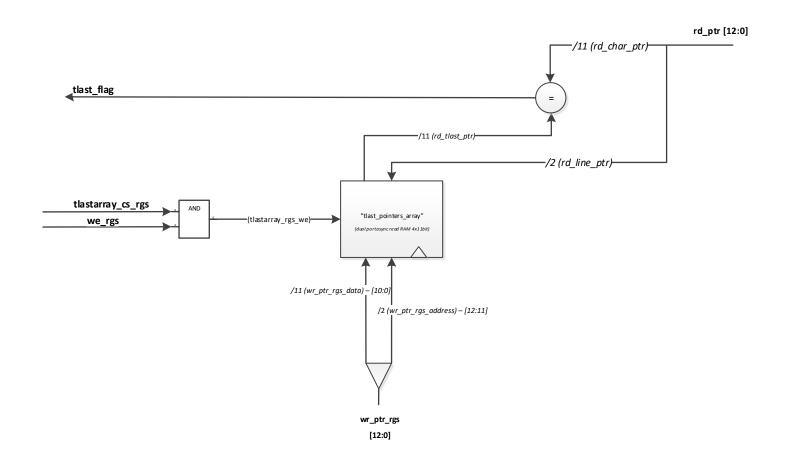
#### "WRITE\_LOGIC" module



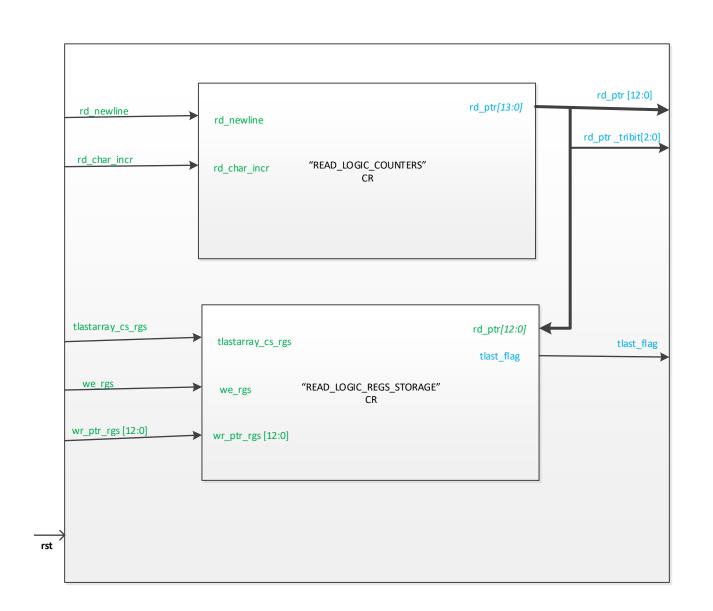
## "READ\_LOGIC\_COUNTERS" module



## "READ\_LOGIC\_REGS\_STORAGE" module

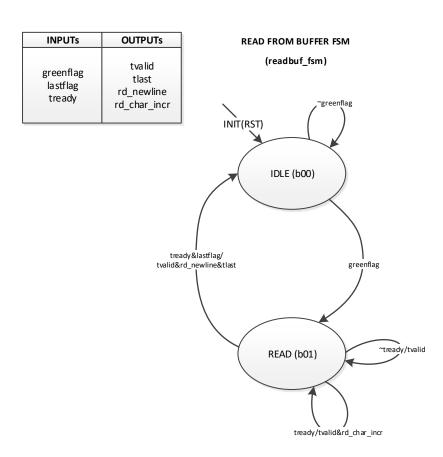


"READ\_LOGIC\_STORAGE" module



#### Write and Read Finite state machines

INPUTs	OUTPUTs	WRITE TO BUFFER FSM
greenflag tvalid tlast tuser	tready wren wr_newline wr_char_incr wr_restart_line	(writebuf_fsm)



### "L23\_buffer" module

