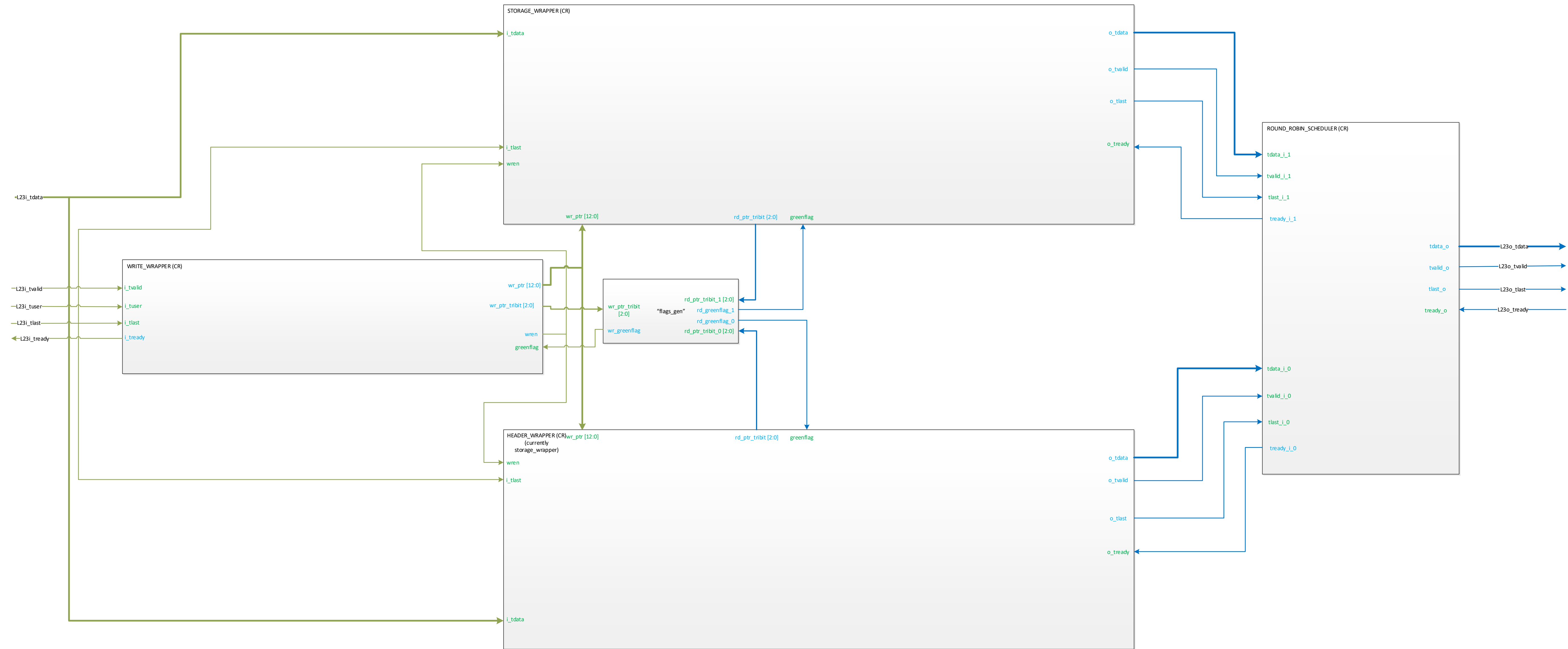
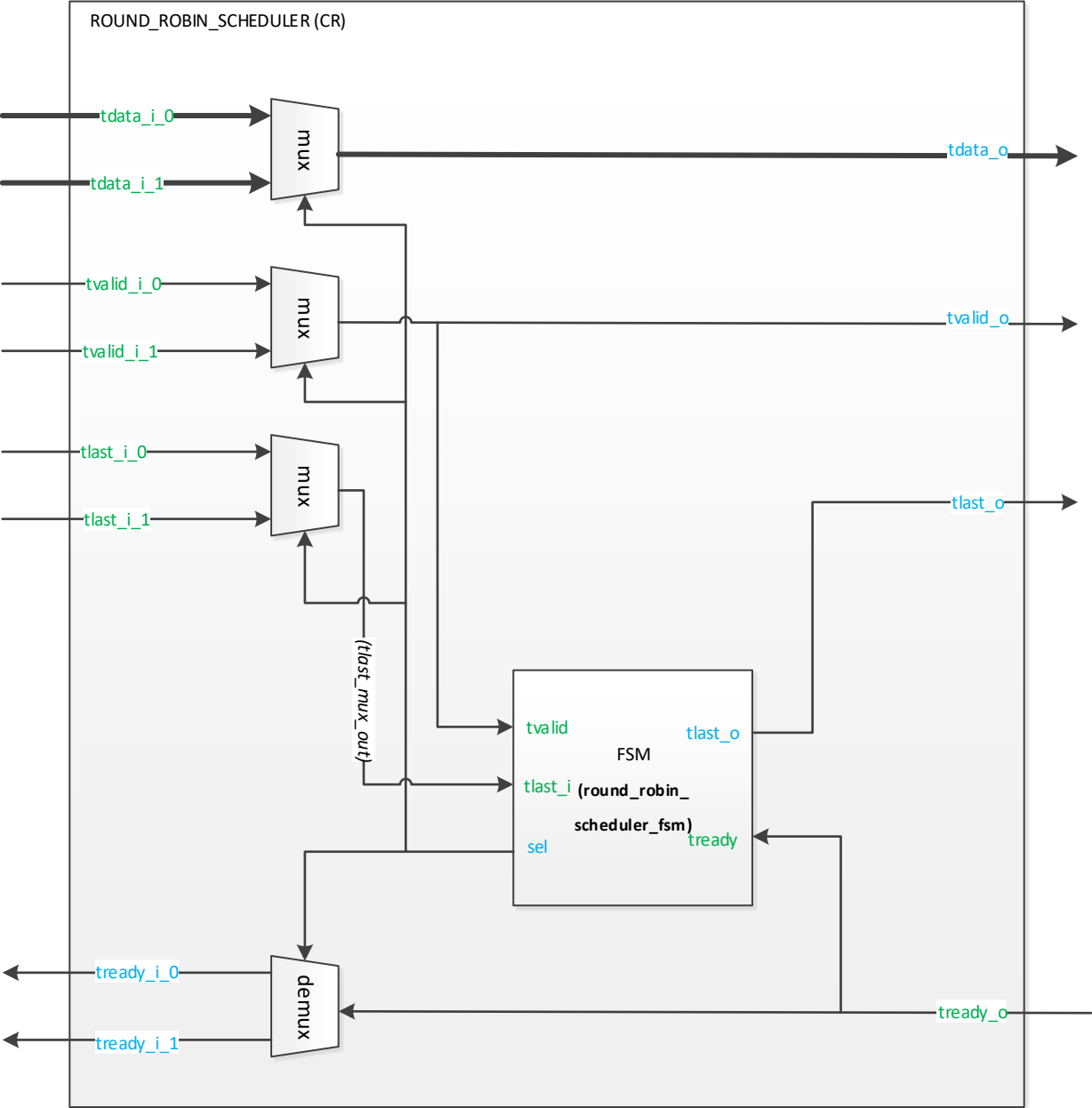


“L23_buffer” module

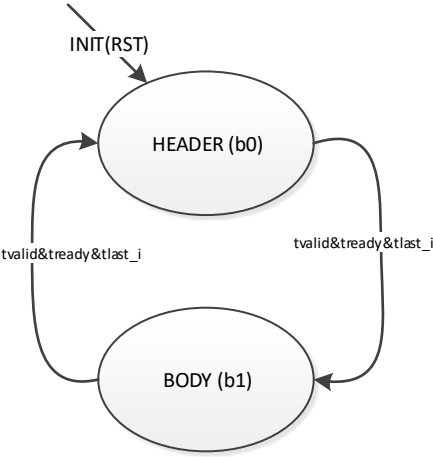


“ROUND_ROBIN_SCHEDULER”
module



FSM
(round_robin_scheduler_fsm)

INPUTs	OUTPUTs
tvalid tready tlast_i	sel tlast_o



SIGNAL ASSIGNS:
sel = ((state == BODY))
tlast_o = ((state == BODY) && (tlast_i))