

Call for Contribution

The 34th International Conference on VLSI Design (VLSID-2021) is the main event in India in the field of VLSI design, bringing together all the stakeholders that includes academia, industry, R&D houses and policy makers in the field of hardware and software system design, verification, test, EDA tools development, and manufacturing of electronic circuits. It is a flagship event of VLSI Society of India. The 20th International Conference on Embedded Systems is collocated with VLSID-2021. This edition is jointly organized by IIT Guwahati and IIT Roorkee, and is being hosted at IIT Guwahati from January 02 – 07, 2021. The six-day event consists of a conference with keynotes, plenary sessions, regular papers, tutorials, workshops, design contests, industry and Ph.D. forums. The conference is complemented by an exhibition showing the state-of-the-art in system design, software tools and techniques. The conference creates an excellent platform for the delegates to interact with all stakeholders in the electronic system design and automation domain. The theme for VLSID-2021 **“From the transistor to cyber-physical systems for solving societal challenges”** aims to emphasize the potential of the VLSI community to find multidisciplinary solutions for the societal and engineering challenges of our times.

Keeping in line with the conference theme, this year’s event will highlight the following focus areas.

- **Emerging Technologies**
- **Smart Systems for a Smarter World**
- **Innovation in Neuro Technologies**
- **Big Data Processing**
- **Sustainable Computing and Systems**
- **Energy-aware Systems**

The conference invites original unpublished contributions in the following tracks.

1. **Microarchitecture Design**
2. **Digital Integrated Circuits and Systems**
3. **Biomedical Circuits and Healthcare Systems**
4. **Electronic Design Automation**
5. **Security and Privacy**
6. **Test, Verification and Reliability**
7. **Emerging Technologies and new age Nano electronics**
8. **Analog and Mixed-Signal Design/RF Circuit Design**
9. **Embedded System Design and IoT**
10. **Artificial Intelligence and Machine Learning**
11. **Miscellaneous (Topics within the conference theme but not fitting to the above tracks)**



VLSI Design 2021 IIT Guwahati, Assam, INDIA
Jan 02 - 07, 2021
(Jointly Organized by IITG and IITR)

Important Dates

Submission deadline for special session proposals	:15 June 2020
Submission deadline for workshops	:15 June 2020
Notification for acceptance for special session proposals	:31 August 2020
Notification for acceptance for workshops	:31 July 2020
Submission deadline for tutorial proposals	:31 July 2020
Notification of acceptance for tutorials	:31 August 2020
Abstract submission deadline for regular papers	:15 June 2020
Full paper submission deadline for regular papers	:22 June 2020
Notification for acceptance for regular papers	:31 August 2020
Submission deadline for camera-ready paper	:30 September 2020
Submission deadline for Design Contest (DC)	:31 July 2020
Submission deadline for Navonmesha*	:31 July 2020
Submission deadline for User Design Track (UDT)	:30 September 2020
Submission deadline for Student Research Forum (SRF)	:30 September 2020
Submission deadline for Industry Forum (IF)	:30 September 2020
Acceptance notification for DC/Navonmesha	:31 August 2020
Acceptance notification for UDT/SRF/IF	:20 October 2020

*Navonmesha – Design competition and innovation challenge for students/faculty from junior colleges, general colleges and informal sectors

VLSID-2021 Committee

Vishwani D. Agarwal, Auburn University
Sukumar Nandi, IIT Guwahati
Sudeb Dasgupta, IIT Roorkee
Gaurav Trivedi, IIT Guwahati
Vijay Raghunathan, Purdue University
Sri Parameswaran, University of New South Wales
Preet Yadav, Wipro Ltd.
Devesh Dwivedi, Samsung Research

Steering Committee Chair
Executive Chair
General Co-Chair
General Co-Chair
Program Chair
Tutorial Chair
Workshop /Special Session Chair
Industry Forum Chair

Webpage: <http://embeddedandvlsidesignconference.org/> [Will be live on 01.05.2020]



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