

BASLINE	SIM INSTS	NUM CYCLES	CPI	IPC	ALU INST	MUL INST	DIV INST	LOAD INST	STORE INST	ICACHE MISS	DCACHE MISS	FETCH RATE	BRANCH RATE	BRANCH MISPREICTS	READ REQUESTS	WRITE REQUESTS	MEM ACCESS LATENCY
mergesort.c	1747054	1131404	0.647607	1.544147	1008976	0	0	533318	204760	0.000068	0.004179	3.648942	0.24949	20296	1456	861	32964.06
fft.c	3346993	1013348	0.302764	3.302906	2284211	73728	0	747446	241608	0.000192	0.011207	3.878836	0.061899	6685	2345	1584	28674.37
aes_sha.c	2716476	2418810	0.890422	1.123063	1591844	2993	3650	871965	246024	0.692341	0.007116	1.182145	0.014169	1028	125170	633	24766.77
AVERAGE	2603507.667	1521187.333	0.6135976667	1.990038667	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.230867	0.007500666667	2.903307667	0.1085193333	9336.333333	42990.33333	1026	28801.73333

PREFETCH	SIM INSTS	NUM CYCLES	CPI	IPC	ALU INST	MUL INST	DIV INST	LOAD INST	STORE INST	ICACHE MISS	DCACHE MISS	FETCH RATE	BRANCH RATE	BRANCH MISPREICTS	READ REQUESTS	WRITE REQUESTS	MEM ACCESS LATENCY
mergesort.c	1747054	1121684	0.642043	1.557528	1008976	0	0	533318	204760	0.000046	0.001888	3.672969	0.251511	20296	1641	878	29076.93
fft.c	3346993	1009005	0.301466	3.317122	2284211	73728	0	747446	241608	0.000106	0.006968	3.89657	0.062169	6685	2570	1626	25991.07
aes_sha.c	2716476	1440554	0.530302	1.885716	1591844	2993	3650	871965	246024	0.587422	0.009549	2.057416	0.024622	1028	142308	634	24416.19
AVERAGE	2603507.667	1190414.333	0.4912703333	2.253455333	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.195858	0.006135	3.208985	0.1127673333	9336.333333	48839.66667	1046	26494.73

BP	SIM INSTS	NUM CYCLES	CPI	IPC	ALU INST	MUL INST	DIV INST	LOAD INST	STORE INST	ICACHE MISS	DCACHE MISS	FETCH RATE	BRANCH RATE	BRANCH MISPREICTS	READ REQUESTS	WRITE REQUESTS	MEM ACCESS LATENCY
mergesort.c	1747054	897743	0.513861	1.946051	1008976	0	0	533318	204760	0.000089	0.004752	3.326074	0.249251	9897	1454	862	33072.64
fft.c	3346993	916583	0.273853	3.651598	2284211	73728	0	747446	241608	0.000208	0.011902	3.671235	0.056363	178	2343	1585	28159.79
aes_sha.c	2716476	2357907	0.868002	1.152071	1591844	2993	3650	871965	246024	0.75529	0.007474	1.16051	0.011367	119	125189	634	24771.83
AVERAGE	2603507.667	1390744.333	0.5519053333	2.249906667	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.2518623333	0.008042666667	2.719273	0.1056603333	3398	42995.33333	1027	28668.08667

FREQ	SIM INSTS	NUM CYCLES	CPI	IPC	ALU INST	MUL INST	DIV INST	LOAD INST	STORE INST	ICACHE MISS	DCACHE MISS	FETCH RATE	BRANCH RATE	BRANCH MISPREICTS	READ REQUESTS	WRITE REQUESTS	MEM ACCESS LATENCY
mergesort.c	1747054	1128427	0.645903	1.548221	1008976	0	0	533318	204760	0.000068	0.005114	3.655105	0.249991	20429	1454	860	45205.32
fft.c	3346993	1019369	0.304563	3.283397	2284211	73728	0	747446	241608	0.000192	0.012272	3.858162	0.061536	6682	2346	1584	30372.17
aes_sha.c	2716476	2898708	1.067084	0.937133	1591844	2993	3650	871965	246024	0.694425	0.007136	0.984726	0.011739	1028	125165	633	23476.56
AVERAGE	2603507.667	1682168	0.6725166667	1.922917	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.2315616667	0.008174	2.832664333	0.1077553333	9379.666667	42988.33333	1025.666667	33018.01667

AVG	SIM INSTS	NUM CYCLES	CPI	IPC	ALU INST	MUL INST	DIV INST	LOAD INST	STORE INST	ICACHE MISS	DCACHE MISS	FETCH RATE	BRANCH RATE	BRANCH MISPREICTS	READ REQUESTS	WRITE REQUESTS	MEM ACCESS LATENCY	FREQ
BASLINE	2603507.667	1521187.333	0.6135976667	1.990038667	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.230867	0.007500666667	2.903307667	0.1085193333	9336.333333	42990.33333	1026	28801.73333	100
PREFETCH	2603507.667	1190414.333	0.4912703333	2.253455333	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.195858	0.006135	3.208985	0.1127673333	9336.333333	48839.66667	1046	26494.73	100
BP	2603507.667	1390744.333	0.5519053333	2.249906667	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.2518623333	0.008042666667	2.719273	0.1056603333	3398	42995.33333	1027	28668.08667	100
FREQ	2603507.667	1682168	0.6725166667	1.922917	1628343.667	25573.66667	1216.666667	717576.3333	230797.3333	0.2315616667	0.008174	2.832664333	0.1077553333	9379.666667	42988.33333	1025.666667	33018.01667	200

NORM	NUM INSTS	NUM CYCLES	CPI	IPC	NUM ALU INST	NUM MUL INST	NUM DIV INST	NUM LOAD INST	NUM STORE INST	ICACHE MISS	DCACHE MISS	RAT	FETCH RATE	BRANCH RATE	BRANCH MISPREICTS	READ REQUESTS	WRITE REQUESTS	MEM ACCESS LATENCY	FREQ
BASLINE	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
PREFETCH	0.00%	-21.74%	-19.94%	13.24%	0.00%	0.00%	0.00%	0.00%	0.00%	-15.16%	-18.21%	10.53%	3.91%	0.00%	0.00%	13.61%	1.95%	-8.01%	0.00%
BRANCH PRI	0.00%	-8.58%	-10.05%	13.06%	0.00%	0.00%	0.00%	0.00%	0.00%	9.09%	7.23%	-6.34%	-2.63%	-63.60%	0.01%	0.10%	-0.46%	-0.46%	0.00%
2X FREQ	0.00%	10.58%	9.60%	-3.37%	0.00%	0.00%	0.00%	0.00%	0.00%	0.30%	8.98%	-2.43%	-0.70%	0.46%	0.00%	-0.03%	14.64%	0.00%	0.00%

