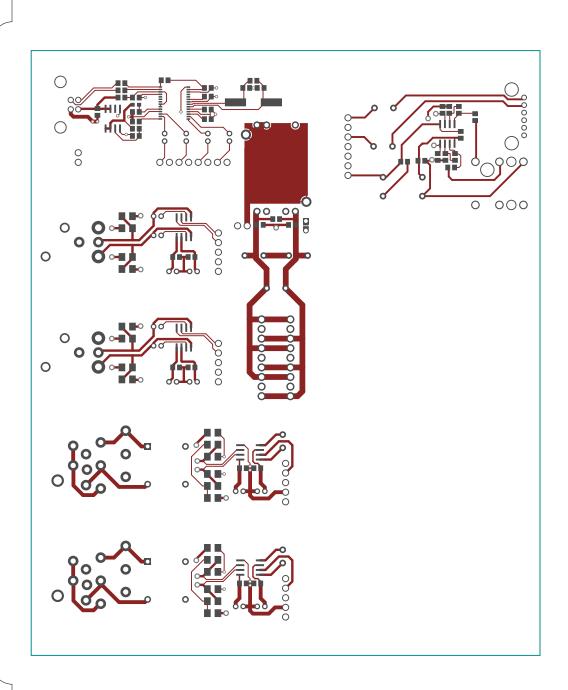
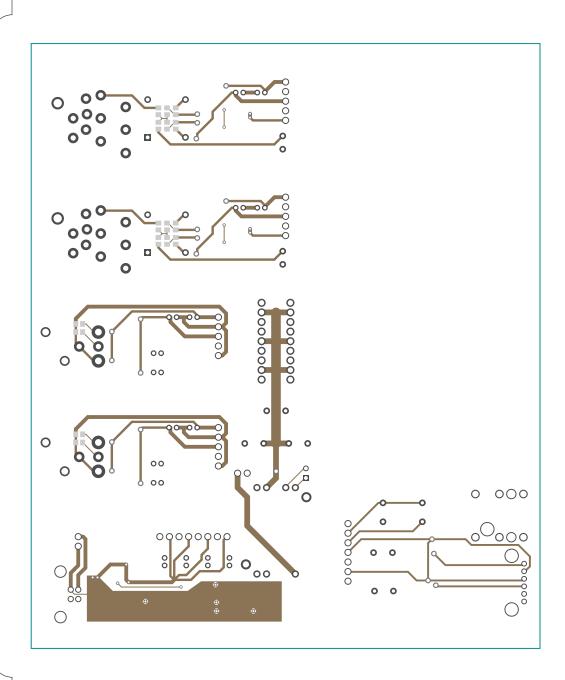
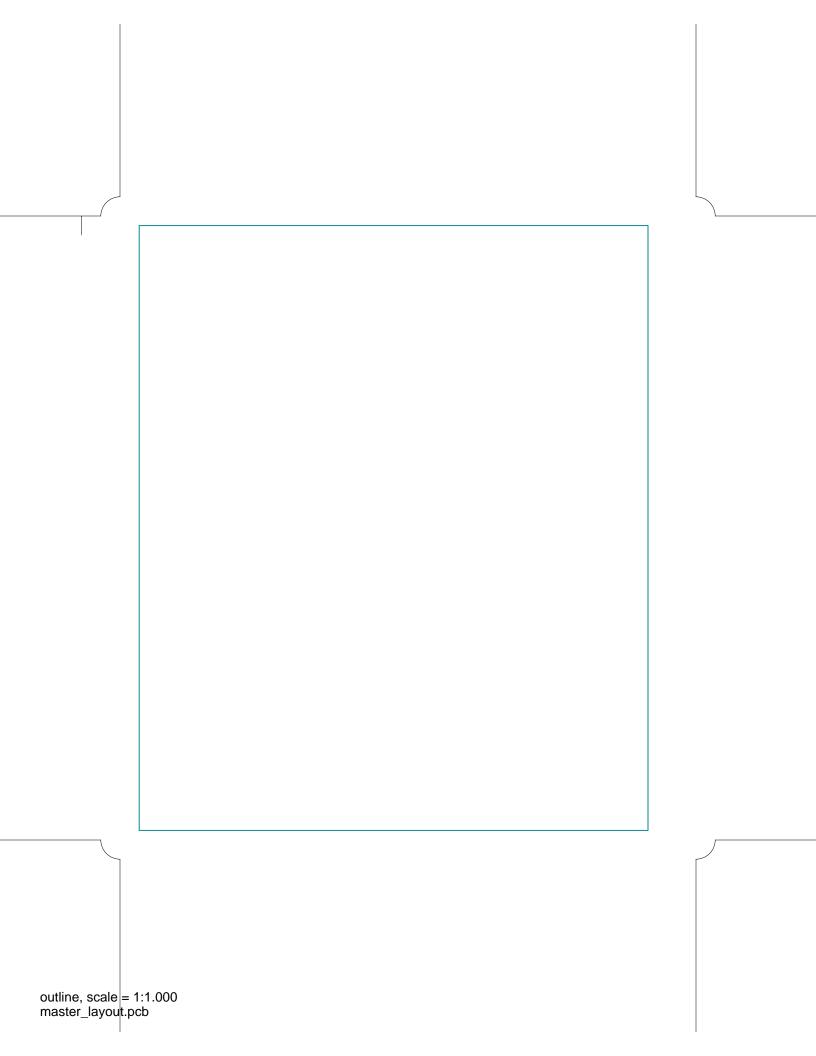
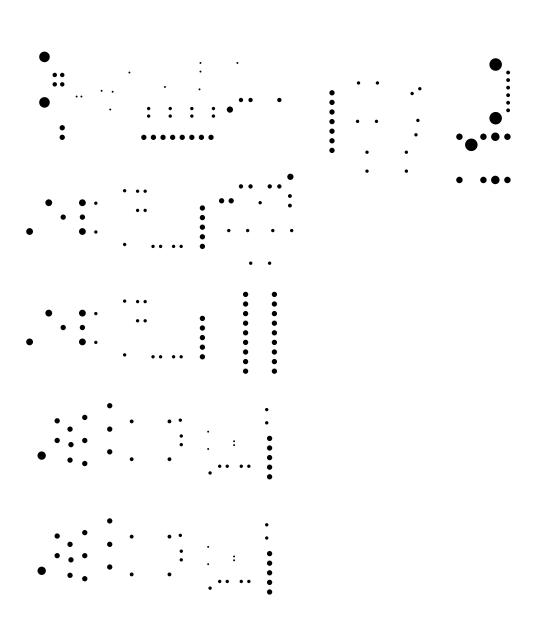
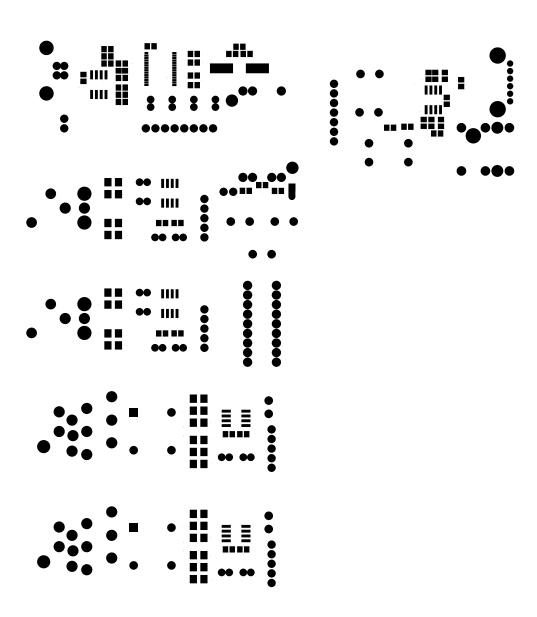
- 1. Table of Contents (This Page) Created on Fri Dec 7 13:11:26 2012
- 2. top
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- 5. plated-drill
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- 7. soldermask
- 8. topsilk
- 9. bottomsilk
- 10. toppaste
- 11. bottompaste
- 12. topassembly
- 13. bottomassembly
- 14. fab

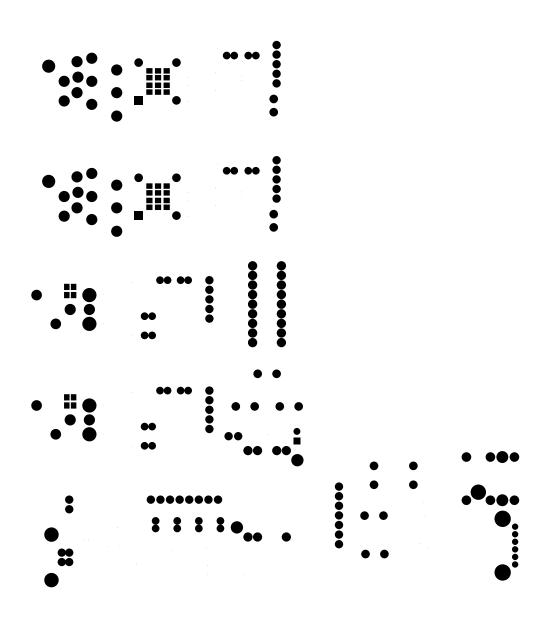


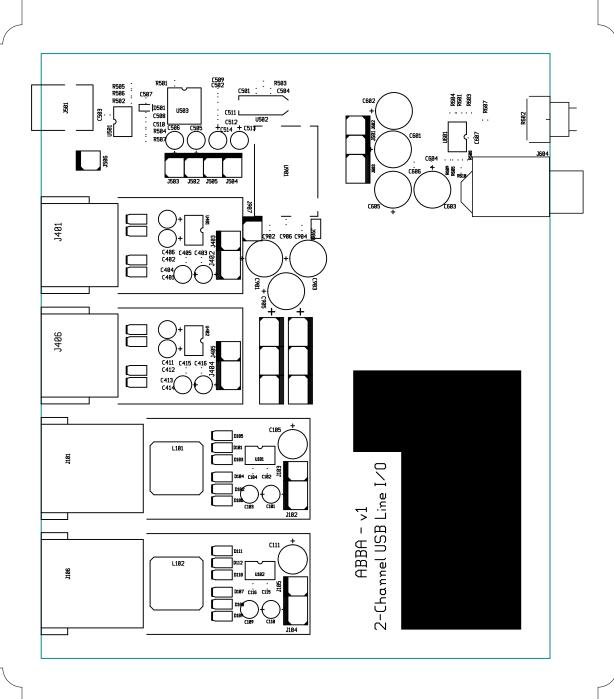






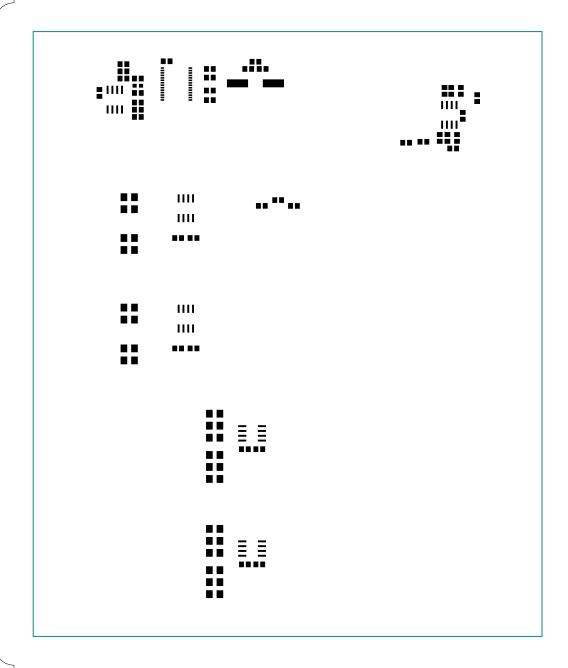


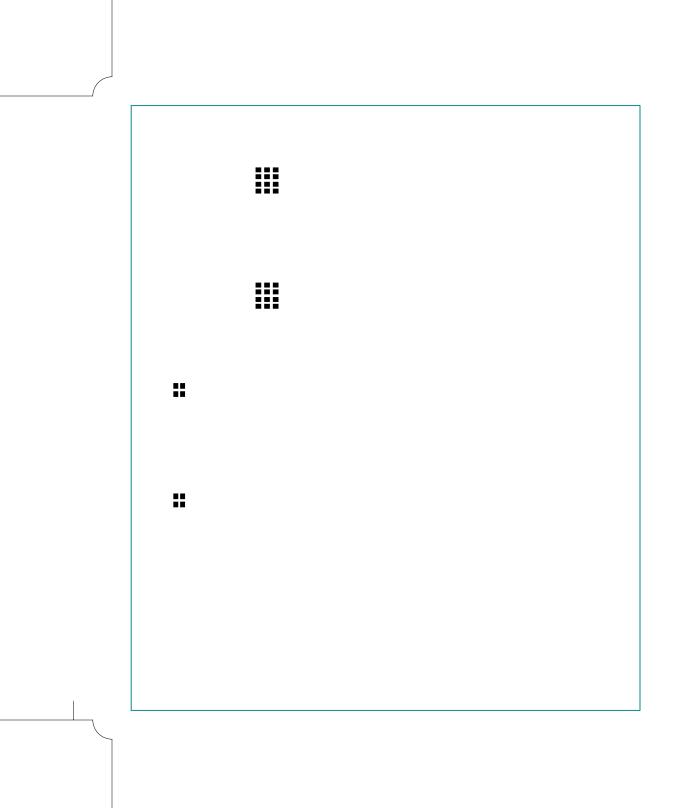




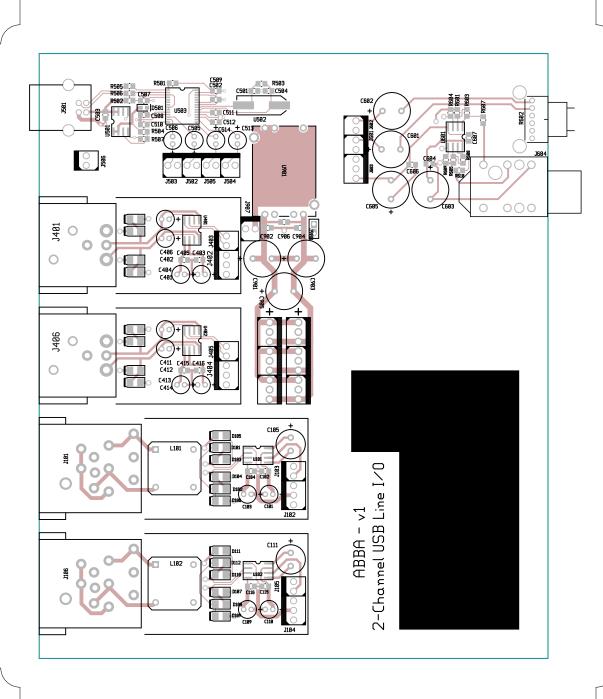
```
98 F1 R185
                      를 위 R182
C410 :
C409 :
C407 :
C408 :
```

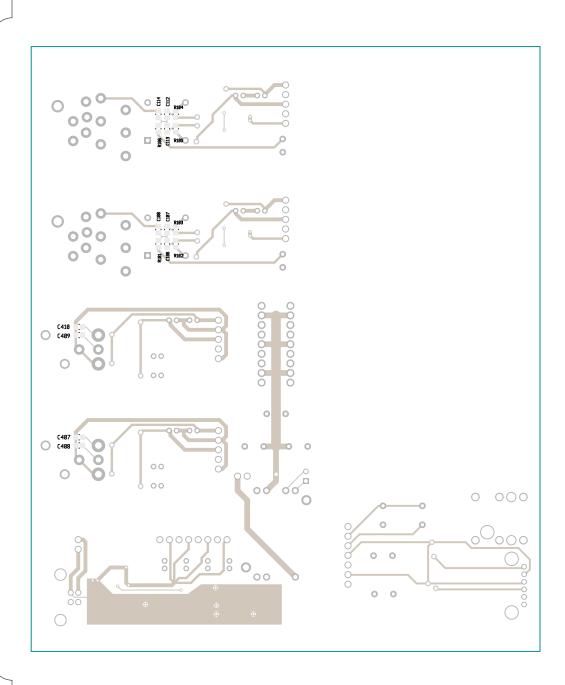
bottomsilk (mirrored), scale = 1:1.000 master_layout.pcb





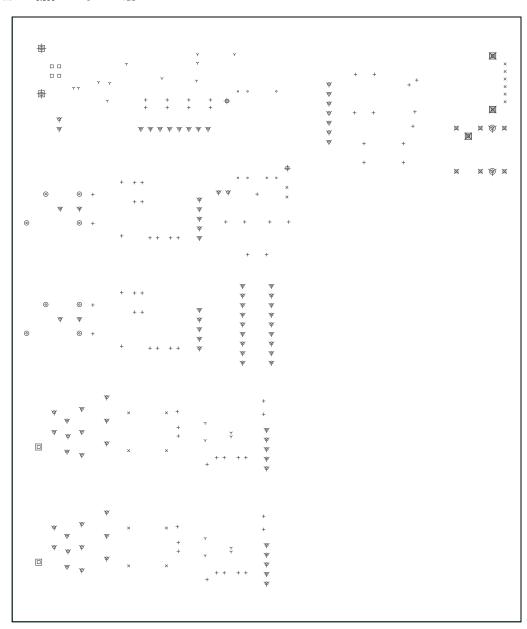
bottompaste (mirrored), scale = 1:1.000 master_layout.pcb





There are 13 different drill sizes used in this layout, 225 holes total

Symbol	Diam. (Inch)	Count	Plated?	
Y	0.020	19	YES	
+	0.035	71	YES	
×	0.040	16	YES	
	0.045	7	YES	
	0.046	4	YES	
Ψ	0.055	83	YES	
+	0.065	2	YES	
×	0.067	6	YES	
0	0.071	8	YES	
	0.087	2	YES	Title: (unknown) - Fabrication Drawing
\$	0.088	2	YES	Author: Dan White
<u></u>	0.110	2	YES	Date: Fri 07 Dec 2012 07:11:27 PM GMT UTC
	0.130	3	YES	



Board outline is the centerline of this path