

OE1_SOC

Clock-50

SW[4:0]

KEY0

HEX0

HEX
5

LEDR
[9:0]

HEX0

HEX5

Clock-
50

clock-divider

SW[9]

LFSR

Q[9:0]

SW[8:0]

CompuTion
CompuTist

CPU

CLK[15]

LEDR[9:6]

LEDR[4:0]

LEDR[5]

Normal
Lights

CLK[15]

Center
light

LEDR[9]
CLK[15]
LEDR[0]

Victory

SW[6]

OFF

userInput

CPU-Processed

KEY0

OFF

userInput

KEY0-Processed

SW[9]