```
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 2
         Date: 1/13/2023
 3
         Class: EE 371
 4
         Lab 6
 5
         Taken from Lab 1 and adapted for lab 6 task 1 */
 7
      // carSensor takes inputs from two sensors, a and b, and output "1" to either enter or exit
      for 1 clock cycle
      // whenever an entering or exiting vehicle is detected.
 9
     module carSensor (a, b, enter, exit, clk, reset);
         input logic a, b, clk, reset;
output logic enter; // car entering
10
11
         output logic exit; // car exiting
12
13
14
         enum {none, entering01, exiting01, entering11, exiting11, entering10, exiting10, idle} ps
15
16
         // Logic for next state
17
         always_comb begin
18
             case(ps)
19
                none: if (\sim a \& \sim b) ns = none;
                       else if (~a & b) ns = entering01;
else if (a & ~b) ns = exiting10;
20
21
22
23
                       else ns = idle;
                entering01: if (~a & ~b) ns = none;
24
25
                       else if (~a & b) ns = entering01;
                       else if (a & ~b) ns = idle;
26
27
                       else ns = entering11;
                exiting01: if (~a & ~b) ns = none;
28
29
30
31
32
33
34
35
                       else if (~a & b) ns = exiting01;
                       else if (a \& \sim b) ns = idle;
                       else ns = exiting11;
                entering11: if (~a & ~b) ns = none;
                       else if (\sima & b) ns = entering01;
                       else if (a & \simb) ns = entering10;
                else ns = entering11;
exiting11: if (~a & ~b) ns = none;
else if (~a & b) ns = exiting01;
else if (a & ~b) ns = exiting10;
36
37
38
                       else ns = exiting11;
                entering10: if (\sim a \& \sim \bar{b}) ns = none;
39
                       else if (\sima & b) ns = idle;
40
41
                       else if (a & \simb) ns = entering10;
42
43
                       else ns = entering11;
                exiting10: if (~a & ~b) ns = none;
else if (~a & b) ns = idle;
44
45
                       else if (a & ~b) ns = exiting10;
46
                       else ns = exiting11;
47
                idle: if (~a & ~b) ns = none;
48
                       else ns = idle;
49
             endcase
50
51
         end // always_comb
52
         //output logic for exiting: outputs 1 to exit when an exiting vehicle is detected.
53
         always_comb begin
54
             case(ps)
55
                exiting01: if (\sima & \simb) exit = 1'b1;
56
                              else exit = 1'b0;
57
                default: exit = 1'b0;
58
             endcase
59
         end // always_comb
60
61
         //DFFs
         always_ff @(posedge clk) begin
62
63
             if (reset)
64
                ps <= none;
65
             else
66
                ps <= ns
         end // always_ff
67
68
      endmodule // carSensor
69
      // carSensor_testbench tests all expected, unexpected, and edgecase behaviors
70
      module carSensor_testbench();
71
```

```
logic a, b, clk, reset, enter, exit;
  73
                logic CLOCK_50;
  74
  75
76
                carSensor dut (.a(b), .b(a), .c1k(CLOCK_50), .reset, .enter, .exit);
                // Setting up a clock.
  78
                 parameter CLOCK_PERIOD = 100;
  79
                 initial begin
  80
                      CLOCK_50 <= 0;
                      forever #(CLOCK_PERIOD/2) CLOCK_50 <= ~CLOCK_50; // toggle the clock forever</pre>
  81
  82
                end // initial
  83
  84
                initial begin
  85
                     // reset
                                                                                  repeat(3) @(posedge CLOCK_50);
  86
                      reset \leftarrow 1;
  87
  88
                      //enters
                                                    a <= 0; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 0; repeat(2) @(posedge CLOCK_50);</pre>
  89
                      reset \leftarrow 0;
  90
  91
  92
  93
  94
                      //exits
  95
                                                     a <= 0; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 1; repeat(2) @(posedge CLOCK_50);</pre>
  96
                                                     a <= 1; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
  97
  98
  99
                                                     a \leftarrow 0; b \leftarrow 0; repeat(2) @(posedge CLOCK_50);
100
                     // direction changes while entering
101
                                                     a <= 0; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);</pre>
102
103
                                                    a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 0; repeat(2) @(posedge CLOCK_50);</pre>
104
105
106
107
108
109
110
111
                     // direction changes while exiting
112
113
                                                     a \leftarrow 0; b \leftarrow 0; repeat(2) @(posedge CLOCK_50);
114
                                                     a \leftarrow 0; b \leftarrow 1; repeat(2) @(posedge CLOCK_50);
                                                     a \leftarrow 1; b \leftarrow 1; repeat(2) @(posedge CLOCK_50);
115
                                                     a \leftarrow 0; b \leftarrow 1; repeat(2) @(posedge CLOCK_50);
116
                                                     a <= 1; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
117
118
                                                     a <= 1; b <= 1; repeat(2) @(posedge CLOCK_50);
a <= 1; b <= 0; repeat(2) @(posedge CLOCK_50);
a <= 0; b <= 0; repeat(2) @(posedge CLOCK_50);
119
120
                      $stop;
                end // intial
123
124
125
           endmodule // carSensor_testbench
126
```