UNIVERSITY OF CALIFORNIA AT BERKELEY
College of Engineering
Department of Electrical Engineering and Computer Sciences

EE105 Lab Experiments

Prelab 6: Biasing Circuitry

Name:

Lab Section:

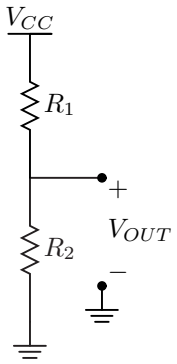


Figure 1: Resistive divider "voltage source"

1. Consider the resistor network shown in Figure 1. Let $V_{CC} = 10$ V, $R_1 = 9.35$ kΩ, and $R_2 = 650$ Ω. We can turn this resistive divider into a voltage source with an output resistance by taking its Thévenin equivalent. Find the open circuit output voltage and the output resistance of this voltage source.

2. Now, consider a BJT voltage source such as the one shown in Figure 2. Size resistor R_C to achieve an output voltage of 650 mV. Let $V_{CC} = 10$ V, $I_S = 26.03$ fA, and $V_T = 26$ mV. Ignore the Early effect for this calculation.

3. Find the output impedance and the power dissipated by this BJT voltage source. *Hint: Remember the definition of power, $P = IV$.*

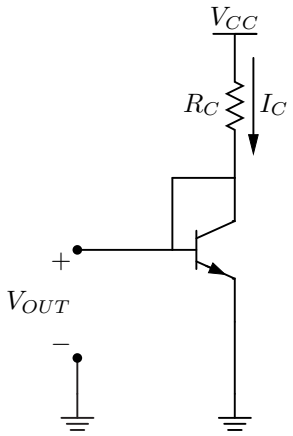


Figure 2: BJT voltage source

4. If we were to resize the resistors of our resistive divider (Figure 1) to achieve the same output impedance as the BJT voltage source given the same output voltage, what would be the values of the two resistors? How much power would it draw?

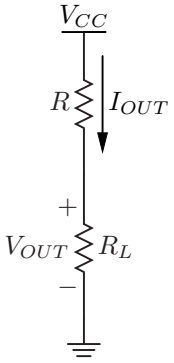
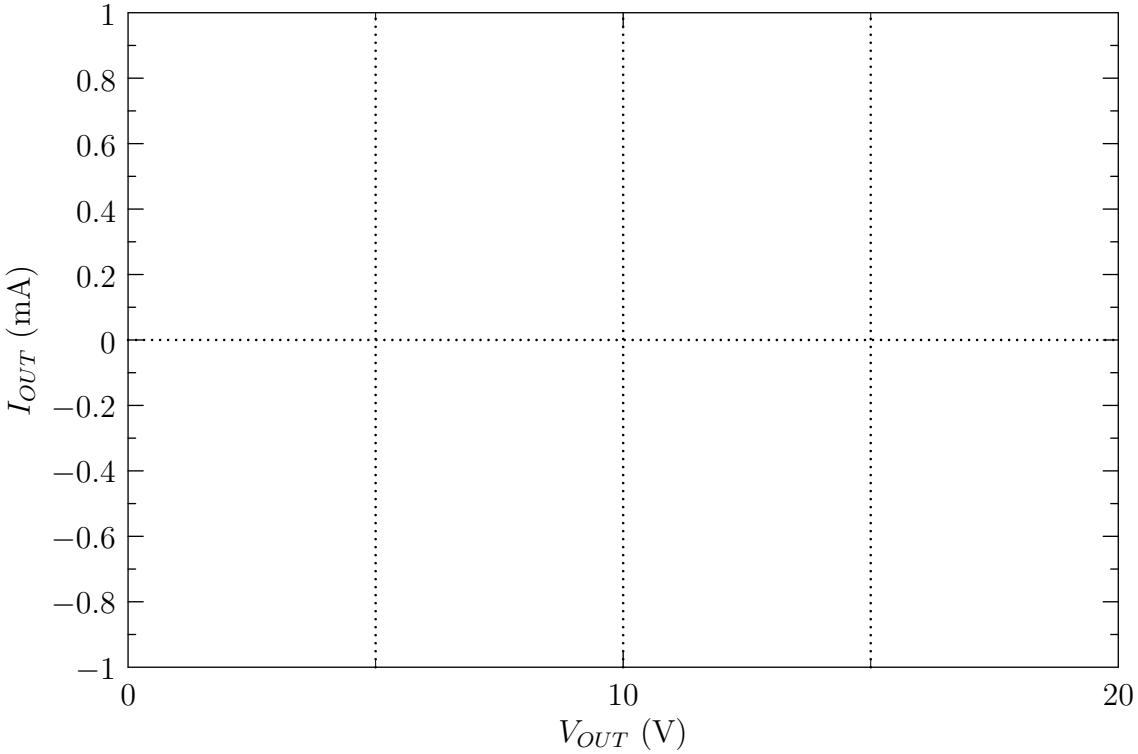


Figure 3: Resistor "current source"

5. Consider the circuit shown in Figure 3. Let $V_{CC} = 10\,\text{V}$ and $R = 10\,\text{k}\Omega$. Roughly sketch I_{OUT} vs. V_{OUT}.

6. Find the short circuit output current and the output impedance of the current source.



7. Is it possible to increase the output impedance without decreasing the output current and without changing V_{CC}? Explain.