

EE105 Lab Experiments

## Experiment 2: Electronic Test Equipment

### 1 Objective

This lab's purpose is to familiarize you with the lab equipment you will be using throughout the semester. Several devices will be the same as those used in EE40, but others may be unfamiliar (such as the HP4155 parameter analyzer). Learning to use the equipment now will spare you some headaches later in the semester.

### 2 Materials

Each lab workstation has the necessary test equipment and breadboards. You will also need the components listed in Table 1.

Component	Quantity
20 k $\Omega$ resistor	1
10 k $\Omega$ resistor	1
100 $\Omega$ resistor	1
0.01 $\mu$ F capacitor	1
1N4148 diode	1

Table 1: Components used in this lab

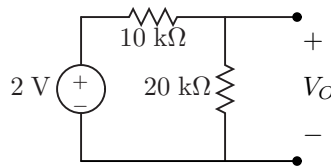
### 3 Procedure

You can generally trust measurement equipment (like the DMM and oscilloscope) to be more accurate than the output readings on generators (like the DC power supply and function generator). Several of these lab exercises involve measuring the output error on the generators using the DMM or oscilloscope. You will also try some basic circuit measurements.

When performing measurements or using multiple sources, be sure ground terminals are connected appropriately. Usually, you will create one ground node in your circuit and connect all device grounds to that node.

#### 3.1 DC Measurements (DMM and DC Power Supply)

1. Set the DC power supply to read +5 V on its panel, using the +6 V output. Measure the output with the digital multimeter (DMM). What is the error (in percent) between the +5 V setting on the +6 V output and the actual output? Record the error at +1 V, +5 V, and +9 V, using the +6 V output to generate +1 V and +5 V, and the +18 V output to generate +9 V.
2. Build the circuit in Figure 1 on your breadboard. Compare the voltage measured at  $V_O$  with a hand calculation using the ideal values given in the diagram. What is the error (in percent)? Other than measurement error and error in the voltage source, what might contribute to the deviation from the ideal calculation? *Hint: Measure the resistor values using the DMM.*



**Figure 1:** Resistor circuit to build

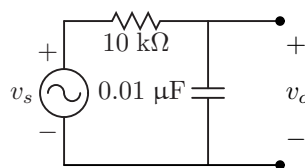
3. You are not supposed to connect the DMM in current mode to the terminals of a voltage source. Why not?
4. Measure the current through the 20 kΩ resistor and compare the value to your hand calculation.

### 3.2 AC Measurements (Oscilloscope and Function Generator)

The function generator panel often displays a different voltage amplitude than what is measured at its output because the panel setting assumes a certain load impedance (see the function generator tutorial for details).

Also, remember that the **AMP** parameter on the function generator sets the *peak-to-peak amplitude*. Note that in the labs, we will always use the word “amplitude” to refer to the maximum displacement from the offset (i.e. the traditional definition of amplitude) and “peak-to-peak” (or “peak-to-peak amplitude”) to refer to the peak-to-peak amplitude of a signal (i.e. double the amplitude).

1. Set the function generator to output a 1 kHz, 1  $V_{p-p}$  square wave according to the panel display. Measure the wave on the oscilloscope by directly connecting the function generator to an oscilloscope probe. Record the measured values for the wave’s frequency and  $V_{p-p}$  and calculate the error in the function generator panel in both frequency and voltage. Remember to take into account the source and load resistances (assume that the input resistance of the oscilloscope is much greater than the output resistance of the function generator).
2. What is the highest frequency sinusoid that the generator can produce with the 1  $V_{p-p}$  panel setting as above? What is the error in measured frequency vs. panel setting frequency?
3. What is the smallest  $V_{p-p}$  sinusoid that the generator can produce at 1 kHz? What is the error in measured  $V_{p-p}$  vs. the panel setting? Have the oscilloscope average over 64 measurements to compensate for system noise and allow better measurement accuracy. Without averaging, does the oscilloscope over- or under-measure the  $V_{p-p}$  value?
4. Set the generator to output a pulse with height 500 mV and a repetition frequency of 1 kHz. Compare period and frequency readings to those observed on the oscilloscope. What is the shortest pulse width that the function generator can produce at 500 mV height and 1 kHz frequency?
5. Construct the RC circuit in Figure 2.

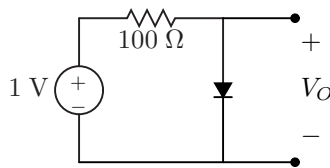


**Figure 2:** RC circuit to build

6. Set up  $v_s$  as a sine wave with frequency 1 kHz and delivered  $V_{p-p}$  of 1 V. Measure the input signal with the oscilloscope to verify the amplitude setting. *Hint: To achieve a delivered 1  $V_{p-p}$  amplitude, the panel setting should be 500 mV.*
7. Measure the amplitudes of the input and output waves. Calculate the value of  $|\frac{v_o}{v_s}|$  based on your oscilloscope measurements. This value is the magnitude of the transfer function evaluated at 1 kHz.

### 3.3 Parameter Analyzer Basics

1. Using the HP4155, plot a  $100\ \Omega$  resistor's I-V characteristic. What is the measured resistance according to your plot? Save the plot, since you will need to turn it in with your lab report.
2. Now plot the diode's I-V characteristic over an interesting interval. Again, save the plot, since you will need to turn it in with your lab report.
3. Using your empirical plots and load-line analysis, determine the value of  $V_O$  in the circuit shown in Figure 3. Verify your expected operating point by building the circuit and measuring the appropriate voltage with the DMM.
  - You can generate the load-line plot in Excel. To perform a mathematical operation, first enter the desired expression at the top of a new column preceeded by an equals sign. Then click on the lower-right corner of the new cell and drag downwards to fill in the new column. For example, if your data is in column *B* starting at cell *B1*, you can add a constant 2 to the column by first entering  $= B1 + 2$  in cell *C1*. Then click the lower-right corner of *C1* and drag downwards until column *C* is filled appropriately.



**Figure 3:** Diode circuit to build