## UNIVERSITY OF CALIFORNIA AT BERKELEY

## College of Engineering Department of Electrical Engineering and Computer Sciences

## EE105 Lab Experiments

## Prelab 10: Differential Amplifiers

Name:

Lab Section:

For this lab, assume all NPN transistors are identical 2N3904 BJTs and all PNP transistors are identical 2N3906 BJTs.

Component	$I_S$ (A)	$V_A$ (V)
2N3904 NPN BJT	$6.734 \times 10^{-15}$	74.03
2N3906 PNP BJT	$1.41 \times 10^{-15}$	18.7

Table 1: Transistor properties

A differential pair with a resistive load is shown in Figure 1. Use this circuit to answer the following questions. You can ignore base currents. Use the device parameters given in Table 1. Assume  $V_{CC} = 9 \text{ V}$ . Assume that the inputs are biased at 0 V DC.

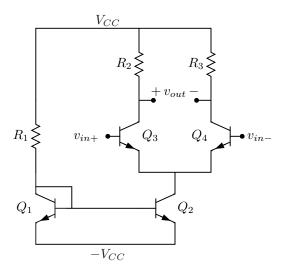


Figure 1: Differential pair with resistive load

1. What value of  $R_1$  would give  $I_{C1} = 2$  mA?

 $R_1 =$ 

2.	In lab, we have 500 $\Omega$ , 1 k $\Omega$ , 5.1 k $\Omega$ , and 10 k $\Omega$ resistors. Assuming we only wanted to use one resistor
	for $R_1$ , which value would give us $I_{C1}$ closest to 2 mA? What current would we get with the resistor
	vou chose? Use this value for $R_1$ for the remainder of the prelab.

$$R_1 = I_{C1} =$$

3. We'd like to bias the output half-way between 0 V and 9 V to achieve the maximum swing by picking  $R_2$  and  $R_3$  appropriately. Calculate a value for  $R_2$  and  $R_3$  that achieves an output bias of 4.5 V (assuming  $R_1$  is what you chose for the previous question). Out of the resistors listed previously, which should we use? What output bias does it achieve? Use this value (of the available resistors) for  $R_2$  and  $R_3$  for the remainder of the prelab.

(Exact) 
$$R_2 = R_3 =$$
 (Available)  $R_2 = R_3 =$   $V_{out,DC} =$ 

4. What is the output resistance of the circuit (be sure to include  $r_o$  of the BJTs)? Assume  $R_2$  and  $R_3$  are as you chose them in the previous question (of the available resistors).

$$R_{out} =$$

5. What is the differential-mode gain of the circuit?

$$A_{DM} =$$

6. What is the common-mode gain of the circuit?

$A_{CM}$	=		

7. What is the common-mode rejection ratio of the circuit? Use  $CMRR = \left| \frac{A_{DM}}{A_{CM}} \right|$  rather than the book's equation.

CMRR =