```
library IEEE;
use IEEE.std_logic_1164.all; -- defines std_logic types
use IEEE.STD_LOGIC_ARITH.ALL;
use IEEE.STD_LOGIC_UNSIGNED.ALL;
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```
POSSIBILITY OF SUCH DAMAGE.
use work.IDROMConst.all;
package PIN_DRINGx2_34 is
    constant ModuleID : ModuleIDType :=(
                          x"00",
                                                         x"01".
                                                                  WatchDogTimeAddr&PadT,
         (WatchDogTag,
                                   ClockLowTag,
        WatchDogNumRegs,
                                                WatchDogMPBitMask),
                                       x"00",
                                                         x"02",
                          x"00",
        (IOPortTag,
                                   ClockLowTag,
                                                                  PortAddr&PadT,
        IOPortNumRegs,
                                   x"00", IOPortMPBitMask),
                                                         x"08".
                          x"02",
                                   ClockLowTag,
                                                                  StepGenRateAddr&PadT,
         (StepGenTag,
                                       x"00",
                                                StepGenMPBitMask),
        StepGenNumRegs,
                          x"00",
         (PWMTag,
                                   ClockHighTag
                                                         x"02",
                                                                  PWMValAddr&PadT,
                                   x"00",
        PWMNumRegs,
                                           PWMMPBitMask),
                          x"00",
                                                         x"01".
                                   ClockLowTag
         (LEDTag
                                                                  LEDAddr&PadT,
                                   x"00", LEDMPBitMask),
        LEDNumRegs,
                          x"00",
                                   NullTag,
        (NullTag,
                                                                  NullAddr&PadT,
                                                         x"00",
                                            x"00000000")
                                   x"00",
        x"00",
                          x"00",
         (NullTag,
                                   NullTag,
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000")
                          x"00",
                                   NullTag,
                                                                  NullAddr&PadT,
        (NullTag,
                                                         x"00",
                                            x"00000000")
        x"00",
                                   x"00",
        (NullTag,
                          x"00",
                                   NullTag,
                                                         x"00",
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000"),
                          x"00",
                                                         x"00",
        (NullTag,
                                                                  NullAddr&PadT,
                                   NullTag,
                                   x"00",
                                            x"00000000")
        x"00",
                                                         x"00",
         (NullTag,
                          x"00",
                                   NullTag,
                                                                  NullAddr&PadT,
        x"00",
                                   x"00",
                                            x"00000000"),
                                   NullTag,
                          x"00",
                                                         x"00",
        (NullTag,
                                                                  NullAddr&PadT,
                                            x"00000000").
        x"00",
                                   x"00",
                                   NullTag,
        (NullTag,
                          x"00",
                                                                  NullAddr&PadT,
                                            x"00000000")
        x"00",
                                   x"00",
                          x"00",
                                   NullTag,
        (NullTag,
                                                                  NullAddr&PadT,
        x"00",
                                   x"00",
                                            x"00000000")
                          x"00",
                                   NullTag,
                                                         x"00",
        (NullTag,
                                                                  NullAddr&PadT,
                                   x"00",
                                            x"00000000")
        x"00",
        (NullTag,
                          x"00",
                                   NullTag,
                                                         x"00",
                                                                  NullAddr&PadT,
        x"00",
                                            x"00000000")
                                   x"00",
                          x"00",
                                   NullTag,
        (NullTag,
                                                         x"00",
                                                                  NullAddr&PadT,
                                            x"00000000")
        x"00",
                                   x"00",
                          x"00",
         (NullTag,
                                   NullTag,
                                                                  NullAddr&PadT,
        x"00",
                                   x"00",
                                            x"00000000"),
                                   NullTag,
        (NullTag,
                          x"00",
                                                         x"00",
                                                                  NullAddr&PadT,
                                            x"00000000"),
                                   x"00",
        x"00",
         (NullTag,
                                   NullTag,
                          x"00",
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000")
                          x"00",
                                   NullTag,
        (NullTag,
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000")
                                                         x"00",
        (NullTag,
                          x"00",
                                   NullTag,
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000")
        (NullTag,
                          x"00",
                                   NullTag,
                                                          x"00",
                                                                  NullAddr&PadT,
        x"00",
                                            x"00000000")
                                   x"00",
                          x"00",
        (NullTag,
                                   NullTag,
                                                         x"00",
                                                                  NullAddr&PadT,
                                            x"00000000")
        x"00",
                                   x"00",
                          x"00",
         (NullTag,
                                   NullTag,
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000"),
                          x"00",
                                   NullTag,
                                                         x"00",
        (NullTag,
                                                                  NullAddr&PadT,
                                            x"00000000")
                                   x"00",
        x"00",
                                   NullTag,
         (NullTag,
                          x"00",
                                                                  NullAddr&PadT,
                                   x"00",
        x"00",
                                            x"00000000")
        (NullTag,
                          x"00",
                                   NullTag,
                                                                  NullAddr&PadT,
        x"00",
                                   x"00",
                                            x"00000000")
        (NullTag,
                          x"00",
                                   NullTag,
                                                         x"00",
                                                                  NullAddr&PadT,
```

34 pin 5I25

```
(NullTag,
                             NullTag,
                                                  x"00",
                     x"00",
                                                          NullAddr&PadT,
                                     x"00000000").
    x"00",
                             x"00",
    (NullTag,
                     x"00",
                             NullTag,
                                                           NullAddr&PadT,
                             x"00",
                                     x"00000000").
    x"00",
                     x"00",
    (NullTag,
                             NullTag,
                                                  x"00",
                                                          NullAddr&PadT,
                                      x"00000000")
    x"00",
                             x"00",
                                                  x"00",
    (NullTag,
                     x"00",
                             NullTag,
                                                           NullAddr&PadT,
                             x"00",
                                     x"00000000")
    x"00",
    );
constant PinDesc : PinDescType :=(
Base func sec unit sec func
                                  sec pin
                                                           -- external DB25
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 00
                                                                       PIN 1
                                                                                just GPI0
    IOPortTag & x"00" & PWMTag & PWMAOutPin,
                                                              I/0 01
                                                                       PIN 14
                                                                                PWM
    IOPortTag & x"00" & StepGenTag & StepGenStepPin,
                                                              I/0 02
                                                                       PIN 2
                                                                                 X Step
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 03
                                                                       PIN 15
                                                                                just GPIO
    IOPortTag & x"00" & StepGenTag & StepGenDirPin,
                                                           -- I/O 04
                                                                       PIN 3
                                                                                X Dir
    IOPortTag & x"00" & NullTag & NullPin,
                                                             I/O 05
                                                                       PIN 16
                                                                                iust GPIO
    IOPortTag & x"01" & StepGenTag & StepGenStepPin,
                                                           -- I/O 06
                                                                       PIN 4
                                                                                Y Step
    IOPortTag & x"00" & NullTag & NullPin,
                                                              I/0 07
                                                                       PIN 17
                                                                                just GPI0
    IOPortTag & x"01" & StepGenTag & StepGenDirPin,
                                                              I/0 08
                                                                       PIN 5
                                                                                Y Dir
                                                                                Z Step
    IOPortTag & x"02" & StepGenTag & StepGenStepPin,
                                                              I/0 09
                                                                       PIN 6
    IOPortTag & x"02" & StepGenTag & StepGenDirPin,
                                                                       PIN 7
                                                           -- I/O 10
                                                                                Z Dir
    IOPortTag & x"03" & StepGenTag & StepGenStepPin,
                                                           -- I/O 11
                                                                       PIN 8
                                                                                A Step
    IOPortTag & x"03" & StepGenTag & StepGenDirPin,
                                                             I/0 12
                                                                       PIN 9
                                                                                A Dir
    IOPortTag & x"00" & NullTag & NullPin,
                                                             I/0 13
                                                                       PIN 10
                                                                                just GPI0
    IOPortTag & x"00" & NullTag & NullPin,
                                                             I/0 14
                                                                       PIN 11
                                                                                just GPIO
    IOPortTag & x"00" & NullTag & NullPin,
                                                              I/0 15
                                                                       PIN 12
                                                                                just GPIO
    IOPortTag & x"00" & NullTag & NullPin,
                                                              I/0 16
                                                                       PIN 13
                                                                                iust GPIO
                                                           -- 26 HDR
                                                                       -- IDC DB25
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 17
                                                                       PIN 1
                                                                                PIN 1
                                                                                        just
    GPI0
    IOPortTag & x"01" & PWMTag & PWMAOutPin,
                                                           -- I/O 18
                                                                       PIN 2
                                                                                PIN 14
                                                                                        PWM
    IOPortTag & x"04" & StepGenTag & StepGenStepPin,
                                                           -- I/O 19
                                                                       PIN 3
                                                                                PIN 2
                                                                                        X2 Step
    IOPortTag & x"00" & NullTag & NullPin,
                                                              I/0 20
                                                                       PIN 4
                                                                                PIN 15
                                                                                        just
    GPI0
    IOPortTag & x"04" & StepGenTag & StepGenDirPin,
                                                           -- I/O 21
                                                                       PIN 5
                                                                                PIN 3
                                                                                        X2 Dir
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 22
                                                                       PIN 6
                                                                                PIN 16
                                                                                        just
    IOPortTag & x"05" & StepGenTag & StepGenStepPin,
                                                                                PIN 4
                                                           -- I/O 23
                                                                       PIN 7
                                                                                        Y2 Step
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 24
                                                                       PIN 8
                                                                                PIN 17
                                                                                        just
   GPI0
    IOPortTag & x"05" & StepGenTag & StepGenDirPin,
                                                           -- I/O 25
                                                                       PIN 9
                                                                                PIN 5
                                                                                        Y2 Dir
    IOPortTag & x"06" & StepGenTag & StepGenStepPin,
                                                                       PIN 11
                                                                                PIN 6
                                                                                        Z2 Step
                                                           -- I/O 26
    IOPortTag & x"06" & StepGenTag & StepGenDirPin,
                                                           -- I/O 27
                                                                       PIN 13
                                                                                PIN 7
                                                                                        Z2 Dir
    IOPortTag & x"07" & StepGenTag & StepGenStepPin,
                                                           -- I/O 28
                                                                       PIN 15
                                                                                PTN 8
                                                                                        A2 Step
    IOPortTag & x"07" & StepGenTag & StepGenDirPin,
                                                          -- I/O 29
                                                                       PTN 17
                                                                                PTN 9
                                                                                        A2 Dir
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 30
                                                                       PIN 19
                                                                                PIN 10
                                                                                        just
   GPI0
    IOPortTag & x"00" & NullTag & NullPin,
                                                                       PIN 21
                                                           -- I/O 31
                                                                                PIN 11
                                                                                        just
    GPI0
    IOPortTag & x"00" & NullTag & NullPin,
                                                           -- I/O 32
                                                                       PIN 23
                                                                                PIN 12
                                                                                        just
    IOPortTag & x"00" & NullTag & NullPin,
                                                          -- I/O 33
                                                                       PIN 25
                                                                                PIN 13
                                                                                        just
    emptypin,emptypin,emptypin,emptypin,emptypin,emptypin,emptypin,emptypin, -- added for
```

emptypin,emptypin,emptypin,emptypin,emptypin,emptypin,emptypin,emptypin, -- added for

emptypin,emptypin,emptypin,emptypin,emptypin,

```
emptypin, emptypin, emptypin, emptypin, emptypin, emptypin, emptypin, emptypin, -- added for
IDROM v3
emptypin, emptypin,
```

end package PIN\_DRINGx2\_34;