

Trabalho

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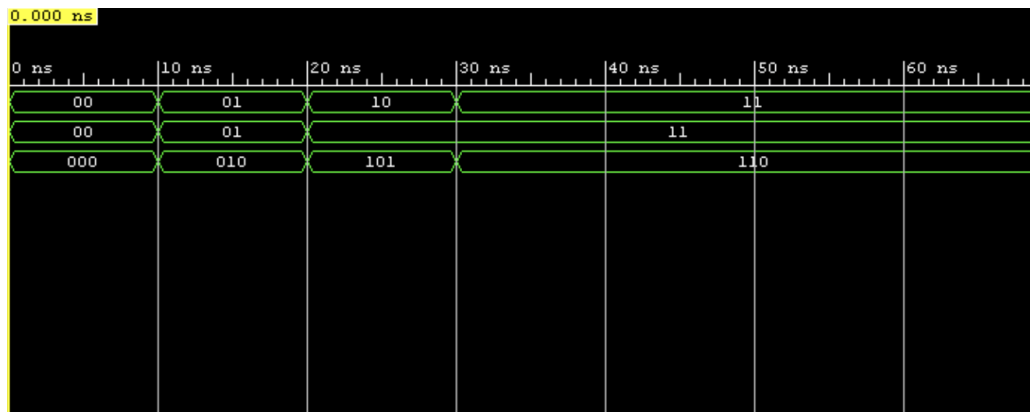


Figura 1: Simulação do Somador de 2 bits.

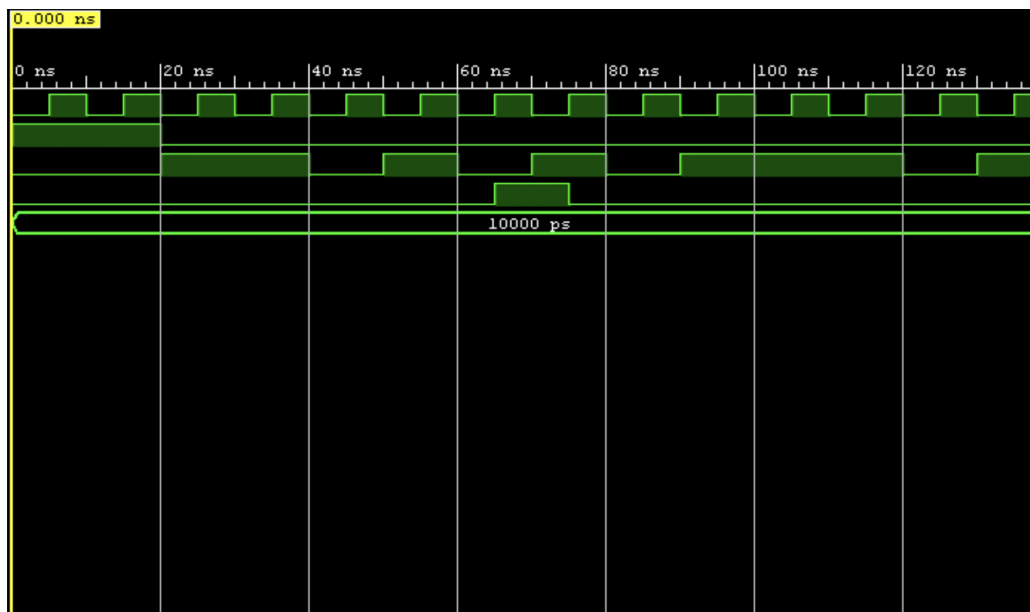


Figura 2: Simulação do Detector.

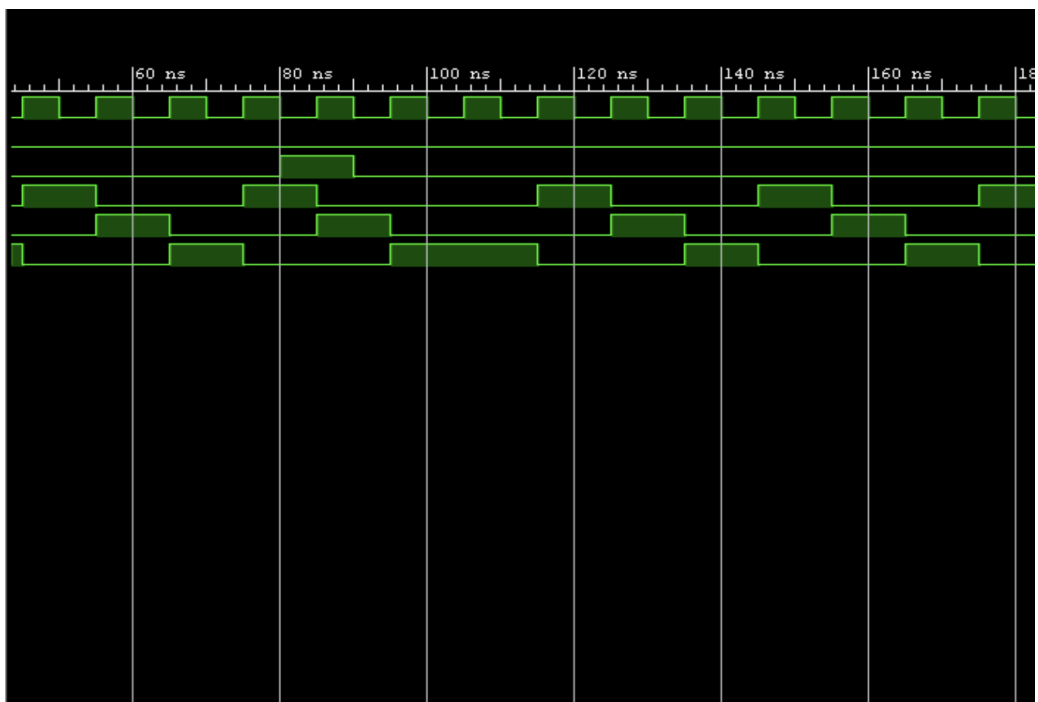


Figura 3: Simulação do Semáforo.