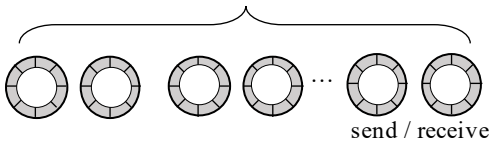


NVMe queues



data

(1) *nvm\_poller*

to\_ftl / to\_poller

(2) *ftl\_thread*

DRAM

mapping table

write buffer

data

(3) *ftl\_flush\_thread*

NAND  
Pkg

NAND  
Pkg

...

NAND  
Pkg

Flash memory (backed by DRAM)