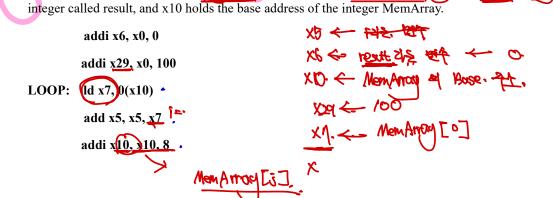
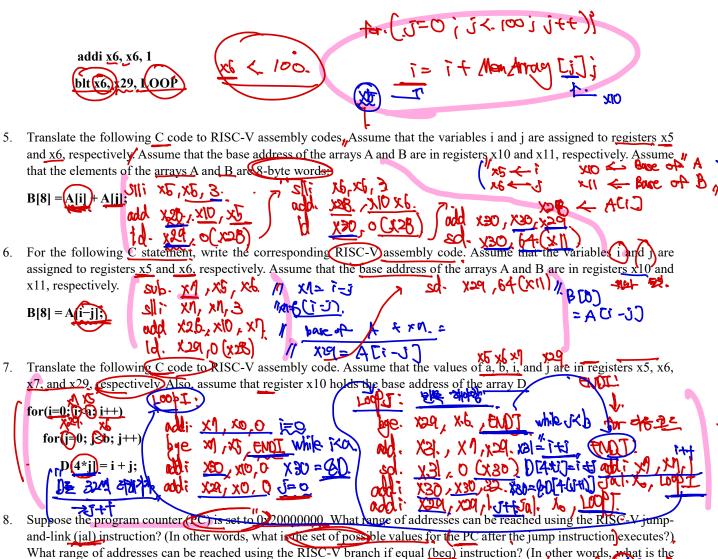


4. Translate the following loop into C ode. Assume that the C-level integer is held in register x3 x6 holds the C-level





set of possible values for the PC after the branch instruction executes?) by the homekate (2041-1-2) b

0000000 0000000 00000 1010/010 cologol lowlow or x7, x7, x6 0111-1000 0010 1100 0100 1000 COO1 0100 01010110 00110100 010101100 01 00 1000 alillioi oloilioi dellillo MILLIO 01101018 0100 1000 00/0/00 11111000

10. Translate the following C code into RISC-V assembly language. You can assume the registers that correspond to the C variables.

temp= V[k]; V[k] = V[k+1]; V[k+1] = temp; V[k+1] = temp;

11. Translate function f into RISC-V assembly language, Assume the function declaration for g is long long in glong long int a, long long int b). The code for function f is as follows:

