

# **Common Concurrency Problems**





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### What Types of Bugs Exist

- Researchers have spent a great deal of time and effort looking into concurrency bugs over many years
  - What types of concurrency bugs manifest in complex, concurrent programs?
  - A study focuses on four major and important open-source applications

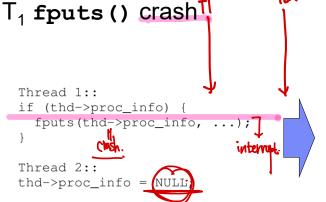
,	<b>Application</b>	What it does	Non-Deadlock	Deadlock
/	MySQL	Database Server	14	9
l	Apache	Web Server	13	4
	Mozilla	Web Browser	41	16
	<b>OpenOffice</b>	Office Suite	6	2
\	Total		74	31
-\	•			

- There were 105 total bugs, most of which were not deadlock (74); remaining 31 were deadlock bugs
- We now dive into these different classes of bugs (non-deadlock, deadlock) a bit more deeply

### Non-Deadlock Bugs: Atomicity Violation

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- Non-deadlock bugs make up a majority of concurrency bugs
  - Two major types: atomicity violation bugs and order violation bugs.
- Consider an example that exposes atomicity violation bug
  - Two different threads access the field thd->proc\_info
  - T₁ performs if and it is not NULL and then interrupted → T₂ sets it NULL →



```
pthread_mutex_t proc_info_lock = PTHREAD_MUTEX_INITIALIZER;

Thread 1::
    pthread_mutex_lock(&proc_info_lock);

if (thd->proc_info) {
    fputs(thd->proc_info, ...);
}

pthread_mutex_unlock(&proc_info_lock);

Thread 2::
    pthread_mutex_lock(&proc_info_lock);

thd->proc_info = NULL;

pthread_mutex_unlock(&proc_info_lock);

**Thread 2::
    pthread_mutex_lock(&proc_info_lock);

**Thread_mutex_lock(&proc_info_lock);

**Thread_mutex_lock(&
```

- The formal definition of an atomicity violation is that the desired serializability among multiple memory accesses is violated
- The solution is to simply add locks around the shared-variable references → 地址 / 域 独立 动.

### Non-Deadlock Bugs: Order Violation Bugs

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- Consider a simple example that exposes the order violation
  - T<sub>2</sub> seems to assume that mThread has already been initialized
  - T<sub>2</sub> runs immediately once created,
     mThread will not be set when it is accessed within mMain() in T<sub>2</sub>
     → crash

```
Thread 1::
void init() {
   mThread = PR_CreateThread(mMain, ...);
}

Thread 2::
void mMain(...) {
   mState = mThread->State;
}
```

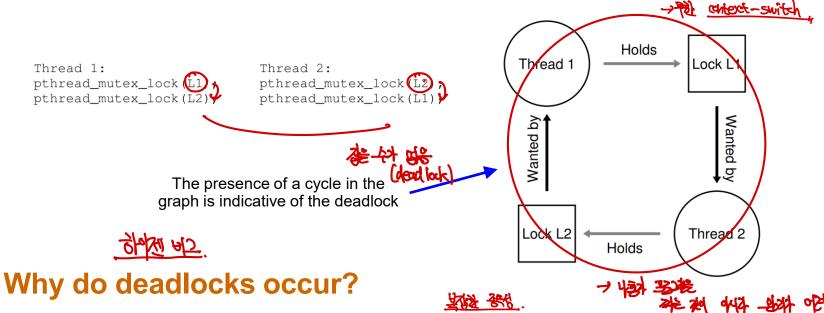
```
int mtInit
                  State voviable to condition voriable.
Thread 1::
void init() {
   mThread = PR CreateThread(mMain, ...);
   // signal that the thread has been created...
   pthread_mutex_lock(&mtLock);
  pthread_cond_signal(&mtCond
   pthread mutex unlock (&mtLock);
Thread 2::
void mMain(...) {
    // wait for the thread to be initialized...
    pthread_mutex_lock(amtLock);
    while (mtInit == 0)
        pthread cond wait (&mtCond, &mtLock);
    pthread_mutex_unlock(&mtLock
    mState = mThread->State;
```

- The formal definition of an order violation is that the desired order between two (groups of) memory accesses is flipped
- The fix to this type of bug is generally to enforce ordering
  - Using condition variables is an easy and robust way to add the synchronization

### Deadlock Bugs → 해趣状 与 水環

 A classic problem that arises in many concurrent systems with complex locking protocols is known as deadlock

Deadlock occurs, for example, when T<sub>1</sub> holds a lock L1 and waiting for another one L2; unfortunately, T<sub>2</sub> that holds lock L2 is waiting for L1 to be released



One reason is that In large code bases, complex dependencies arise between components

- Another reason is due to the nature of capsulation, which hides the details of implementation

### **Conditions for Deadlock**

Four conditions need to hold for a deadlock to occur

Condition	Description	
Mutual exclusion	Threads claim exclusive control of resources that they require	
P Âold-and-wait。 →华峰 松 地 地 科	Threads hold resources allocated to them (e.g. locks that they already acquired) while waiting for additional resources (e.g.	
③ No preemption → পৃক্তেই দক্ষিণ কাম ২	Resources (e.g., locks) cannot be forcibly removed from threads that are holding them	
Circular wait  → 洋 chaining	There exists a circular chain of threads such that each thread holds one or more resources (e.g., locks) that are being requested by the next thread in the chain	
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#### We first explore techniques to prevent deadlock

The prevention is one approach to handling the deadlock problem

## Prevention: Circular Wait & Hold-and-Wait

- The most straightforward way to prevent the circular wait is to provide a total ordering on lock acquisition
  - If there are only two locks in the system (L1 and L2), you can prevent deadlock by always acquiring L1 before L2
  - Such strict ordering ensures that no cyclical wait arises; hence, no deadlock
  - Total lock ordering may be difficult to achieve; thus, a partial ordering can be a useful way to structure lock acquisition so as to avoid deadlock
- The hold-and-wait requirement for deadlock can be avoided by acquiring all locks at once, atomically
  - By first grabbing the lock prevention, it guarantees that no thread switch can occur in the midst of lock acquisition and thus deadlock can be avoided

```
pthread_mutex_lock(prevention); // begin acquisition

pthread_mutex_lock(L1);

pthread_mutex_lock(L2);) | /dx, 12 | dx | Abomic | All | Al
```

 Note that the solution is problematic for a number of reasons; encapsulation works against us (know the detail locks), likely to decrease concurrency

### **Prevention: No Preemption**

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- Because we generally view locks as held until unlock is called, multiple lock acquisition often gets us into trouble
  - Many thread libraries provide a more flexible set of interfaces to help avoid this situation, such as pthread mutex trylock()

```
top:

pthread_mutex_lock(L1);

if (pthread_mutex_trylock(L2) != 0) {

pthread_mutex_unlock(L1);

goto top;

}

top:

pthread_mutex_lock(L2);

if (pthread_mutex_trylock(L1) != 0) {

pthread_mutex_unlock(L2);

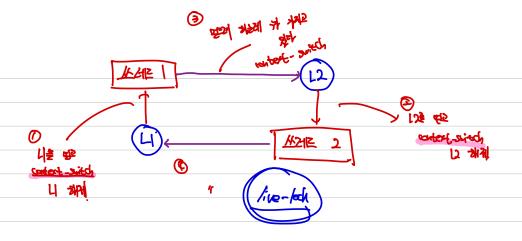
goto top;

}

goto top;

}
```

- Note that another thread grabbing the locks in other order (L2→L1) still works
- One new problem does arise, however: livelock
  - It is possible (though perhaps unlikely) that two threads can both be repeatedly attempting this sequence and repeatedly failing to acquire both locks
  - This is not deadlock because both threads keep running; but no progress.



#### **Prevention: Mutual Exclusion**

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- The final prevention technique would be to avoid the need for mutual exclusion at all
  - The idea behind these lock-free (and related wait-free) approaches here is simple: using powerful hardware instructions
  - This code repeatedly tries to update the value by using compare-and-swap ()

```
int CompareAndSwap(int *address, int expected, int new) {
   if (*address == expected) {
      *address = new;
      return 1; // success
   }
   return 0; // failure
}

void AtomicIncrement(int *value, int amount) {
      do {
      int old = *value;
    } while (CompareAndSwap(value, old, old + amount) == 0);
}

void AtomicIncrement(int *value, int amount) {
      wheth
      int old = *value;
    }
} while (CompareAndSwap(value, old, old + amount) == 0);
}
```

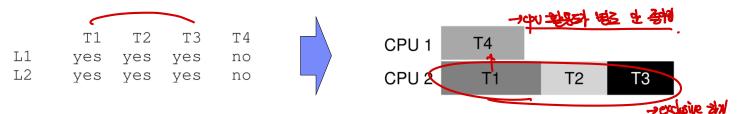
- Let's consider a more complex example: list insertion
  - To avoid a race condition, a lock can be used for the critical section
- Instead, this insertion can lock-free using compare-and-swap () void insert(int value) { d Mesert (int value) { node\_t \*n = malloc(sizeof(node\_t)) = malloc(sizeof(node\_t)); DB 4 184207 assert (n != NULL); Nasser (n != NULL); n->value = value; n->value = value; // begin critical section pthread\_mutex\_lock(listlock); old-rake n->next = head;n->next = headhead = n;while (CompareAndSwap(&head, n->next, n) == 0); pthread\_mutex\_unlock(listlock); // end critical section

### Deadlock Avoidance via Scheduling

- 上海 神隆 对部一一一个时间,
- - Avoidance requires to know which locks threads grab during their execution,
     and subsequently schedules said threads to guarantee no deadlock can occur
  - Consider two processors and four threads with global knowledge for lock
    - 1) A smart scheduler could thus compute that as long as T1 and T2 are not run at the same time, no deadlock could ever arise



2) T1, T2, and T3 all need to grab both locks L1 and L2 at some point during their execution → T1, T2, and T3 are all run on the same processor



Thus, the total time to complete the jobs is lengthened considerably