Automatic detection of MPI assertions



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https://github.com/tudasc/mach

C3PO'20: Compiler-assisted Correctness Checking and Performance Optimization for HPC



Motivation



- The 2019 draft specification of MPI [Mes19] defines new communicator info hints:
 - mpi_assert_allow_overtaking
 - $lue{}$ mpi_assert_exact_length
 - mpi_assert_no_any_tag
 - mpi_assert_no_any_source
- If these assertions are given a more optimized implementation man be used

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- If these assertions are given a more optimized implementation man be used
- We propose to automatically detect if these assertions hold using a Clang/LLVM compiler pass
- ⇒ This saves the developers effort to manually check if these assertions hold





 $allow_overtaking$ assertion

exact length assertion

no_any_tag and no_any_source assertion

Evaluation



allow overtaking assertion

exact length assertion

no_any_tag and no_any_source assertion

Evaluation

allow_overtaking assertion



```
if (rank = 0)
int buffer:
MPI Recv(&buffer, 1, MPI_INT, 1, TAG,
    MPI COMM WOLRD, MPI STATUS IRGNORE);
// do something with message 1
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI_COMM_WOLRD, MPI_STATUS_IRGNORE);
else \{ // rank = 1 \}
int buffer = 1:
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD);
// prepare message 2
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD);
```

Listing 1: Standard-conform MPI code snippet

 The messages must be received in the same order as they where sent

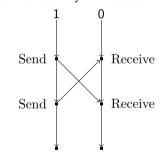


Figure: Messages overtaking each other



allow_overtaking assertion



```
if (rank = 0){
int buffer:
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI COMM WOLRD, MPI_STATUS_IRGNORE);
// do something with message 1
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI_COMM_WOLRD, MPI_STATUS_IRGNORE);
else \{ // rank = 1 \}
int buffer = 1:
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD):
// prepare message 2
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD);
```

Listing 1: Standard-conform MPI code snippet

- The messages must be received in the same order as they where sent
- + Ensuring the order of messages is one of the most costly phases of message matching [SDGB18]
- + If message overtaking is allowed, this phase may be entirely skipped





```
if (rank = 0){
int buffer:
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI COMM_WOLRD, MPI_STATUS_IRGNORE);
// do something with message 1
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI_COMM_WOLRD, MPI_STATUS_IRGNORE);
else \{ // rank = 1 \}
int buffer = 1:
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD):
// prepare message 2
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD);
```

Listing 2: Standard-conform MPI code snippet

- ⇒ We check if there is any pair of sending operations, where the messages might overtake each other
- A pair of sending operations is considered conflict-free if at least one of these conditions hold:
 - 1. they use a different communicator
 - 2. they use a different message tag
 - 3. they are sent to different target ranks
 - 4. there is no codepath between them
 - 5. they are separated by a synchronization of the processes





Listing 3: Non-conflicting sending operations

- 1. they use a different communicator
- 2. they use a different message tag
- 3. they are sent to different target ranks
- 4. there is no codepath between them
- they are separated by a synchronization of the processes
- Often it is possible to statically prove a difference in tag, target or communicator
- We use LLVM's ScalarEvolution analysis for this purpose





```
int buffer:
if (rank \% 2 = 0)
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD)
MPI Recv(&buffer, 1, MPI INT, 0, TAG,
    MPI_COMM_WOLRD, MPI_STATUS_IGNORE)
}else{
int to send = buffer:
MPI Recv(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD. MPI STATUS IGNORE)
MPI_Send(&to_send, 1, MPI_INT, 0, TAG,
    MPI COMM WOLRD)
```

Listing 4: Only one sending operation will be executed

- 1. they use a different communicator
- 2. they use a different message tag
- 3. they are sent to different target ranks
- 4. there is no codepath between them
- 5. they are separated by a synchronization of the processes
- If there does not exist a path in the control flow graph between the two operations, they are conflict free
- Note that an operation can conflict with itself e.g. in a loop





```
if (rank = 0)
int buffer = 1:
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI COMM WOLRD, MPI_STATUS_IRGNORE);
MPI Barrier (MPI COMM WORLD);
MPI Recv(&buffer, 1, MPI INT, 1, TAG,
    MPI_COMM_WOLRD, MPI_STATUS_IRGNORE);
else \{ // rank = 1 \}
int buffer:
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD):
MPI Barrier (MPI COMM WORLD):
MPI Send(&buffer, 1, MPI INT, 0, TAG,
    MPI COMM WOLRD);
```

Listing 5: Communication is split by synchronization

- 1. they use a different communicator
- 2. they use a different message tag
- 3. they are sent to different target ranks
- 4. there is no codepath between them
- 5. they are separated by a synchronization of the processes
- If there is a barrier or an allreduce on all possible codepaths between them:
- We can assume that the target process has completed the matching receive
 - Otherwise there might be a deadlock
- For this analysis: consider non-blocking operations at corresponding wait





- Check all pairs of sending operations if
 - 1. they use a different communicator
 - 2. they use a different message tag
 - 3. they are sent to different target ranks
 - 4. there is no codepath between them
 - 5. they are separated by a synchronization of the processes
- ⇒ If for all pairs at least one condition hold one can specify the assertion



allow_overtaking assertion

 $exact_length$ assertion

no_any_tag and no_any_source assertior

Evaluation

exact_length assertion



Listing 6: Standard conform MPI code snipped
Figure: Illustration of the receive buffer



- Receiving a message into a shorter buffer is not allowed
- Supplying MPI_Recv with a shorter buffer than indicated by type × count is not allowed
- + MPI forbids the modification of buffer[2-3] in this example
- The possibility to overwrite these excess buffer locations will allow for some optimizations

exact_length assertion



Listing 6: Standard conform MPI code snipped

- Receiving a message into a shorter buffer is not allowed
- Supplying MPI_Recv with a shorter buffer than indicated by type × count is not allowed
- + MPI forbids the modification of buffer[2-3] in this example
- The possibility to overwrite these excess buffer locations will allow for some optimizations
- Implementation as a proof of concept that static analysis can check for this assertion
- Grouping all Send/Recv operations by message tag
 - \Rightarrow If all operations within each group use the same message size, the assertion may be specified





allow_overtaking assertion

exact length assertion

no_any_tag and no_any_source assertion

Evaluation

no_any_tag / no_any_source assertion



Listing 7: Standard conform MPI code snipped

- MPI_ANY_TAG and MPI_ANY_SOURCE s to implement non deterministic communication schemes
- Not using these MPI features allow for some optimizations during message matching

no_any_tag / no_any_source assertion



Listing 7: Standard conform MPI code snipped

- MPI_ANY_TAG and MPI_ANY_SOURCE s to implement non deterministic communication schemes
- Not using these MPI features allow for some optimizations during message matching
- ⇒ Easy to determine if a predefined constant value is used within MPI operations





allow_overtaking assertion

exact length assertion

no_any_tag and no_any_source assertion

Evaluation

Evaluation



- Evaluated our tool using 48 different small self-written MPI programs.
- Specifically designed to test various cases of mpi_allow_overtaking
- √ Minimal impact on compilation time
 - -ftime-report reports that our pass uses only 0.3% (0.0014 seconds) of the compilation time for test program (≈ 400 lines of code)

- √ All cases correct for mpi_no_any_tag
- √ All cases correct for mpi_no_any_source
 - simple constant checking
 - no need for extensive evaluation
- ✓ All cases correct for mpi_exact_length
 - more refined implementation desirable
 - with more extensive evaluation
- Most cases correct for mpi_allow_overtaking
 - our tool only suggests using the assertion when it is safe to do so
 - imes but it misses some of the cases where one can specify the assertion (see next slides)



Missed cases of allow_overtaking Interleaved communication and synchronization



```
if (rank = 0)
    MPI Recv(&buffer, 1, MPI INT, 0, TAG,
        MPI COMM WOLRD.MPI STATUS IGNORE):
    MPI Ibarrier (MPI COMM WORLD, &bar req);
   MPI Wait(&bar reg, MPI STATUS IGNORE);
    MPI Recv(&buffer, 1, MPI INT, 0, TAG,
        MPI COMM WOLRD.MPI STATUS IGNORE):
else \{ // rank = 1 \}
    MPI Ibarrier (MPI COMM WORLD, &bar reg):
    MPI Send(&buffer, 1, MPI INT, 0, TAG,
        MPI COMM WOLRD):
   MPI Wait(&bar reg, MPI STATUS IGNORE);
    MPI Send(&buffer, 1, MPI INT, 0, TAG,
       MPI COMM WOLRD);
```

Listing 8: The messages can not overtake in this case

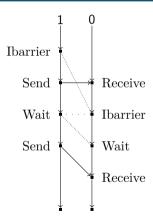


Figure: Illustration of the code snipped



Missed cases of allow_overtaking

Interleaved communication and synchronization

```
TECHNISCHE UNIVERSITÄT DARMSTADT
```

```
if (rank = 0)
    MPI Recv(&buffer, 1, MPI INT, 0, TAG,
        MPI COMM WOLRD, MPI STATUS IGNORE);
    MPI Ibarrier (MPI_COMM_WORLD, &bar_req);
   MPI Wait(&bar reg, MPI STATUS IGNORE);
    MPI Recv(&buffer, 1, MPI INT, 0, TAG,
        MPI_COMM_WOLRD, MPI_STATUS_IGNORE);
else \{ // rank = 1 \}
    MPI Ibarrier (MPI COMM WORLD, &bar reg):
    MPI Send(&buffer, 1, MPI INT, 0, TAG,
       MPI COMM WOLRD):
   MPI_Wait(&bar_req, MPI_STATUS_IGNORE);
    MPI Send(&buffer, 1, MPI INT, 0, TAG,
       MPI COMM WOLRD);
```

Listing 8: The messages can not overtake in this case

- The first sending operation is "within" the Ibarrier
 - part of the pre-synchronization communication phase
 - part of the post-synchronization communication phase
- Our analysis detects a conflict in the post-synchronization communication phase in this example



Missed cases of allow_overtaking

Ring communication scheme



Listing 9: Ring communication scheme

(pretend that one rank communicates "backward" first to avoid deadlock)

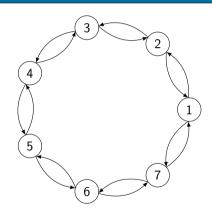


Figure: Ring communication scheme



Missed cases of allow_overtaking

Ring communication scheme

```
TECHNISCHE UNIVERSITÄT DARMSTADT
```

Listing 10: Ring communication scheme

(pretend that one rank communicates "backward" first to avoid deadlock)

- Static analysis can not prove that pre and next are different
 - if executed with 2 ranks they are same
- Therefore our tool has to assume the sending operations may conflict
- Using different message tags for "forward" and "backward" communication mitigates this problem





allow_overtaking assertion

exact length assertion

no any tag and no any source assertion

Evaluation



 Our prototype implemenation shows that detecting if the newly defined communicator info hints hold is possible by static analysis only in many cases



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- Our analysis is currently limited to the scope of one object file
 - but one can limit the scope of an assertion to one object file by using a duplicate MPI communicator for each object file
- We plan to extend our Pass, so that it insert the specification of the assertion if it holds
- We plan to extend our tool to give the programmer guidance on how to change an application so that the assertions hold
- Once MPI implementations exploit the associated performance benefits, we plan an empirical evaluation on the performance gained by specifying the assertions



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- We plan to extend our tool to give the programmer guidance on how to change an application so that the assertions hold
- Once MPI implementations exploit the associated performance benefits, we plan an empirical evaluation on the performance gained by specifying the assertions
- As we operate on the LLVM IR our approach and implementation is easily transferable to other LLVM input languages
- Our code is available online: https://github.com/tudasc/mach



References



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