

# Cooley–Tukey FFT algorithm

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The **Cooley–Tukey algorithm**, named after J.W. Cooley and John Tukey, is the most common fast Fourier transform (FFT) algorithm. It re-expresses the discrete Fourier transform (DFT) of an arbitrary composite size  $N = N_1 N_2$  in terms of smaller DFTs of sizes  $N_1$  and  $N_2$ , recursively, in order to reduce the computation time to  $O(N \log N)$  for highly-composite  $N$  (smooth numbers). Because of the algorithm's importance, specific variants and implementation styles have become known by their own names, as described below.

Because the Cooley-Tukey algorithm breaks the DFT into smaller DFTs, it can be combined arbitrarily with any other algorithm for the DFT. For example, Rader's or Bluestein's algorithm can be used to handle large prime factors that cannot be decomposed by Cooley–Tukey, or the prime-factor algorithm can be exploited for greater efficiency in separating out relatively prime factors.

See also the fast Fourier transform for information on other FFT algorithms, specializations for real and/or symmetric data, and accuracy in the face of finite floating-point precision.

## History

This algorithm, including its recursive application, was invented around 1805 by Carl Friedrich Gauss, who used it to interpolate the trajectories of the asteroids Pallas and Juno, but his work was not widely recognized (being published only posthumously and in neo-Latin).<sup>[1]</sup> <sup>[2]</sup> Gauss did not analyze the asymptotic computational time, however. Various limited forms were also rediscovered several times throughout the 19th and early 20th centuries.<sup>[2]</sup> FFTs became popular after J. W. Cooley of IBM and John W. Tukey of Princeton published a paper in 1965 reinventing the algorithm and describing how to perform it conveniently on a computer.<sup>[3]</sup>

Tukey reportedly came up with the idea during a meeting of a US presidential advisory committee discussing ways to detect nuclear-weapon tests in the Soviet Union.<sup>[4]</sup> <sup>[5]</sup> Another participant at that meeting, Richard Garwin of IBM, recognized the potential of the method and put Tukey in touch with Cooley, who implemented it for a different (and less-classified) problem: analyzing 3d crystallographic data (see also: multidimensional FFTs). Cooley and Tukey subsequently published their joint paper, and wide adoption quickly followed.

The fact that Gauss had described the same algorithm (albeit without analyzing its asymptotic cost) was not realized until several years after Cooley and Tukey's 1965 paper.<sup>[2]</sup> Their paper cited as inspiration only work by I. J. Good on what is now called the prime-factor FFT algorithm (PFA),<sup>[3]</sup> although Good's algorithm was initially mistakenly thought to be equivalent to the Cooley–Tukey algorithm, it was quickly realized that PFA is a quite different algorithm (only working for sizes that have relatively prime factors and relying on the Chinese Remainder Theorem, unlike the support for any composite size in Cooley–Tukey).<sup>[6]</sup>

## The radix-2 DIT case

A **radix-2** decimation-in-time (**DIT**) FFT is the simplest and most common form of the Cooley–Tukey algorithm, although highly optimized Cooley–Tukey implementations typically use other forms of the algorithm as described below. Radix-2 DIT divides a DFT of size  $N$  into two interleaved DFTs (hence the name "radix-2") of size  $N/2$  with each recursive stage.

The discrete Fourier transform (DFT) is defined by the formula:

$$X_k = \sum_{n=0}^{N-1} x_n e^{-\frac{2\pi i}{N} nk},$$

where  $k$  is an integer ranging from 0 to  $N - 1$ .

Radix-2 DIT first computes the DFTs of the even-indexed inputs  $x_{2m}$  ( $x_0, x_2, \dots, x_{N-2}$ ) and of the odd-indexed inputs  $x_{2m+1}$  ( $x_1, x_3, \dots, x_{N-1}$ ), and then combines those two results to produce the DFT of the

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whole sequence. This idea can then be performed recursively to reduce the overall runtime to  $O(N \log N)$ . This simplified form assumes that  $N$  is a power of two; since the number of sample points  $N$  can usually be chosen freely by the application, this is often not an important restriction.

The Radix-2 DIT algorithm rearranges the DFT of the function  $x_n$  into two parts: a sum over the even-numbered indices  $n = 2m$  and a sum over the odd-numbered indices  $n = 2m + 1$ :

$$X_k = \sum_{m=0}^{N/2-1} x_{2m} e^{-\frac{2\pi i}{N}(2m)k} + \sum_{m=0}^{N/2-1} x_{2m+1} e^{-\frac{2\pi i}{N}(2m+1)k}.$$

One can factor a common multiplier  $e^{-\frac{2\pi i}{N}k}$  out of the second sum, as shown in the equation below. It is then clear that the two sums are the DFT of the even-indexed part  $x_{2m}$  and the DFT of odd-indexed part  $x_{2m+1}$  of the function  $x_n$ . Denote the DFT of the **E**ven-indexed inputs  $x_{2m}$  by  $E_k$  and the DFT of the **O**dd-indexed inputs  $x_{2m+1}$  by  $O_k$  and we obtain:

$$X_k = \underbrace{\sum_{m=0}^{N/2-1} x_{2m} e^{-\frac{2\pi i}{N/2}mk}}_{\text{DFT of even-indexed part of } x_m} + e^{-\frac{2\pi i}{N}k} \underbrace{\sum_{m=0}^{N/2-1} x_{2m+1} e^{-\frac{2\pi i}{N/2}mk}}_{\text{DFT of odd-indexed part of } x_m} = E_k + e^{-\frac{2\pi i}{N}k} O_k.$$

However, these smaller DFTs have a length of  $N/2$ , so we need compute only  $N/2$  outputs: thanks to the periodicity properties of the DFT, the outputs for  $N/2 \leq k < N$  from a DFT of length  $N/2$  are identical to the outputs for  $0 \leq k < N/2$ . That is,  $E_{k+N/2} = E_k$  and  $O_{k+N/2} = O_k$ . The phase factor  $\exp[-2\pi i k/N]$  (called a twiddle factor) obeys the relation:  $\exp[-2\pi i(k + N/2)/N] = e^{-\pi i} \exp[-2\pi i k/N] = -\exp[-2\pi i k/N]$ , flipping the sign of the  $O_{k+N/2}$  terms. Thus, the whole DFT can be calculated as follows:

$$X_k = \begin{cases} E_k + e^{-\frac{2\pi i}{N}k} O_k & \text{if } k < N/2 \\ E_{k-N/2} - e^{-\frac{2\pi i}{N}(k-N/2)} O_{k-N/2} & \text{if } k \geq N/2. \end{cases}$$

This result, expressing the DFT of length  $N$  recursively in terms of two DFTs of size  $N/2$ , is the core of the radix-2 DIT fast Fourier transform. The algorithm gains its speed by re-using the results of intermediate computations to compute multiple DFT outputs. Note that final outputs are obtained by a  $+/-$  combination of  $E_k$  and  $O_k \exp(-2\pi i k/N)$ , which is simply a size-2 DFT; when this is generalized to larger radices below, the size-2 DFT is replaced by a larger DFT (which itself can be evaluated with an FFT).

This process is an example of the general technique of divide and conquer algorithms; in many traditional implementations, however, the explicit recursion is avoided, and instead one traverses the computational tree in breadth-first fashion.

The above re-expression of a size- $N$  DFT as two size- $N/2$  DFTs is sometimes called the **Danielson–Lanczos** lemma, since the identity was noted by those two authors in 1942<sup>[7]</sup> (influenced by Runge's 1903 work<sup>[21]</sup>). They applied their lemma in a "backwards" recursive fashion, repeatedly *doubling* the DFT size until the transform spectrum converged (although they apparently didn't realize the linearithmic asymptotic complexity they had achieved). The Danielson–Lanczos work predated widespread availability of computers and required hand calculation (possibly with mechanical aids such as adding machines); they reported a computation time of 140 minutes for a size-64 DFT operating on real inputs to 3–5 significant digits. Cooley and Tukey's 1965 paper reported a running time of 0.02 minutes for a size-2048 complex DFT on an IBM 7094 (probably in 36-bit single precision, ~8 digits).<sup>[31]</sup> Rescaling the time by the number of operations, this corresponds roughly to a speedup factor of around 800,000. (To put the time for the hand calculation in perspective, 140 minutes for size 64 corresponds to an average of at most 16 seconds per floating-point operation, around 20% of which are multiplications.)

## Pseudocode

In pseudocode, the above process could be written:

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 $Y_{0,\dots,N-1} \leftarrow \text{ditfft2}(X, N, s):$            DFT of  $(X_0, X_s, X_{2s}, \dots, X_{(N-1)s})$ :
  if  $N = 1$  then
     $Y_0 \leftarrow X_0$                                trivial size-1 DFT base case
  else
     $Y_{0,\dots,N/2-1} \leftarrow \text{ditfft2}(X, N/2, 2s)$        DFT of  $(X_0, X_{2s}, X_{4s}, \dots)$ 
     $Y_{N/2,\dots,N-1} \leftarrow \text{ditfft2}(X+s, N/2, 2s)$      DFT of  $(X_s, X_{s+2s}, X_{s+4s}, \dots)$ 
    for  $k = 0$  to  $N/2-1$                                 combine DFTs of two halves into full DFT:
       $t \leftarrow Y_k$ 
       $Y_k \leftarrow t + \exp(-2\pi i k/N) Y_{k+N/2}$ 
       $Y_{k+N/2} \leftarrow t - \exp(-2\pi i k/N) Y_{k+N/2}$ 
    endfor
  endif

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Here,  $\text{ditfft2}(X, N, 1)$ , computes  $Y = \text{DFT}(X)$  out-of-place by a radix-2 DIT FFT, where  $N$  is an integer power of 2 and  $s=1$  is the stride of the input  $X$  array.  $X+s$  denotes the array starting with  $X_s$ .

(The results are in the correct order in  $Y$  and no further bit-reversal permutation is required; the often-mentioned necessity of a separate bit-reversal stage only arises for certain in-place algorithms, as described below.)

It should be emphasized that high-performance FFT implementations make many modifications to the implementation of such an algorithm compared to this simple pseudocode. For example, one can use a larger base case than  $N=1$  is useful to amortize the overhead of recursion, the twiddle factors  $\exp(-2\pi i k/N)$  can be precomputed, and larger radices are often used for cache reasons; these and other optimizations together can improve the performance by an order of magnitude or more.<sup>[8]</sup> (In many textbook implementations the depth-first recursion is eliminated entirely in favor of a nonrecursive breadth-first approach, although depth-first recursion has been argued to have better memory locality.<sup>[8] [9]</sup>) Several of these ideas are described in further detail below.

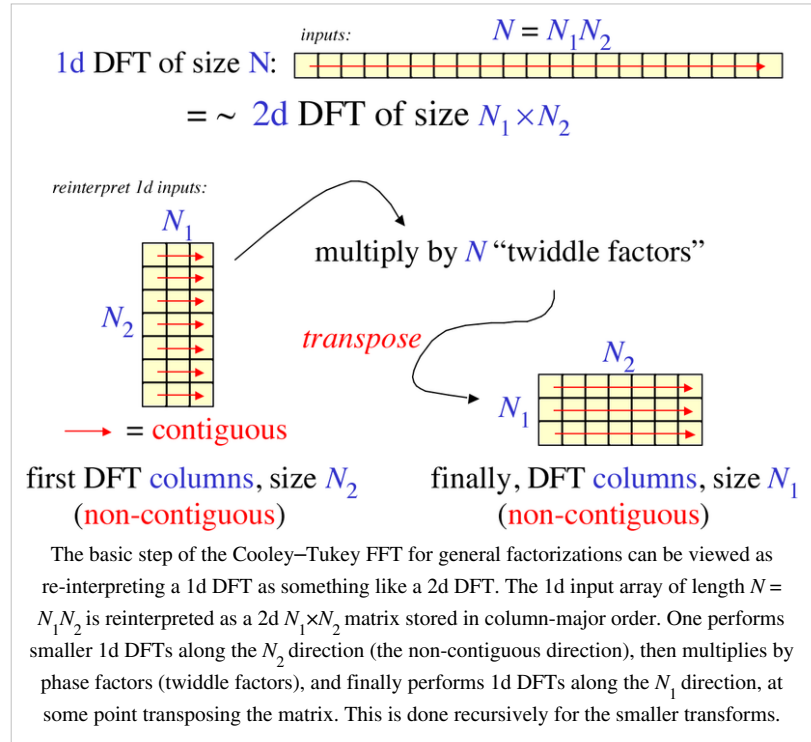
## General factorizations

More generally, Cooley–Tukey algorithms recursively re-express a DFT of a composite size  $N = N_1 N_2$  as:<sup>[10]</sup>

1. Perform  $N_1$  DFTs of size  $N_2$ .
2. Multiply by complex roots of unity called twiddle factors.
3. Perform  $N_2$  DFTs of size  $N_1$ .

Typically, either  $N_1$  or  $N_2$  is a small factor (*not* necessarily prime), called the **radix** (which can differ between stages of the recursion). If  $N_1$  is the radix, it is called a **decimation in time** (DIT) algorithm, whereas if  $N_2$  is the radix, it is **decimation in frequency** (DIF, also called the Sande–Tukey algorithm). The version presented above was a radix-2 DIT algorithm; in the final expression, the phase

multiplying the odd transform is the twiddle factor, and the  $\pm$  combination (*butterfly*) of the even and odd transforms is a size-2 DFT. (The radix's small DFT is sometimes known as a butterfly, so-called because of the shape of the dataflow diagram for the radix-2 case.)



There are many other variations on the Cooley–Tukey algorithm. **Mixed-radix** implementations handle composite sizes with a variety of (typically small) factors in addition to two, usually (but not always) employing the  $O(N^2)$  algorithm for the prime base cases of the recursion [it is also possible to employ an  $N \log N$  algorithm for the prime base cases, such as Rader's or Bluestein's algorithm]. Split radix merges radices 2 and 4, exploiting the fact that the first transform of radix 2 requires no twiddle factor, in order to achieve what was long the lowest known arithmetic operation count for power-of-two sizes,<sup>[10]</sup> although recent variations achieve an even lower count.<sup>[11] [12]</sup> (On present-day computers, performance is determined more by cache and CPU pipeline considerations than by strict operation counts; well-optimized FFT implementations often employ larger radices and/or hard-coded base-case transforms of significant size.<sup>[13]</sup>) Another way of looking at the Cooley–Tukey algorithm is that it re-expresses a size  $N$  one-dimensional DFT as an  $N_1$  by  $N_2$  two-dimensional DFT (plus twiddles), where the output matrix is transposed. The net result of all of these transpositions, for a radix-2 algorithm, corresponds to a bit reversal of the input (DIF) or output (DIT) indices. If, instead of using a small radix, one employs a radix of roughly  $\sqrt{N}$  and explicit input/output matrix transpositions, it is called a **four-step** algorithm (or *six-step*, depending on the number of transpositions), initially proposed to improve memory locality,<sup>[14] [15]</sup> e.g. for cache optimization or out-of-core operation, and was later shown to be an optimal cache-oblivious algorithm.<sup>[16]</sup>

The general Cooley–Tukey factorization rewrites the indices  $k$  and  $n$  as  $k = N_2 k_1 + k_2$  and  $n = N_1 n_2 + n_1$ , respectively, where the indices  $k_a$  and  $n_a$  run from  $0..N_a - 1$  (for  $a$  of 1 or 2). That is, it re-indexes the input ( $n$ ) and output ( $k$ ) as  $N_1$  by  $N_2$  two-dimensional arrays in column-major and row-major order, respectively; the difference between these indexings is a transposition, as mentioned above. When this re-indexing is substituted into the DFT formula for  $nk$ , the  $N_1 n_2 N_2 k_1$  cross term vanishes (its exponential is unity), and the remaining terms give

$$\begin{aligned}
X_{N_2 k_1 + k_2} &= \sum_{n_1=0}^{N_1-1} \sum_{n_2=0}^{N_2-1} x_{N_1 n_2 + n_1} e^{-\frac{2\pi i}{N_1 N_2} \cdot (N_1 n_2 + n_1) \cdot (N_2 k_1 + k_2)} \\
&= \sum_{n_1=0}^{N_1-1} \left[ e^{-\frac{2\pi i}{N} n_1 k_2} \right] \left( \sum_{n_2=0}^{N_2-1} x_{N_1 n_2 + n_1} e^{-\frac{2\pi i}{N_2} n_2 k_2} \right) e^{-\frac{2\pi i}{N_1} n_1 k_1}
\end{aligned}$$

where the inner sum is a DFT of size  $N_2$ , the outer sum is a DFT of size  $N_1$ , and the [...] bracketed term is the twiddle factor.

An arbitrary radix  $r$  (as well as mixed radices) can be employed, as was shown by both Cooley and Tukey<sup>[3]</sup> as well as Gauss (who gave examples of radix-3 and radix-6 steps).<sup>[2]</sup> Cooley and Tukey originally assumed that the radix butterfly required  $O(r^2)$  work and hence reckoned the complexity for a radix  $r$  to be  $O(r^2 N/r \log_r N) = O(N \log_2(N) r/\log_2 r)$ ; from calculation of values of  $r/\log_2 r$  for integer values of  $r$  from 2 to 12 the optimal radix is found to be 3 (the closest integer to  $e$ , which minimizes  $r/\log_2 r$ ).<sup>[3] [17]</sup> This analysis was erroneous, however: the radix-butterfly is also a DFT and can be performed via an FFT algorithm in  $O(r \log r)$  operations, hence the radix  $r$  actually cancels in the complexity  $O(r \log(r) N/r \log_r N)$ , and the optimal  $r$  is determined by more complicated considerations. In practice, quite large  $r$  (32 or 64) are important in practice in order to effectively exploit e.g. the large number of processor registers on modern processors,<sup>[13]</sup> and even an unbounded radix  $r=\sqrt{N}$  also achieves  $O(N \log N)$  complexity and has theoretical and practical advantages for large  $N$  as mentioned above.<sup>[14] [15] [16]</sup>

## Data reordering, bit reversal, and in-place algorithms

Although the abstract Cooley–Tukey factorization of the DFT, above, applies in some form to all implementations of the algorithm, much greater diversity exists in the techniques for ordering and accessing the data at each stage of the FFT. Of special interest is the problem of devising an in-place algorithm that overwrites its input with its output data using only  $O(1)$  auxiliary storage.

The most well-known reordering technique involves explicit **bit reversal** for in-place radix-2 algorithms. Bit reversal is the permutation where the data at an index  $n$ , written in binary with digits  $b_4 b_3 b_2 b_1 b_0$  (e.g. 5 digits for  $N=32$  inputs), is transferred to the index with reversed digits  $b_0 b_1 b_2 b_3 b_4$ . Consider the last stage of a radix-2 DIT algorithm like the one presented above, where the output is written in-place over the input: when  $E_k$  and  $O_k$  are combined with a size-2 DFT, those two values are overwritten by the outputs. However, the two output values should go in the first and second *halves* of the output array, corresponding to the *most* significant bit  $b_4$  (for  $N=32$ ); whereas the two inputs  $E_k$  and  $O_k$  are interleaved in the even and odd elements, corresponding to the *least* significant bit  $b_0$ . Thus, in order to get the output in the correct place, these two bits must be swapped in the input. If you include all of the recursive stages of a radix-2 DIT algorithm, *all* the bits must be swapped and thus one must pre-process the *input* with a bit reversal to get in-order output. Correspondingly, the reversed (dual) algorithm is radix-2 DIF, and this takes in-order input and produces bit-reversed *output*, requiring a bit-reversal post-processing step. Alternatively, some applications (such as convolution) work equally well on bit-reversed data, so one can do radix-2 DIF without bit reversal, followed by processing, followed by the radix-2 DIT inverse DFT without bit reversal to produce final results in the natural order.

Many FFT users, however, prefer natural-order outputs, and a separate, explicit bit-reversal stage can have a non-negligible impact on the computation time,<sup>[13]</sup> even though bit reversal can be done in  $O(N)$  time and has been the subject of much research<sup>[18] [19] [20]</sup>. Also, while the permutation is a bit reversal in the radix-2 case, it is more generally an arbitrary (mixed-base) digit reversal for the mixed-radix case, and the permutation algorithms become more complicated to implement. Moreover, it is desirable on many hardware architectures to re-order intermediate stages of the FFT algorithm so that they operate on consecutive (or at least more localized) data elements. To these ends, a number of alternative implementation schemes have been devised for the Cooley–Tukey algorithm that do not require separate bit reversal and/or involve additional permutations at intermediate stages.

The problem is greatly simplified if it is **out-of-place**: the output array is distinct from the input array or, equivalently, an equal-size auxiliary array is available. The **Stockham auto-sort** algorithm<sup>[21]</sup> performs every stage of the FFT out-of-place, typically writing back and forth between two arrays, transposing one "digit" of the indices with each stage, and has been especially popular on SIMD architectures<sup>[22]</sup>. Even greater potential SIMD advantages (more consecutive accesses) have been proposed for the **Pease** algorithm<sup>[23]</sup>, which also reorders out-of-place with each stage, but this method requires separate bit/digit reversal and  $O(N \log N)$  storage. One can also directly apply the Cooley–Tukey factorization definition with explicit (depth-first) recursion and small radices, which produces natural-order out-of-place output with no separate permutation step (as in the pseudocode above) and can be argued to have cache-oblivious locality benefits on systems with hierarchical memory.<sup>[9] [24] [13]</sup>

A typical strategy for in-place algorithms without auxiliary storage and without separate digit-reversal passes involves small matrix transpositions (which swap individual pairs of digits) at intermediate stages, which can be combined with the radix butterflies to reduce the number of passes over the data.<sup>[25] [26] [27] [28] [13]</sup>

## External links

- a simple, pedagogical radix-2 Cooley–Tukey FFT algorithm in C++. <sup>[29]</sup>
- KISSFFT <sup>[30]</sup>: a simple mixed-radix Cooley–Tukey implementation in C (open source)

## References

- [1] Gauss, Carl Friedrich, "Nachlass: Theoria interpolationis methodo nova tractata", Werke, Band 3, 265–327 (Königliche Gesellschaft der Wissenschaften, Göttingen, 1866)
- [2] Heideman, M. T., D. H. Johnson, and C. S. Burrus, "Gauss and the history of the fast Fourier transform ([http://ieeexplore.ieee.org/xpls/abs\\_all.jsp?arnumber=1162257](http://ieeexplore.ieee.org/xpls/abs_all.jsp?arnumber=1162257))," *IEEE ASSP Magazine*, 1, (4), 14–21 (1984)
- [3] Cooley, James W., and John W. Tukey, "An algorithm for the machine calculation of complex Fourier series," *Math. Comput.* **19**, 297–301 (1965). doi:10.2307/2003354
- [4] Cooley, James W., Peter A. W. Lewis, and Peter D. Welch, "Historical notes on the fast Fourier transform," *IEEE Trans. on Audio and Electroacoustics* **15** (2), 76–79 (1967).
- [5] Rockmore, Daniel N., *Comput. Sci. Eng.* **2** (1), 60 (2000). The FFT — an algorithm the whole family can use (<http://www.cs.dartmouth.edu/~rockmore/cse-fft.pdf>) Special issue on "top ten algorithms of the century" (<http://amath.colorado.edu/resources/archive/topten.pdf>)
- [6] James W. Cooley, Peter A. W. Lewis, and Peter W. Welch, "Historical notes on the fast Fourier transform," *Proc. IEEE*, vol. **55** (no. 10), p. 1675–1677 (1967).
- [7] Danielson, G. C., and C. Lanczos, "Some improvements in practical Fourier analysis and their application to X-ray scattering from liquids," *J. Franklin Inst.* **233**, 365–380 and 435–452 (1942).
- [8] S. G. Johnson and M. Frigo, "Implementing FFTs in practice (<http://cnx.org/content/m16336/>)," in *Fast Fourier Transforms* (C. S. Burrus, ed.), ch. 11, Rice University, Houston TX: Connexions, September 2008.
- [9] Singleton, Richard C., "On computing the fast Fourier transform", *Commun. of the ACM* **10** (1967), 647–654
- [10] Duhamel, P., and M. Vetterli, "Fast Fourier transforms: a tutorial review and a state of the art," *Signal Processing* **19**, 259–299 (1990)
- [11] Lundy, T., and J. Van Buskirk, "A new matrix approach to real FFTs and convolutions of length  $2^k$ ," *Computing* **80**, 23–45 (2007).
- [12] Johnson, S. G., and M. Frigo, "A modified split-radix FFT with fewer arithmetic operations (<http://www.fftw.org/newsplit.pdf>)," *IEEE Trans. Signal Processing* **55** (1), 111–119 (2007).
- [13] Frigo, M. and S. G. Johnson, "The Design and Implementation of FFTW3 (<http://fftw.org/fftw-paper-ieee.pdf>)," *Proceedings of the IEEE* **93** (2), 216–231 (2005).
- [14] Gentleman W. M., and G. Sande, "Fast Fourier transforms—for fun and profit," *Proc. AFIPS* **29**, 563–578 (1966).
- [15] Bailey, David H., "FFT's in external or hierarchical memory," *J. Supercomputing* **4** (1), 23–35 (1990)
- [16] M. Frigo, C.E. Leiserson, H. Prokop, and S. Ramachandran. Cache-oblivious algorithms. In *Proceedings of the 40th IEEE Symposium on Foundations of Computer Science (FOCS 99)*, p.285–297. 1999. Extended abstract at IEEE (<http://ieeexplore.ieee.org/iel5/6604/17631/00814600.pdf?arnumber=814600>), at CiteSeer (<http://citeseer.ist.psu.edu/307799.html>).
- [17] Cooley, J. W., P. Lewis and P. Welch, "The Fast Fourier Transform and its Applications", *IEEE Trans on Education* **12**, 1, 28–34 (1969)
- [18] Karp, Alan H., "Bit reversal on uniprocessors," *SIAM Review* **38** (1), 1–26 (1996)
- [19] Carter, Larry and Kang Su Gatlin, "Towards an optimal bit-reversal permutation program," *Proc. 39th Ann. Symp. on Found. of Comp. Sci. (FOCS)*, 544–553 (1998).
- [20] Rubio, M., P. Gómez, and K. Drouiche, "A new superfast bit reversal algorithm," *Intl. J. Adaptive Control and Signal Processing* **16**, 703–707 (2002)

- [21] Stockham, T. G., "High speed convolution and correlation", *Spring Joint Computer Conference, Proc. AFIPS* **28**, 229–233 (1966)
  - [22] Swarztrauber, P. N., "Vectorizing the FFTs", in G. Rodrigue (Ed.), *Parallel Computations* (Academic Press, New York, 1982), pp. 51–83.
  - [23] Pease, M. C. "An adaptation of the fast Fourier transform for parallel processing", *J. ACM* **15** (2), 252–264 (1968)
  - [24] Frigo, Matteo and Steven G. Johnson: *FFTW*, <http://www.fftw.org/>. A free (GPL) C library for computing discrete Fourier transforms in one or more dimensions, of arbitrary size, using the Cooley–Tukey algorithm
  - [25] Johnson, H. W. and C. S. Burrus, "An in-place in-order radix-2 FFT," *Proc. ICASSP*, 28A.2.1–28A.2.4 (1984)
  - [26] Temperton, C., "Self-sorting in-place fast Fourier transform," *SIAM J. Sci. Stat. Comput.* **12** (4), 808–823 (1991)
  - [27] Qian, Z., C. Lu, M. An, and R. Tolimieri, "Self-sorting in-place FFT algorithm with minimum working space," *IEEE Trans. ASSP* **52** (10), 2835–2836 (1994)
  - [28] Hegland, M., "A self-sorting in-place fast Fourier transform algorithm suitable for vector and parallel processing," *Numerische Mathematik* **68** (4), 507–547 (1994)
  - [29] <http://www.librow.com/articles/article-10>
  - [30] <http://sourceforge.net/projects/kissfft/>
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