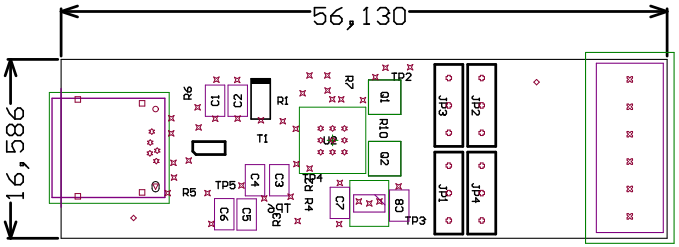
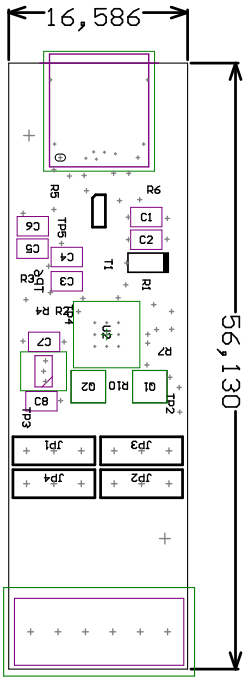
Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	SM-001	0,025mm	4	
	Top Surface Finish	PbSn	0,020mm		
1	Top Layer	CF-004	0,035mm		
	Dielectric 1	Core-042	0,991mm	4.6	
2	Bottom Layer	CF-004	0,035mm		
	Bottom Surface Finish	PbSn	0,020mm		
	Bottom Solder	SM-001	0,025mm	4	
	Bottom Overlay				

Total board thickness: 1,151mm

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template
○	1	0.650mm (25.59mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c65hn65
▽	1	0.650mm (25.59mil)	NPTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r95_65hn65_95r32
◇	2	2.000mm (78.74mil)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c200hn200
□	4	0.500mm (19.69mil)	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r140_80h50_110r40m150_90p0
⊠	6	1.100mm (43.31mil)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
✠	12	1.092mm (43.00mil)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)
✡	14	0.200mm (7.87mil)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	(Mixed)
⊠	39	0.400mm (15.75mil)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v70h40
	79 Total							

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

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56,130
R5
R6
C6
TP5
C5
C4
C1
C2
T1
R3
SOT
C3
R1
R4
R8
U2
C7
R7
B3
Q2
R10
Q1
TP2
TP3
JP1
JP3
JP4
JP2