Open Hardware. Free to Copy.

56.130

16.586

ESP32 / UART
EPB HW ME

R6

C1

D1 R1

R7

U2

T1

C4 C3

U1

C6 C5

C2

R5

J1

V.1.3

R2

R3

R4 C7

U3

C8

ENA RTS/485 VBUS

JP3

BOOT

JP2

CTS RTS

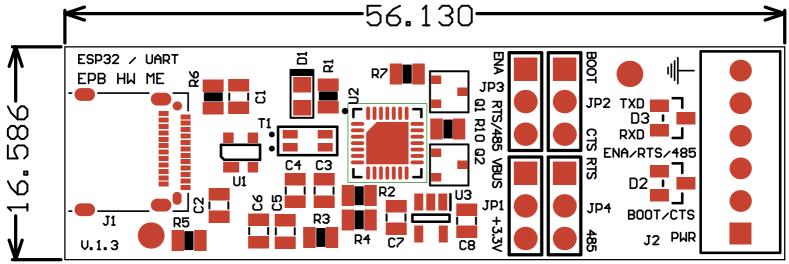JP1 +3.3V

JP4 485

Q1 R10 Q2

TXD
D3
RXD
ENA/RTS/485
D2
BOOT/CTS
J2 PWR

V.1.1 = layout improvement only
V.1.2 = OVERLAY CORRECTIONS
V.1.3 = added D2, D3