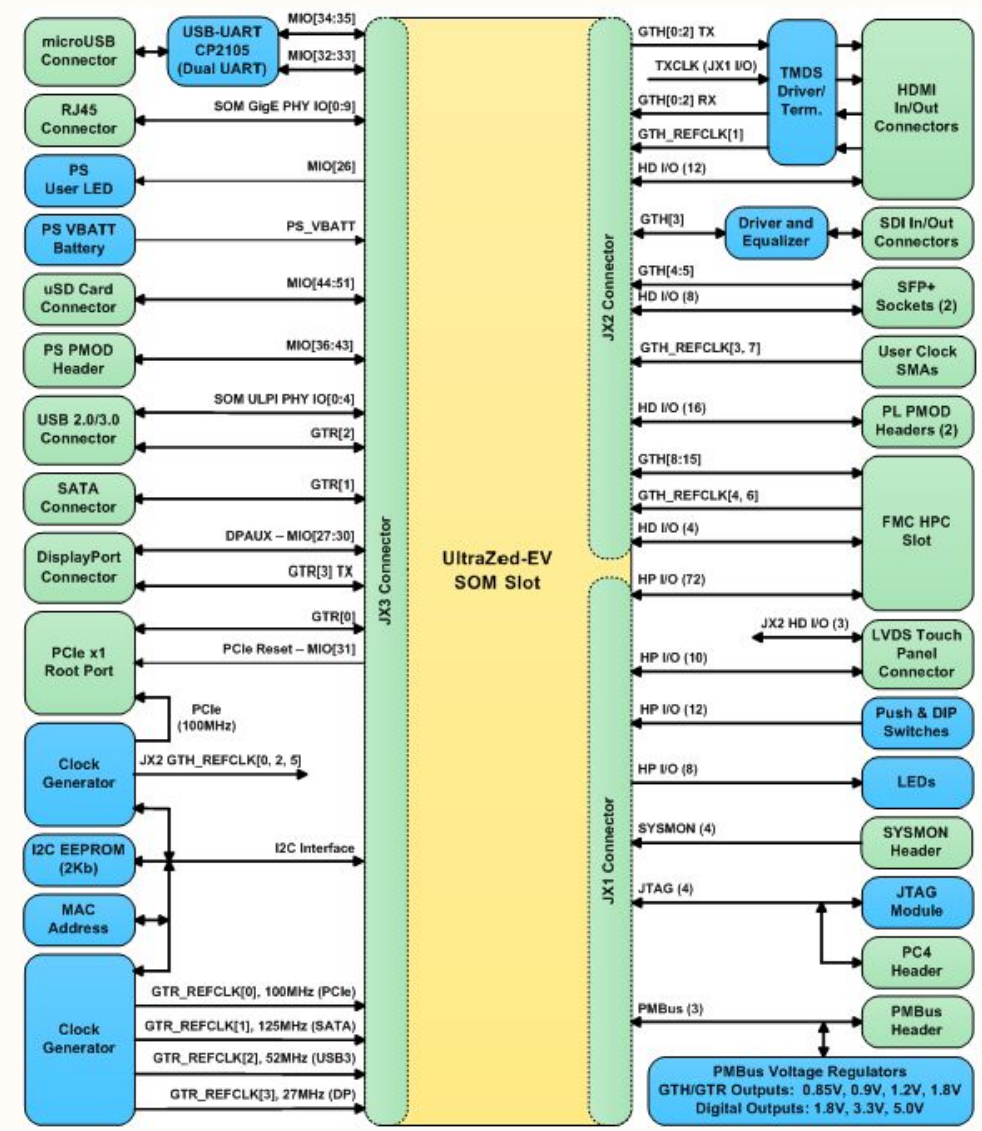
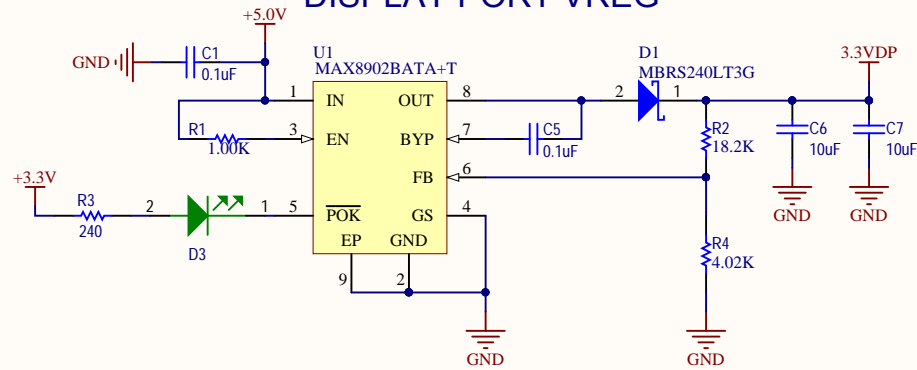


ULTRAZED EV CARRIER CARD BLOCK DIAGRAM

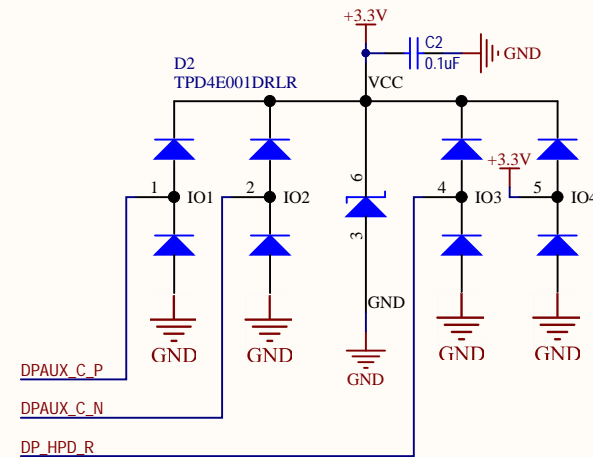


DISPLAY PORT & LVDS TOUCH PANEL INTERFACE

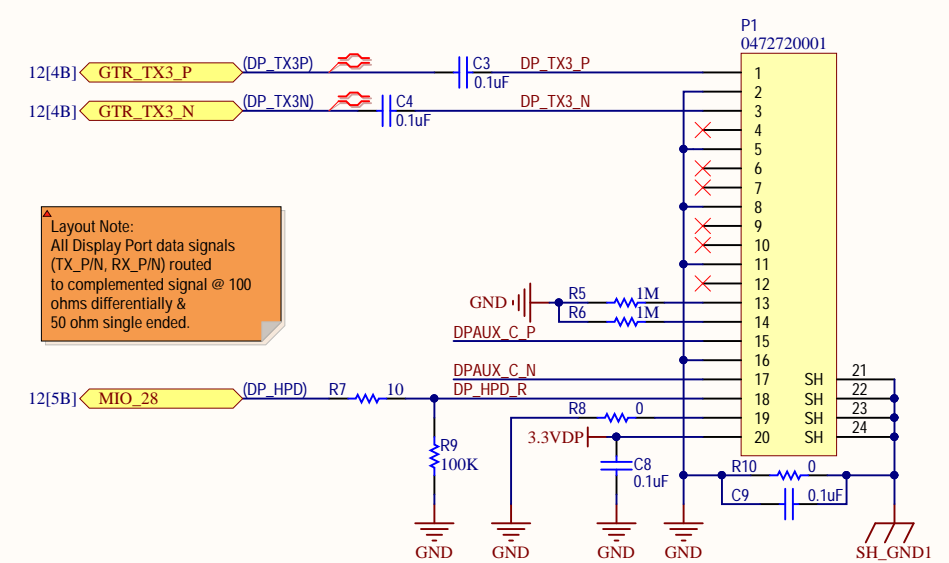
DISPLAY PORT VREG



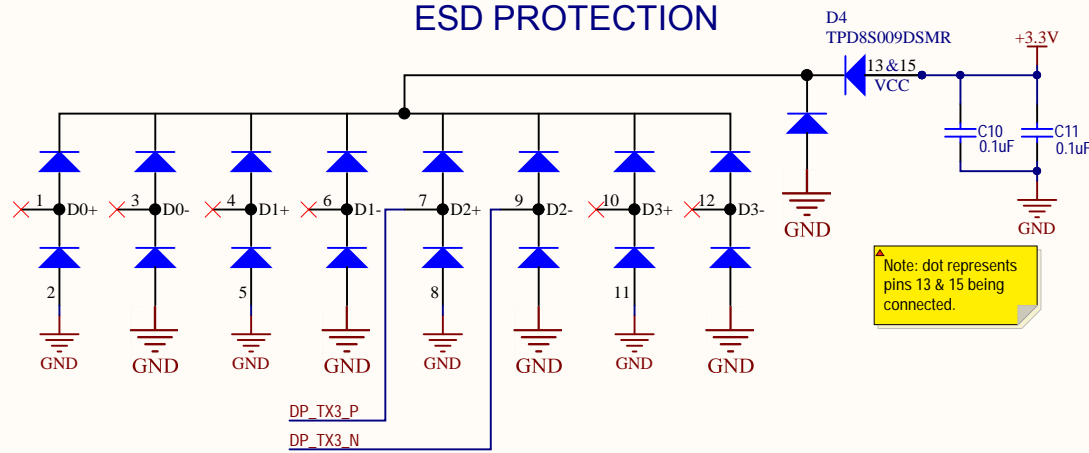
ESD PROTECTION



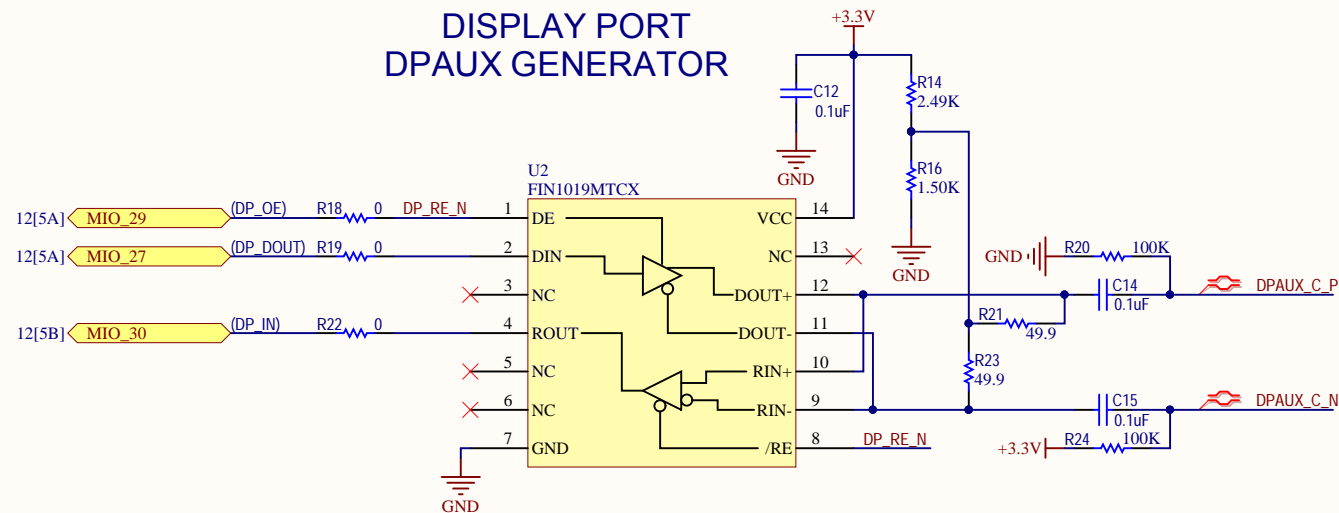
DISPLAY PORT CONNECTOR



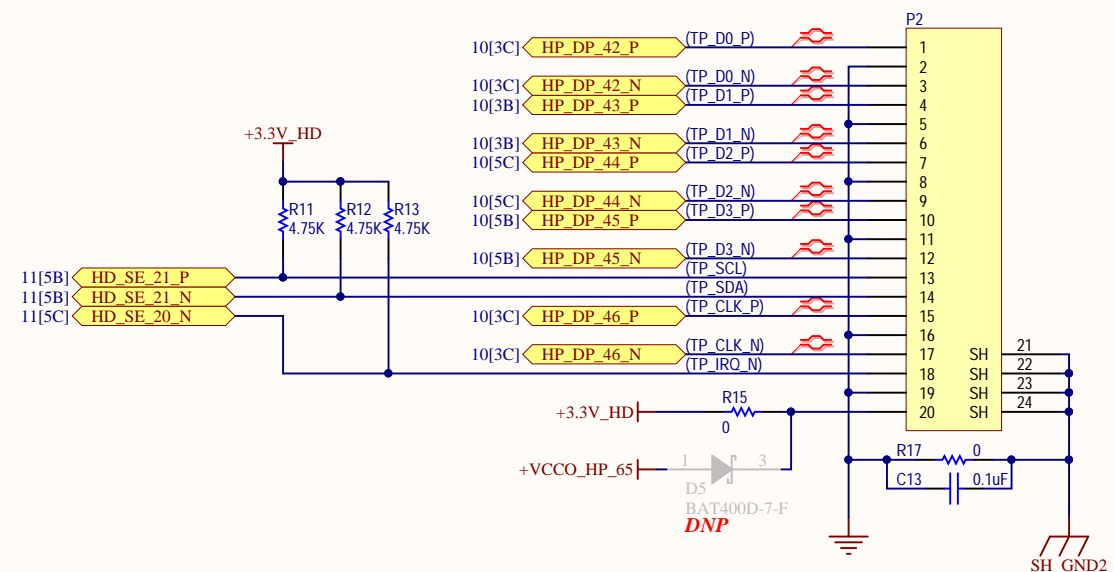
ESD PROTECTION

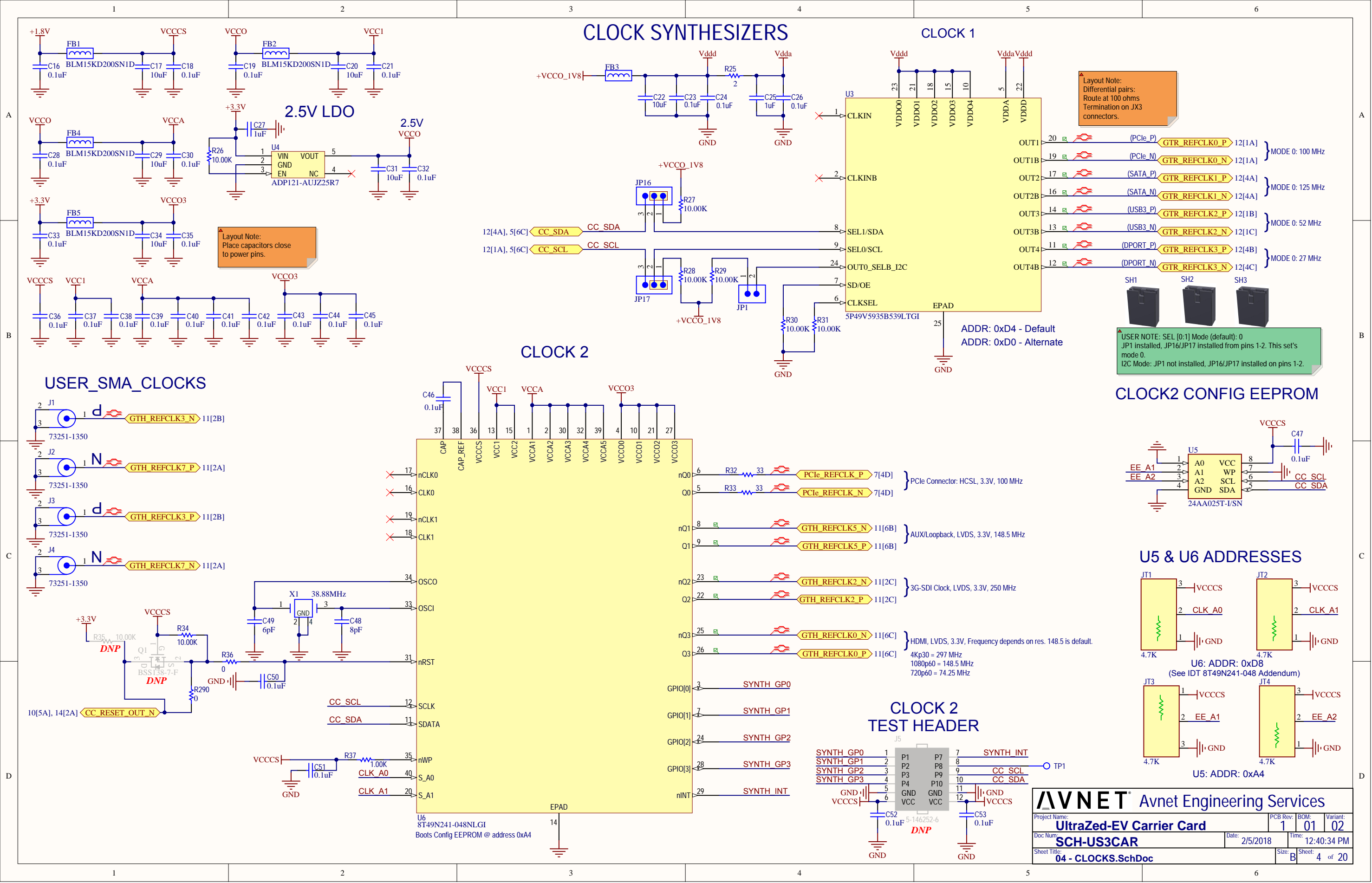


DISPLAY PORT DPAUX GENERATOR

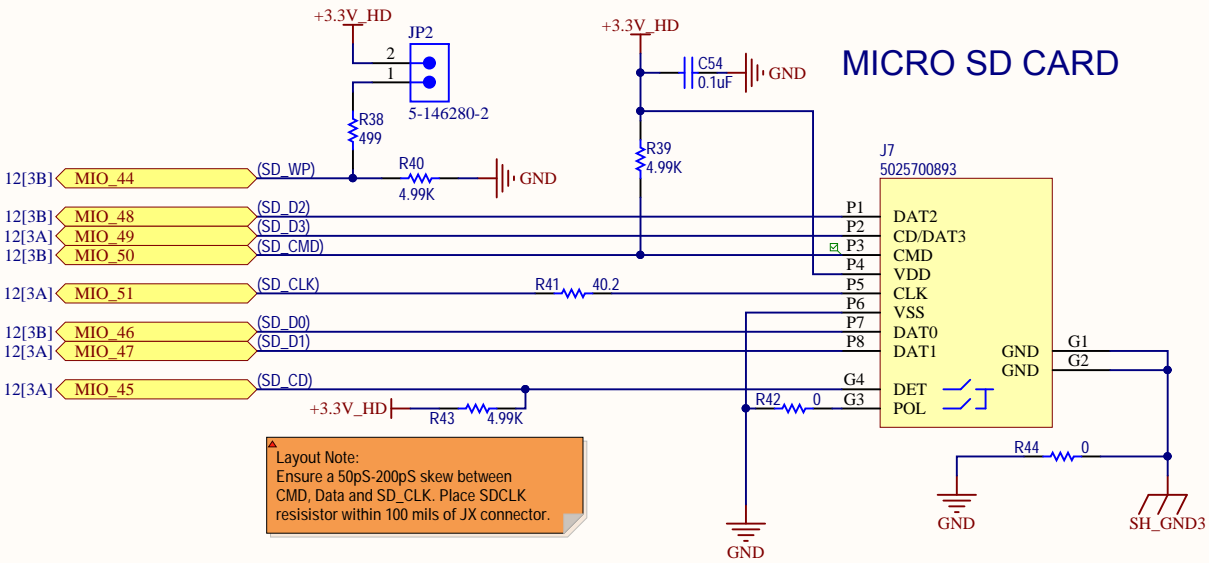


LVDS TOUCH PANEL INTERFACE CONNECTOR

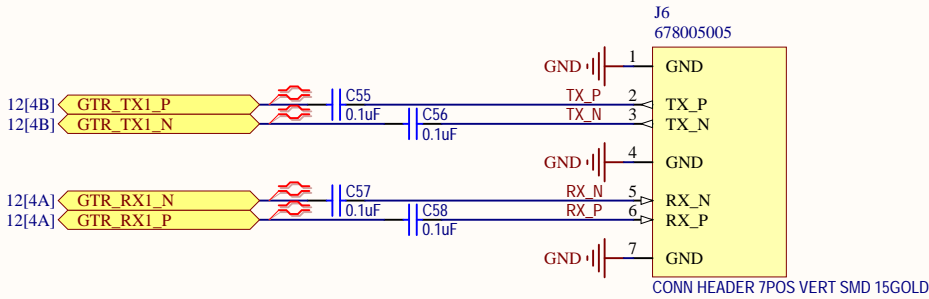




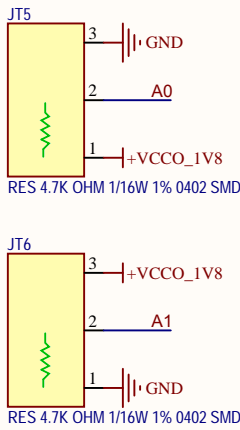
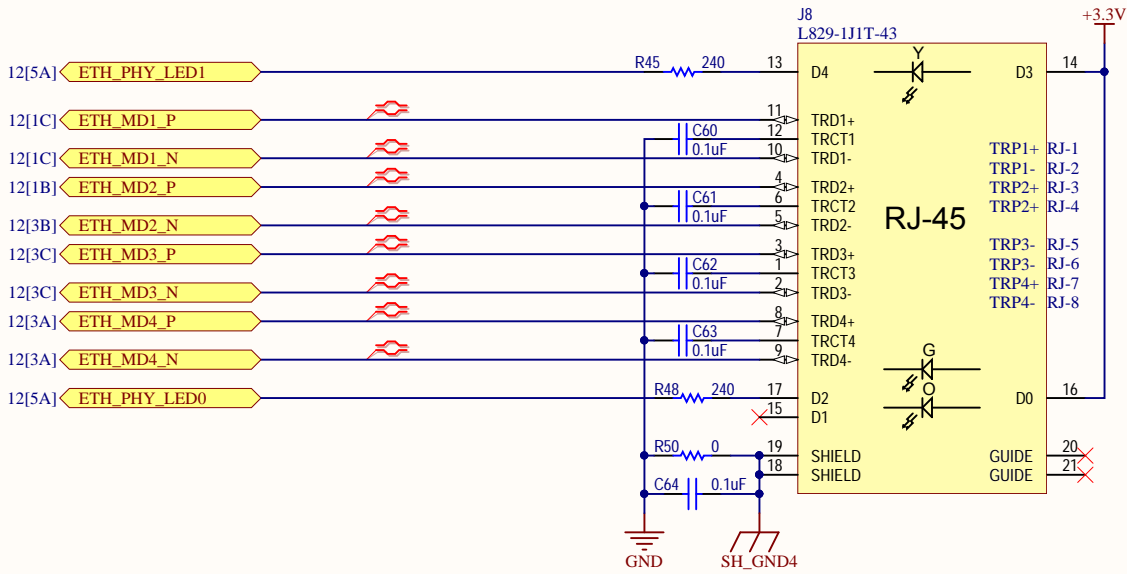
uSD, MAC, GiGE, SATA I/F



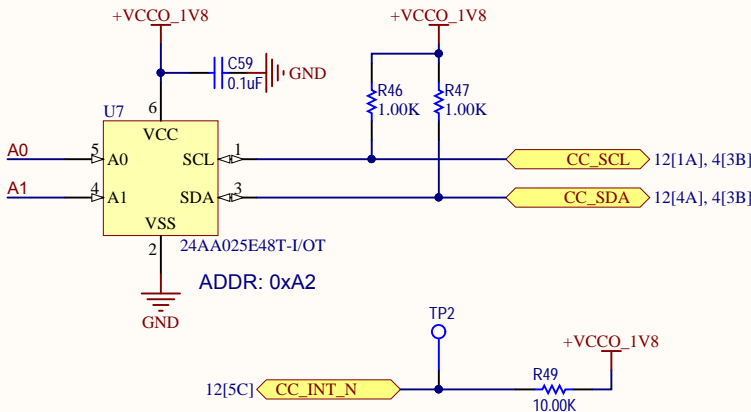
Layout Note:
SATA: GSSG on caps.
100 ohm +/- 15% diff routing.

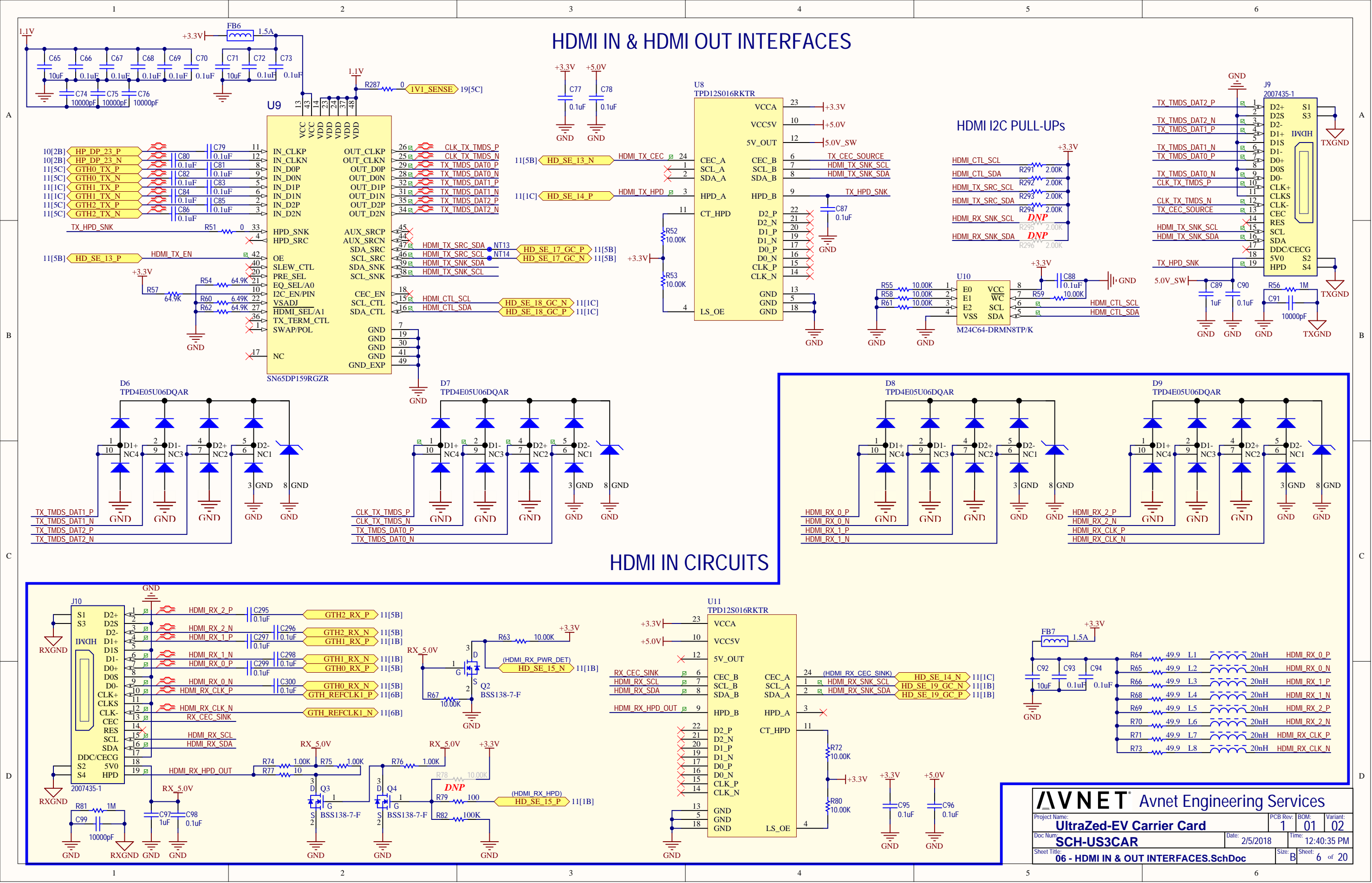


**SOM Gb ETH
10/100/1000**

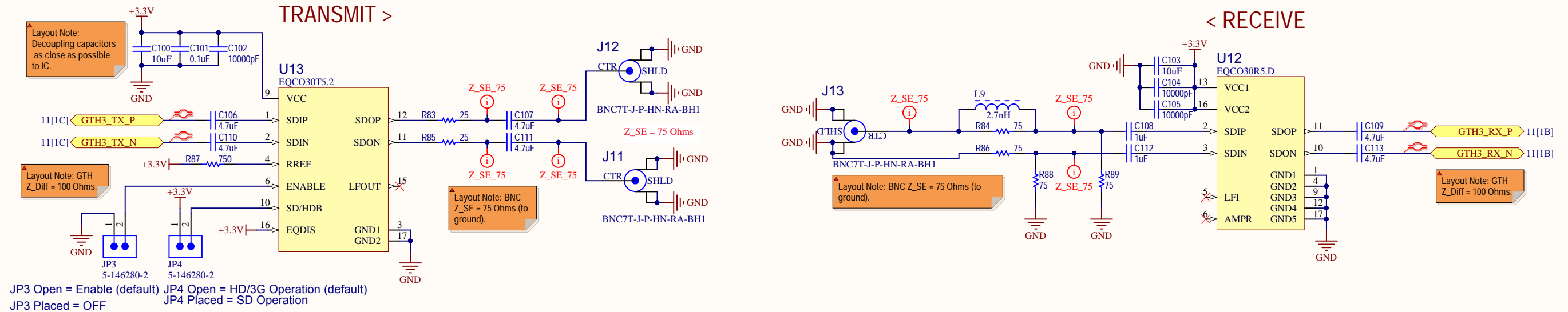


MAC ID - EEPROM

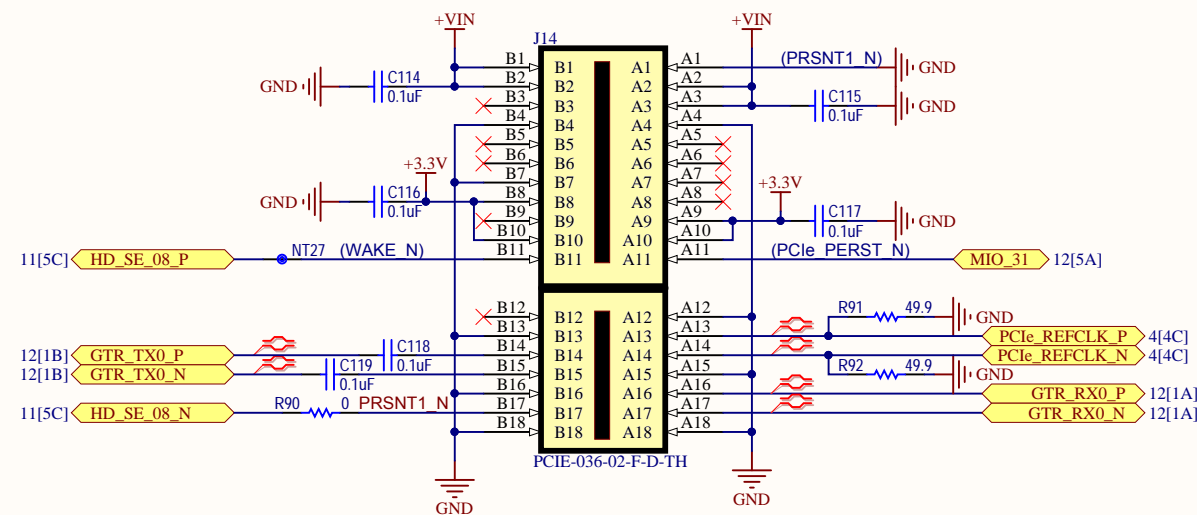




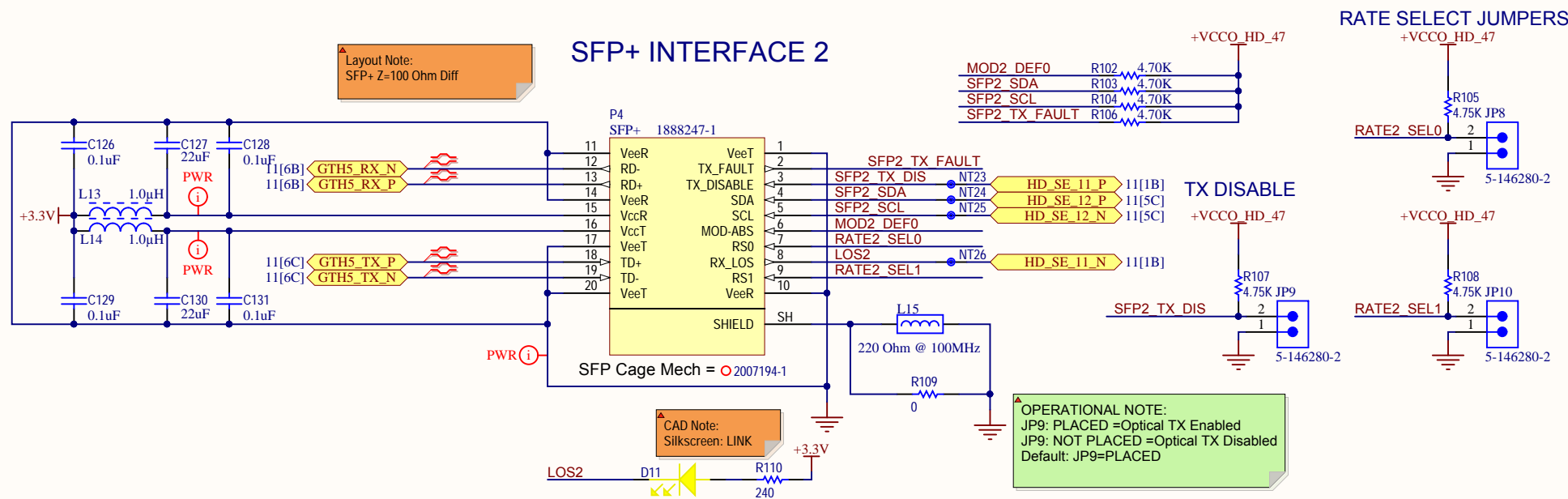
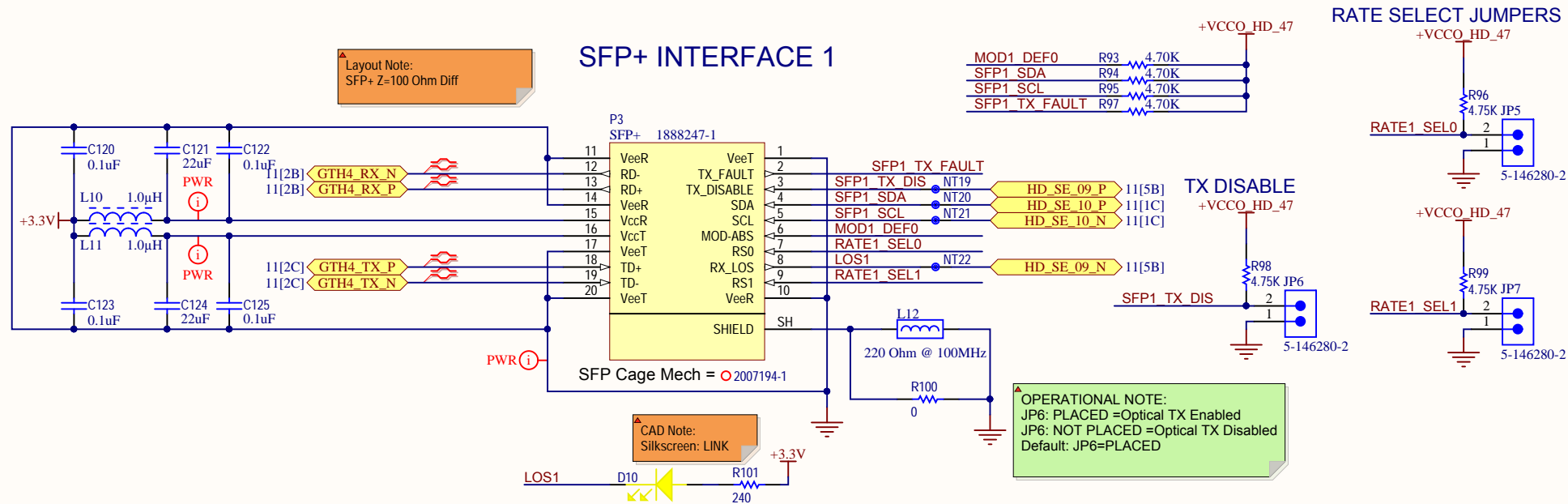
3G-SDI INTERFACE (SMPTE) & PCIe ROOT PORT



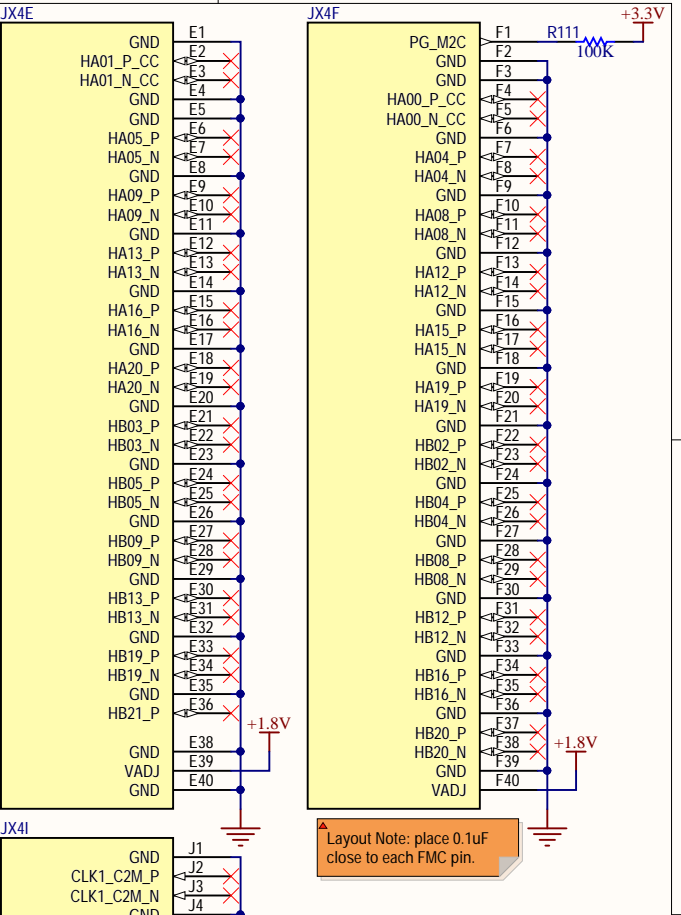
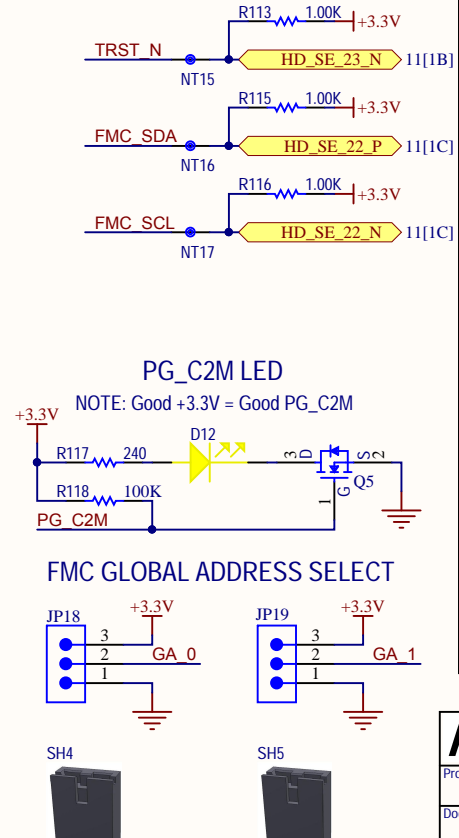
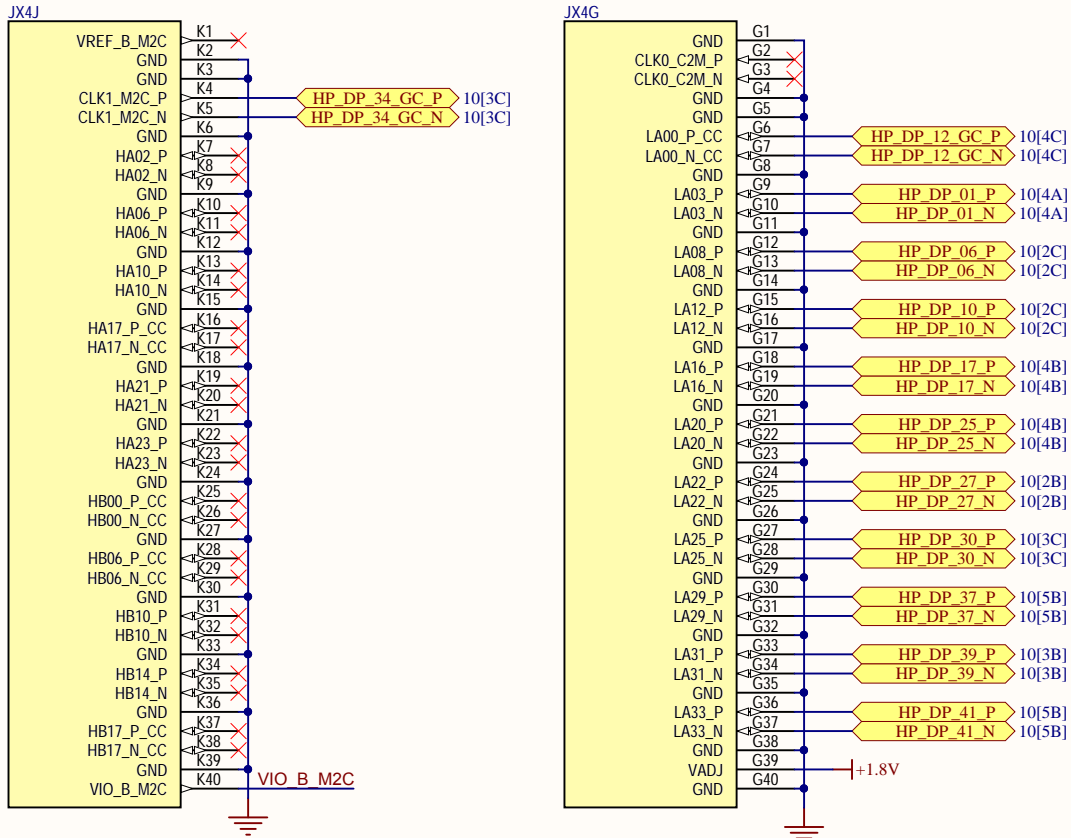
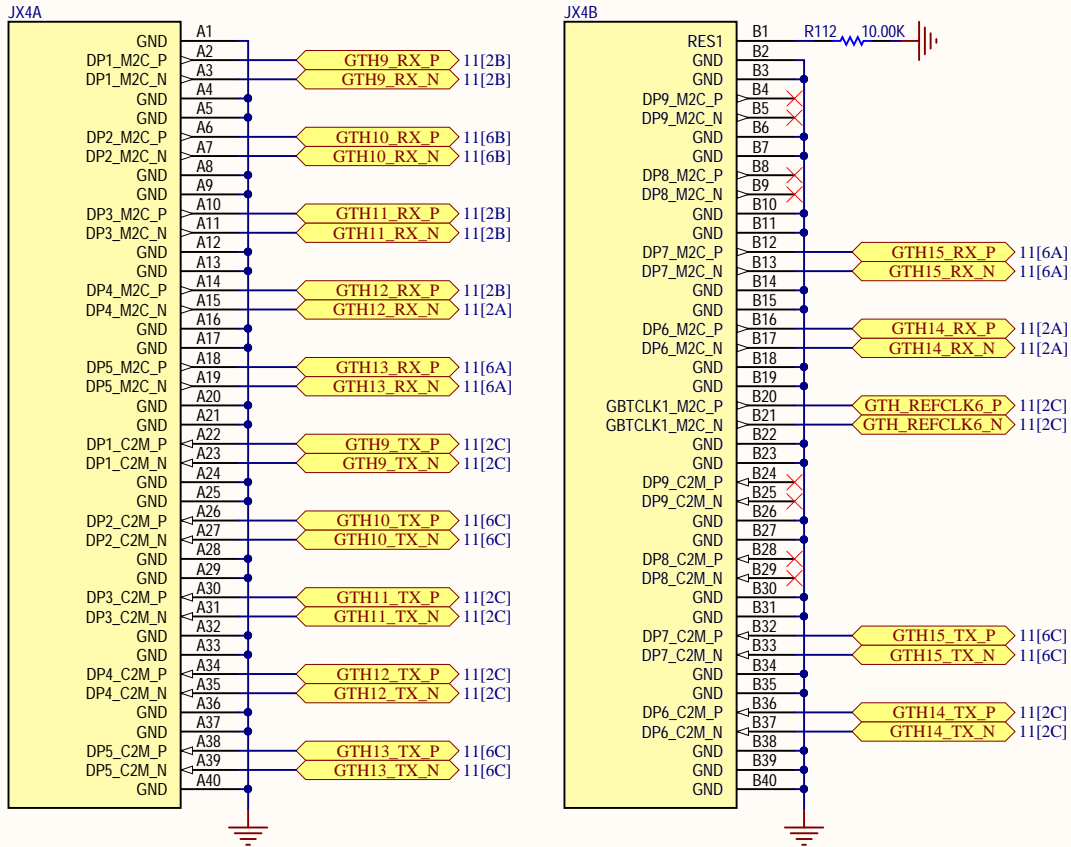
PCIe Root Port x1



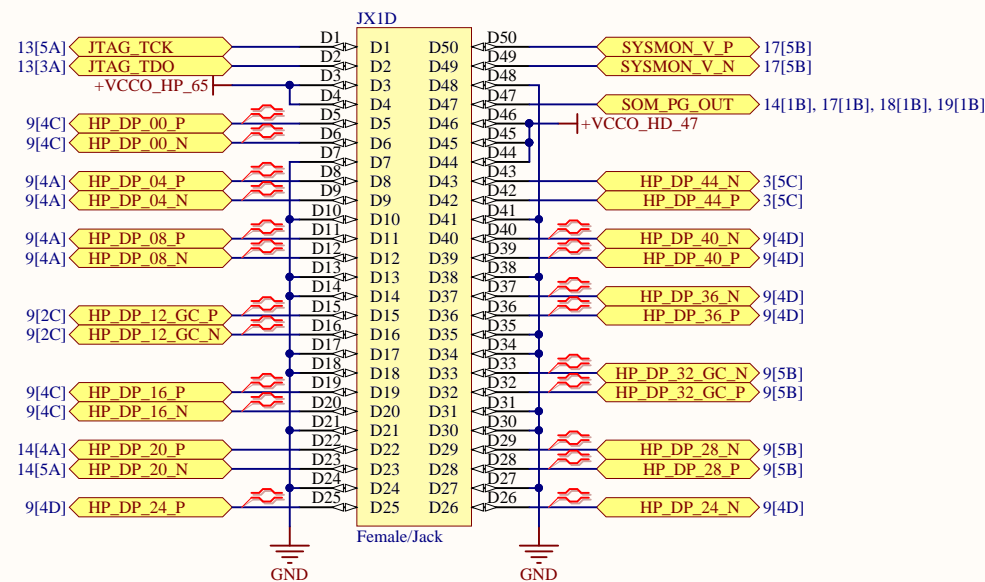
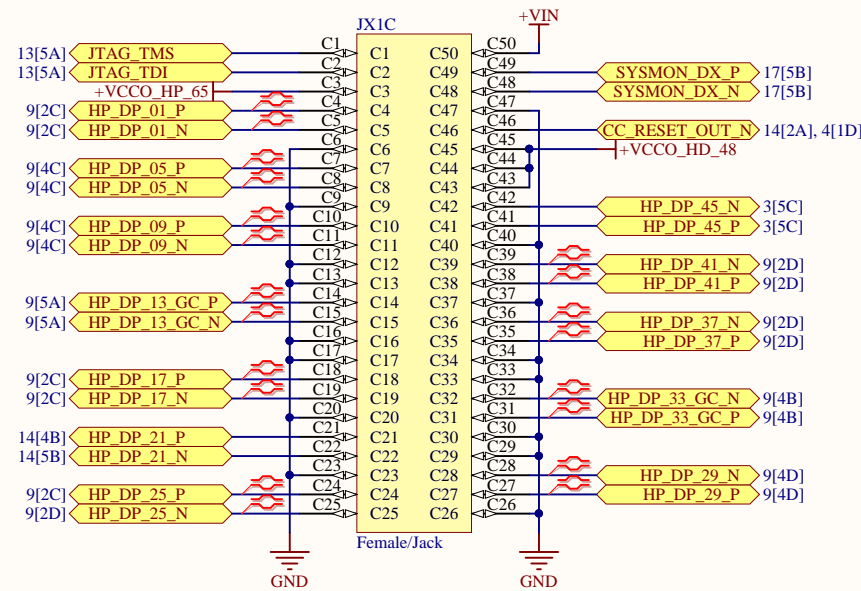
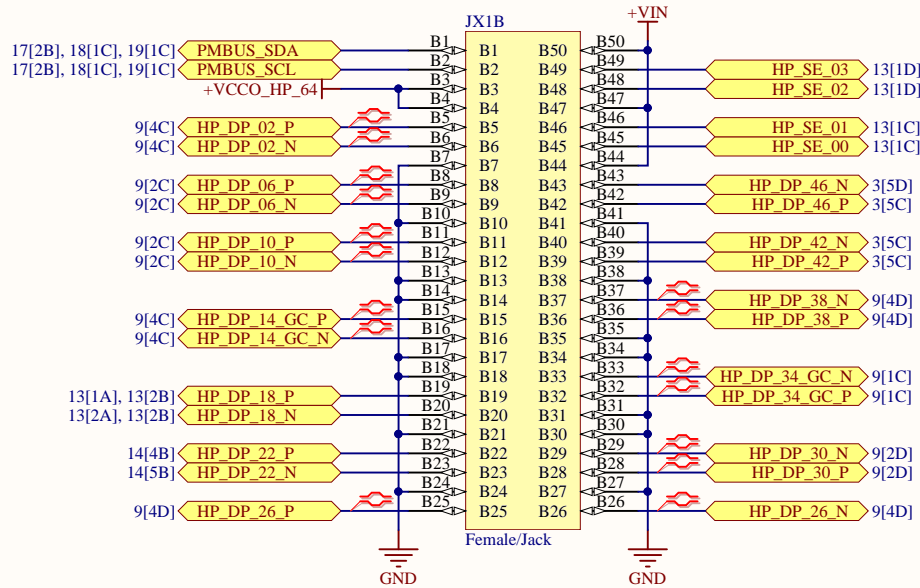
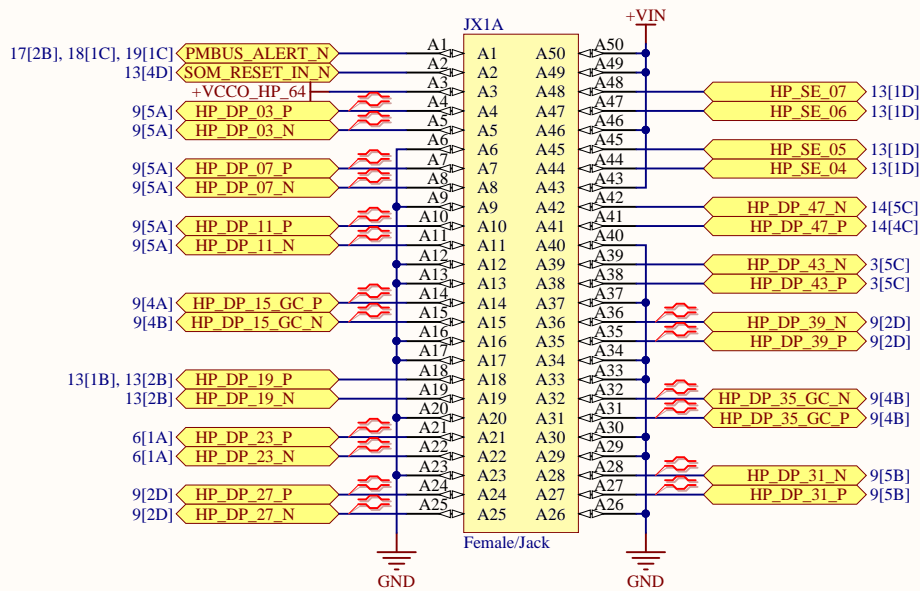
SFP+ INTERFACES



FMC HPC INTERFACE



JX1 CONNECTOR

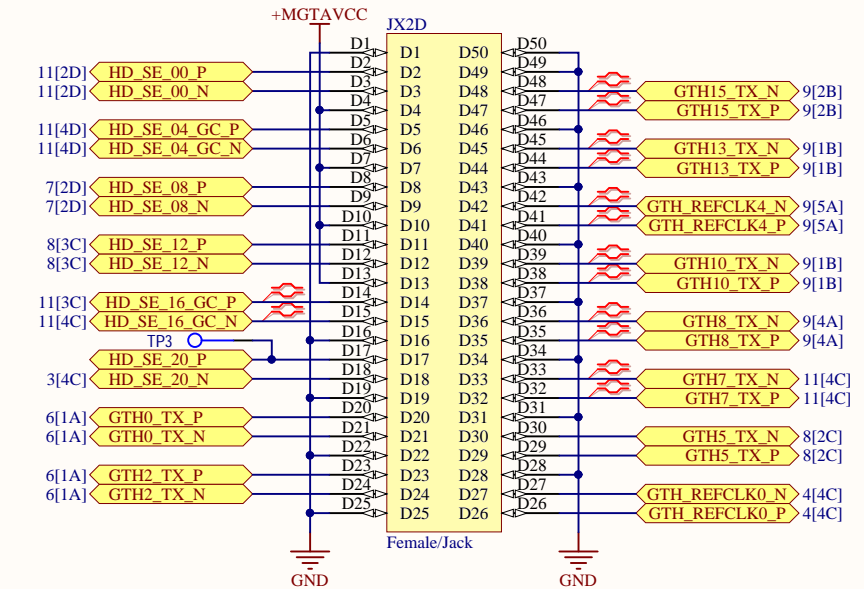
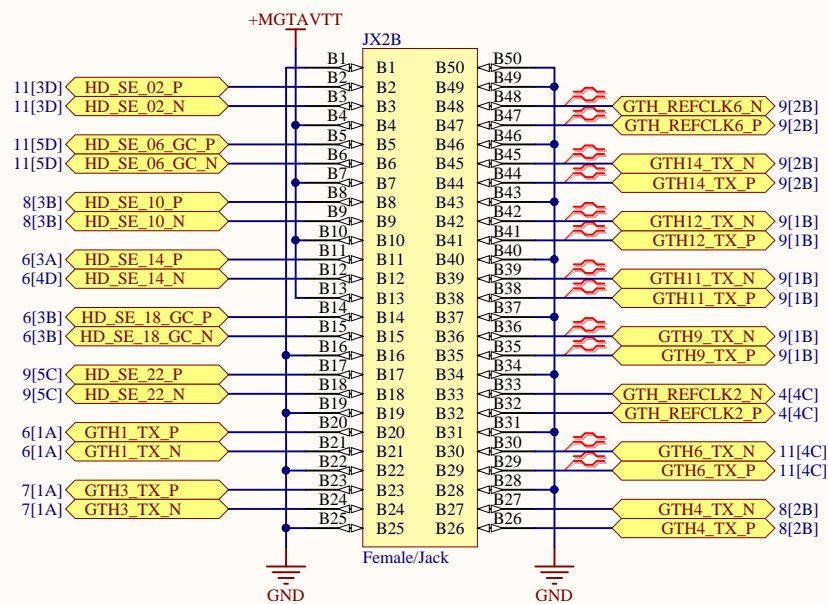


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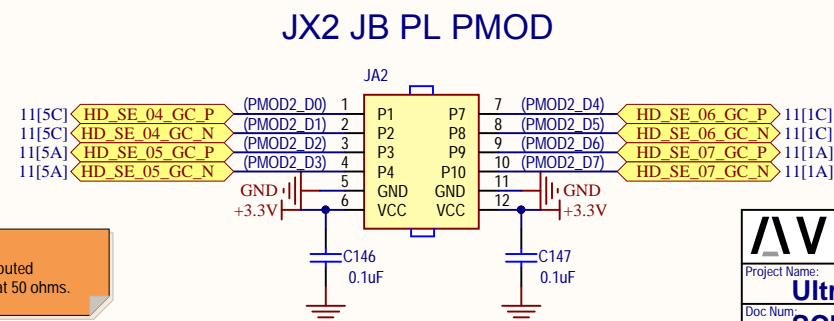
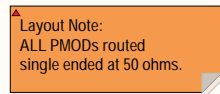
Project Name:	UltraZed-EV Carrier Card	PCB Rev:	BOM:	Variant:
Doc Num:	SCH-US3CAR	Date:	2/5/2018	Time: 12:40:37 PM
Sheet Title:	10 - JX1 CONNECTOR.SchDoc	Size:	B	Sheet: 10 of 20

Layout Notes:

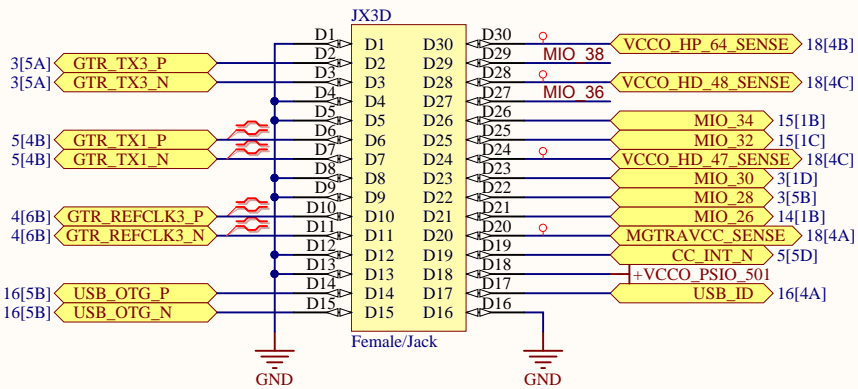
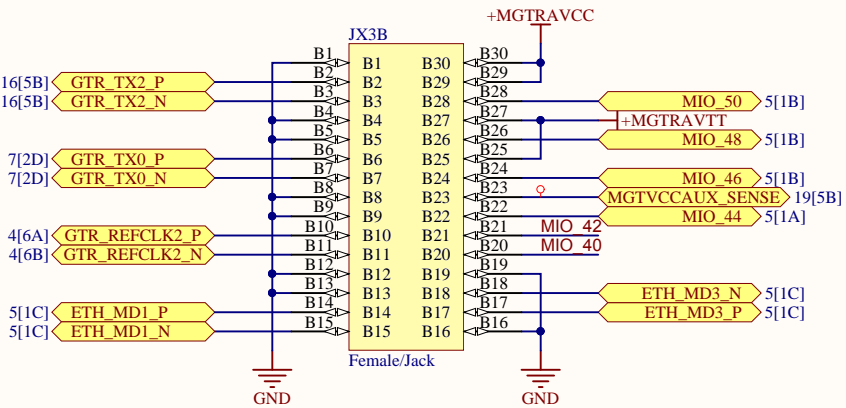
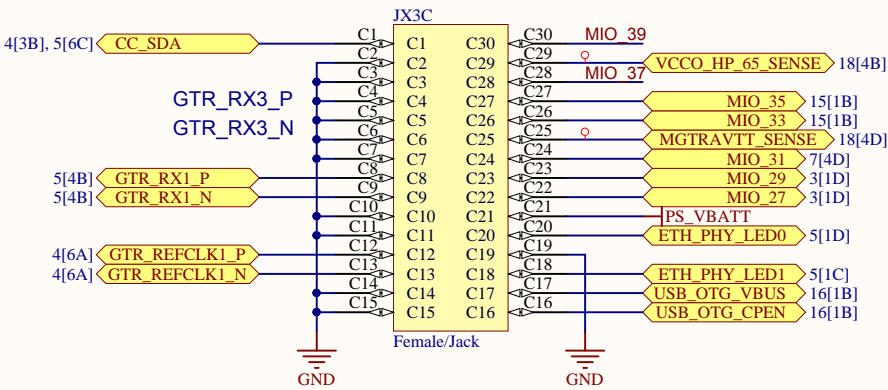
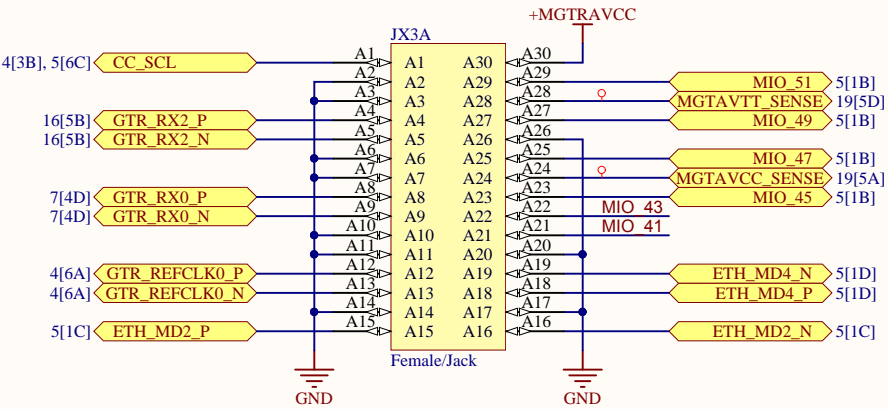
- 1) NO PIN SWAPPING ALLOWED.
- 2) Differential pairs: Route at 100 ohms unless otherwise noted.



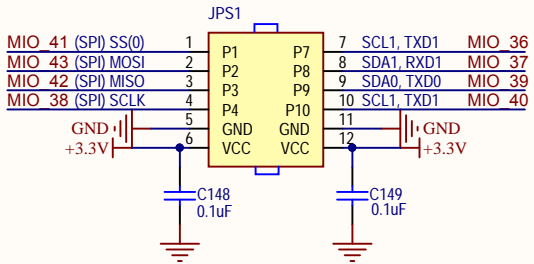
11[5C]	HD_SE_16_GC_P	R119	0	HD_SE_16_GC_N	11[5C]
11[2B]	GTH6_RX_N	R120	0	GTH6_TX_N	11[2C]
11[2B]	GTH6_RX_P	R121	0	GTH6_TX_P	11[2C]
11[6B]	GTH7_RX_N	R122	0	GTH7_TX_N	11[6C]
11[6B]	GTH7_RX_P	R123	0	GTH7_TX_P	11[6C]



JX3 CONNECTOR



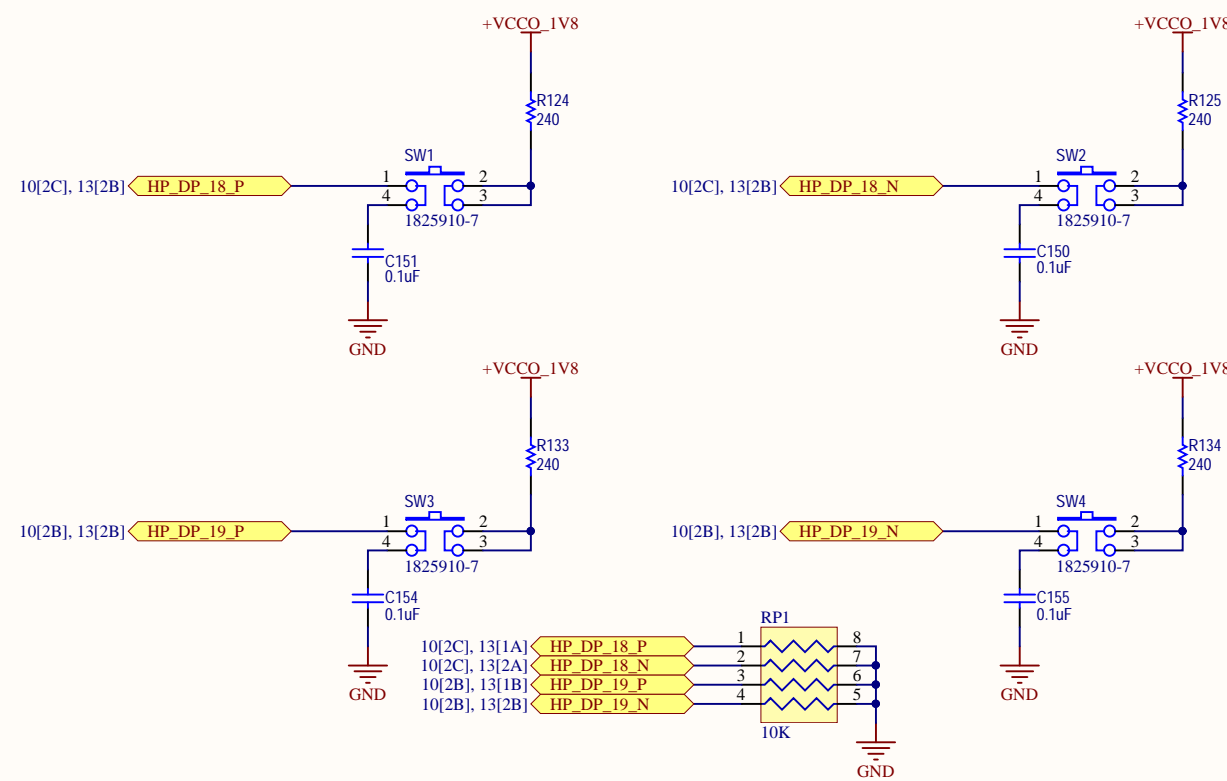
PS PMOD



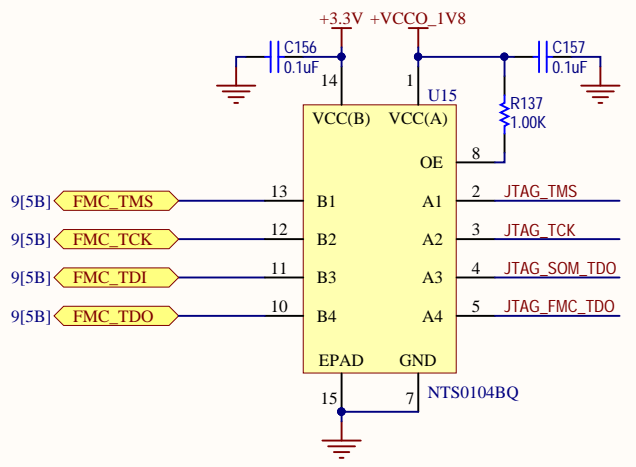
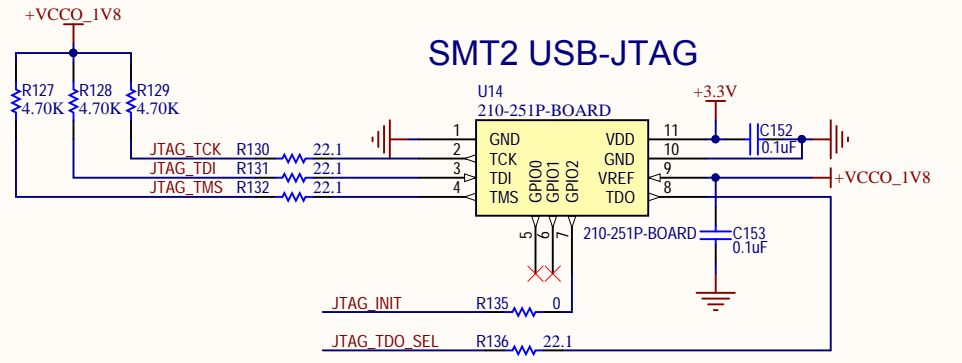
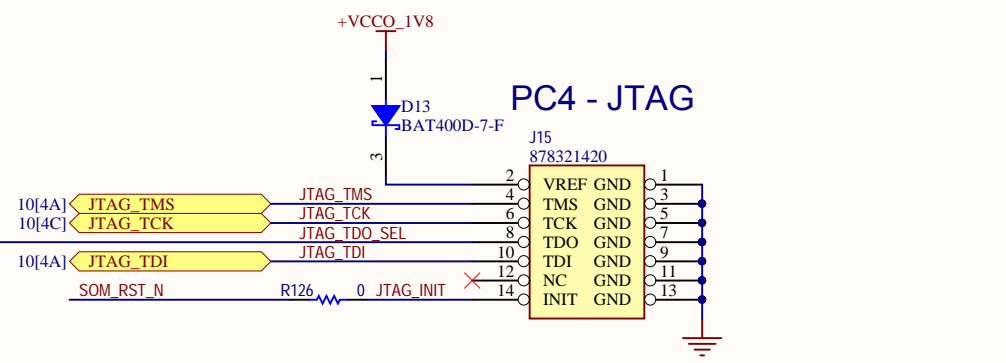
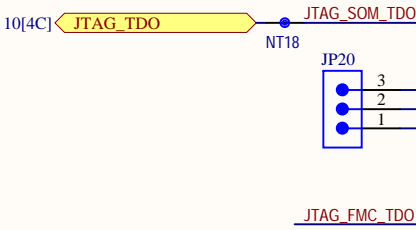
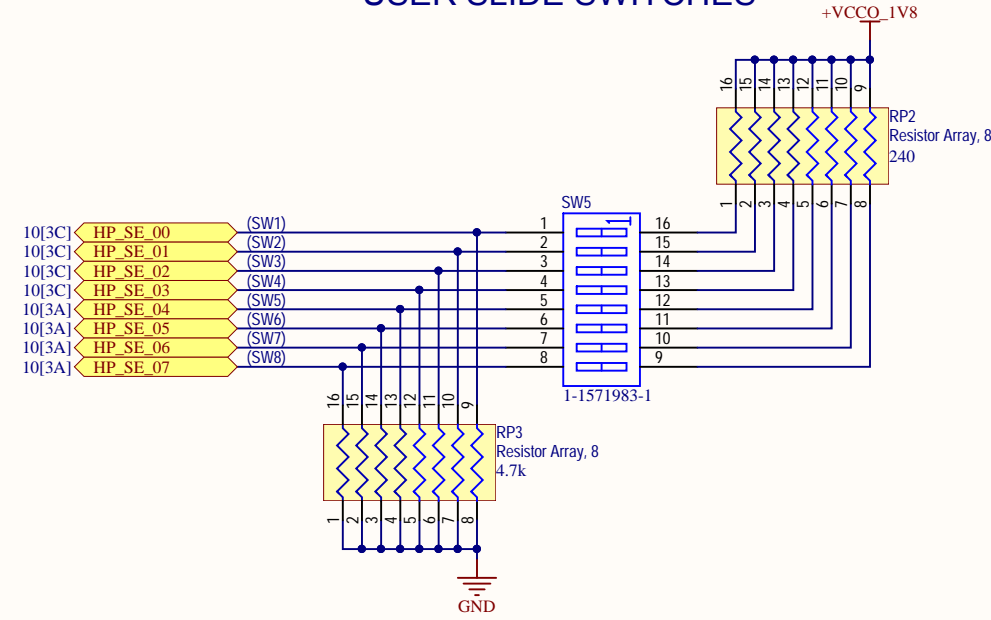
Layout Note:
ALL PMOD signals
routed at 50 ohm SE.

SWITCHES & JTAGS

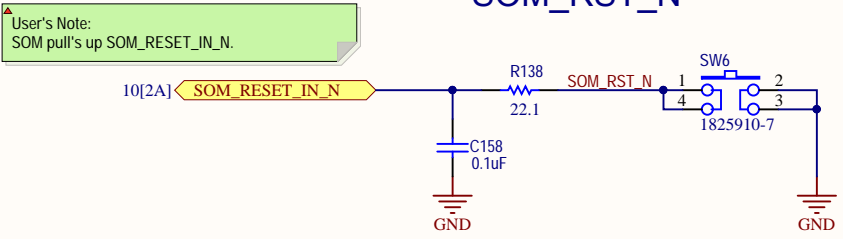
USER PUSH BUTTONS



USER SLIDE SWITCHES



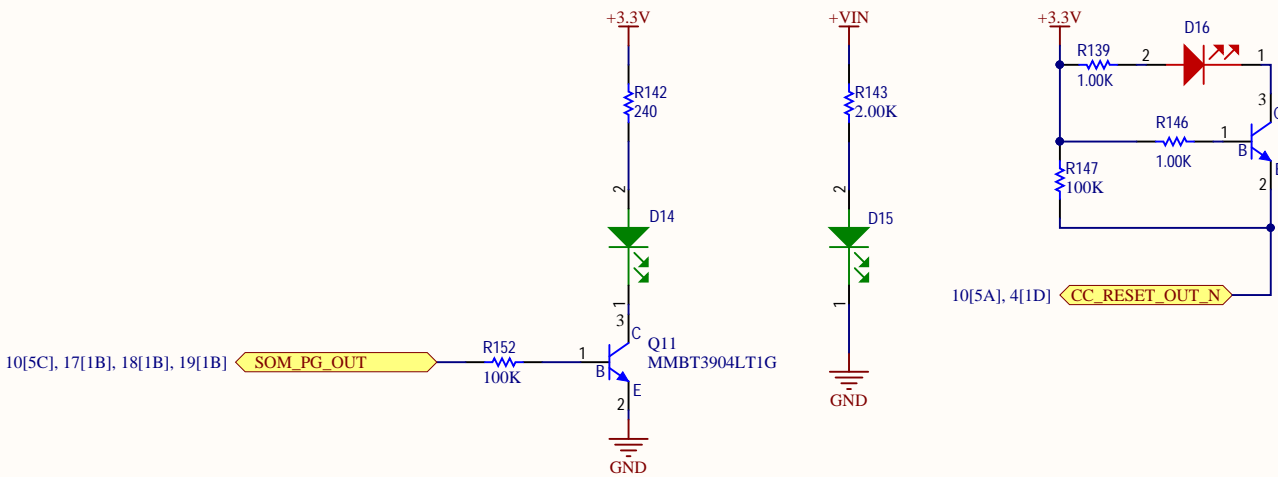
SOM_RST_N



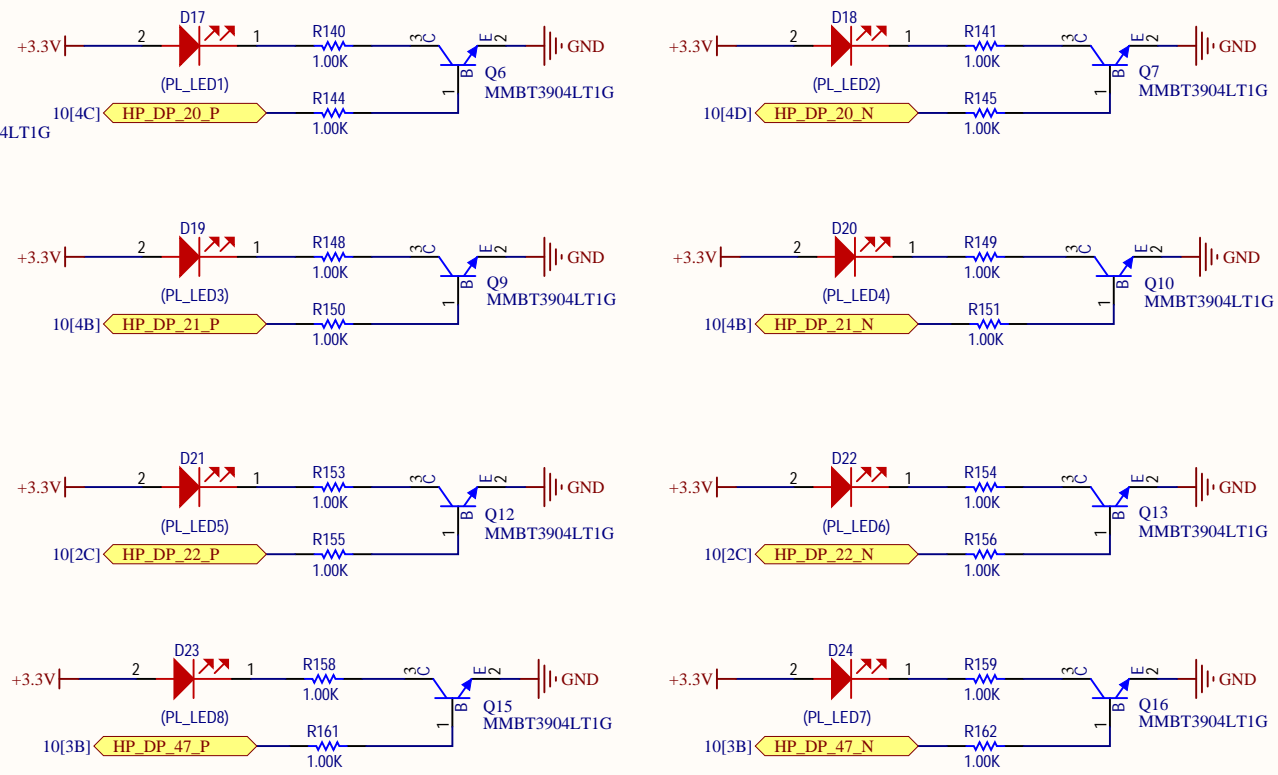
User's Note:
SOM pull's up SOM_RESET_IN_N.

LED INDICATORS

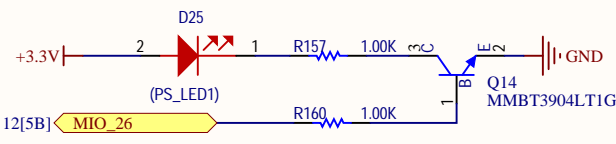
PG MODULE VIN_HDR_LED SOM_RESET_LED



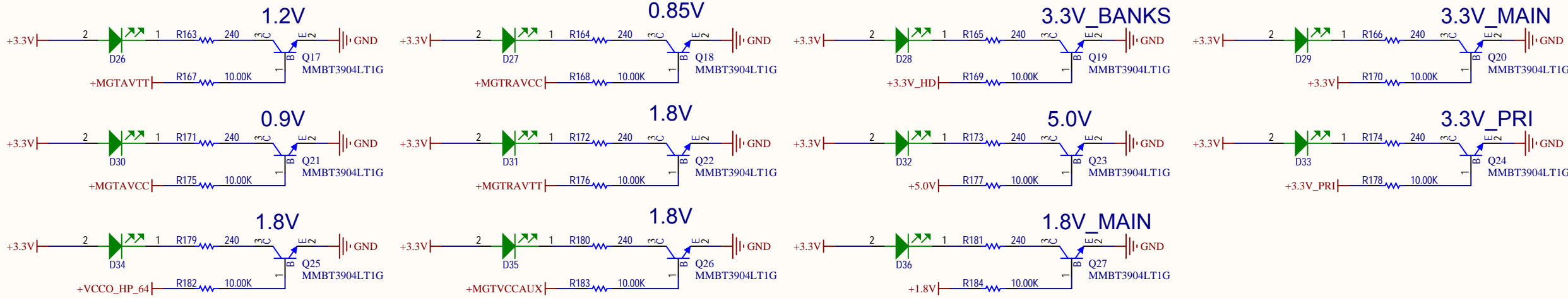
PL USER LEDS X8



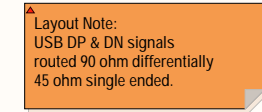
PS USER LED



POWER SUPPLY LEDS

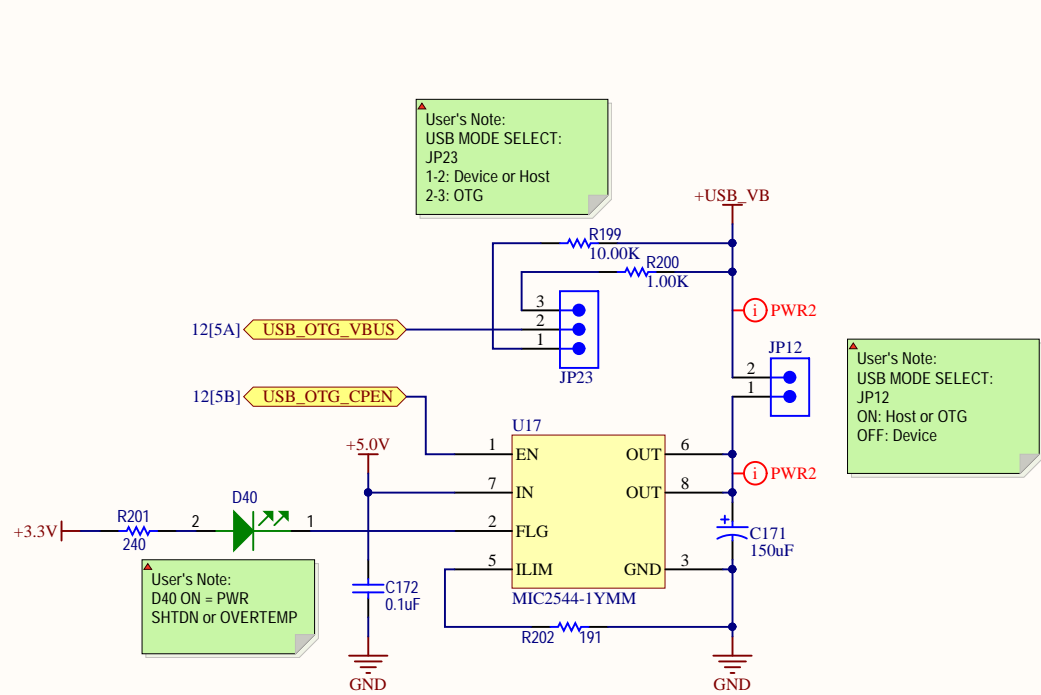


DUAL USB UART

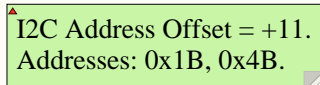


AVNET Avnet Engineering Services				
Project Name: UltraZed-EV Carrier Card		PCB Rev: 1	BOM: 01	Variant: 02
Doc Num: SCH-US3CAR		Date: 2/5/2018	Time: 12:40:40 PM	
Sheet Title: 15 - DUAL UARTs.SchDoc			Size: B	Sheet: 15 of 20

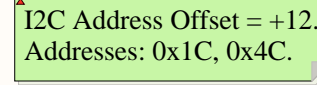
USB 2.0/3.0 INTERFACE



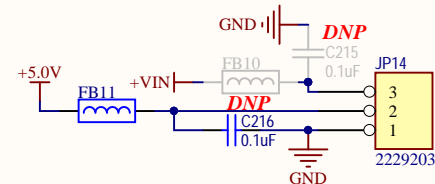
**NOT ATX COMPATIBLE
120W+ POWER ONLY**

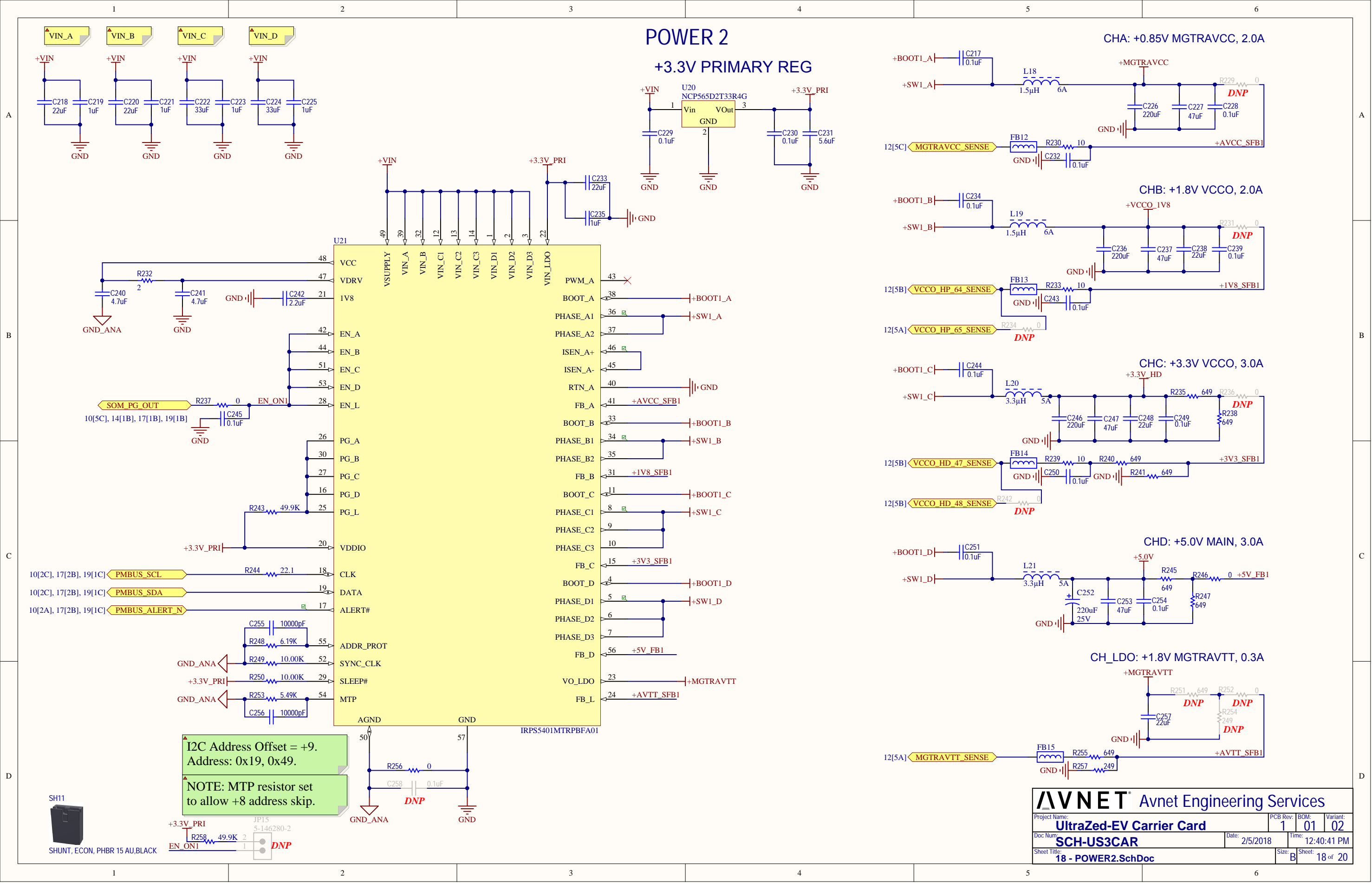


+PS_VBATT_TEST

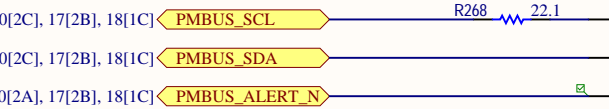
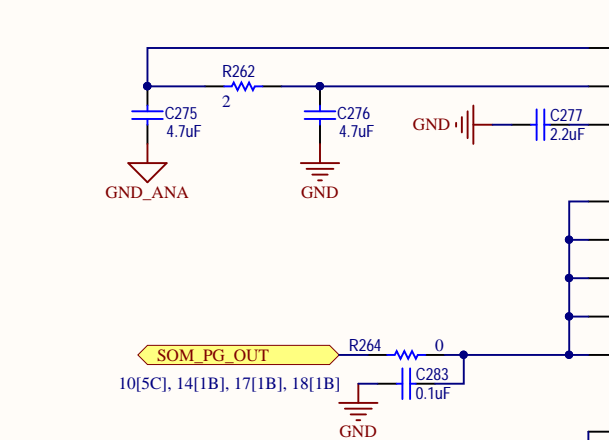
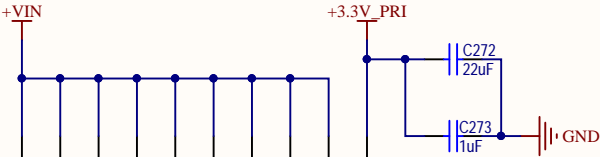
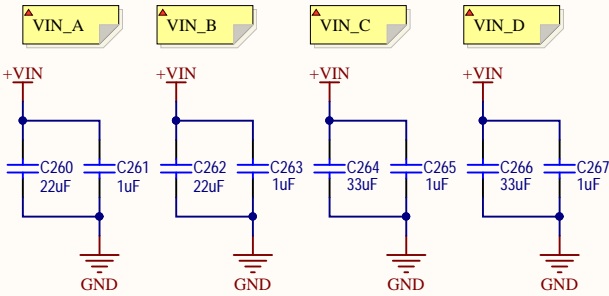


SYSMON HEADER



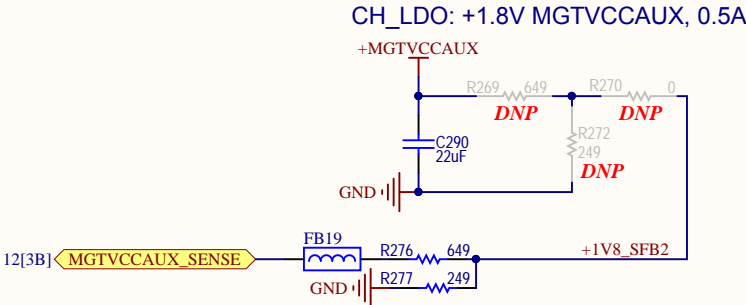
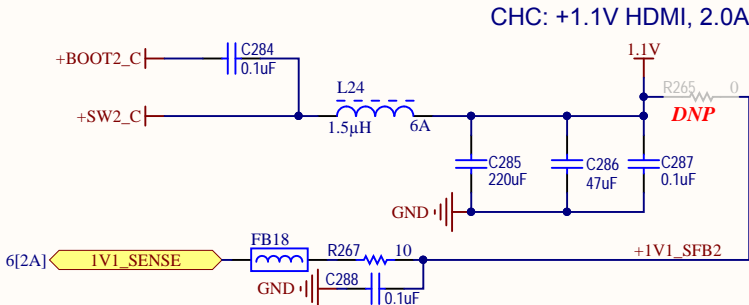
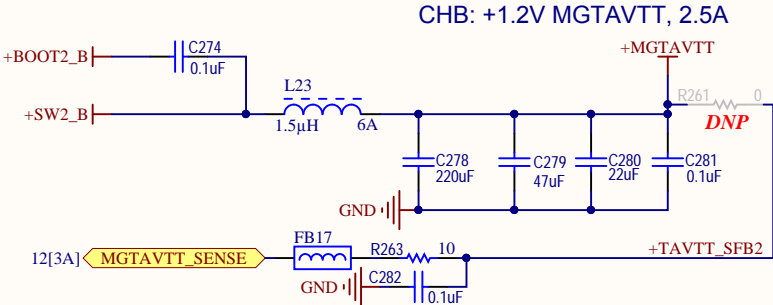
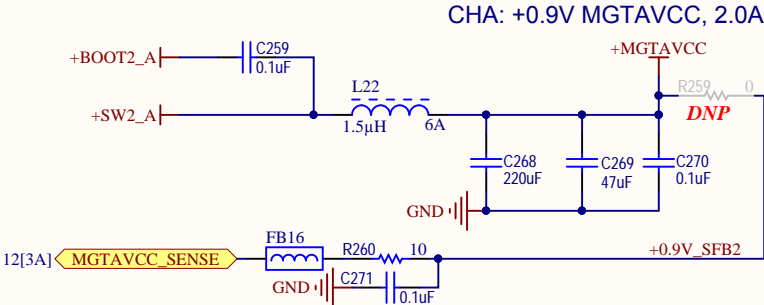
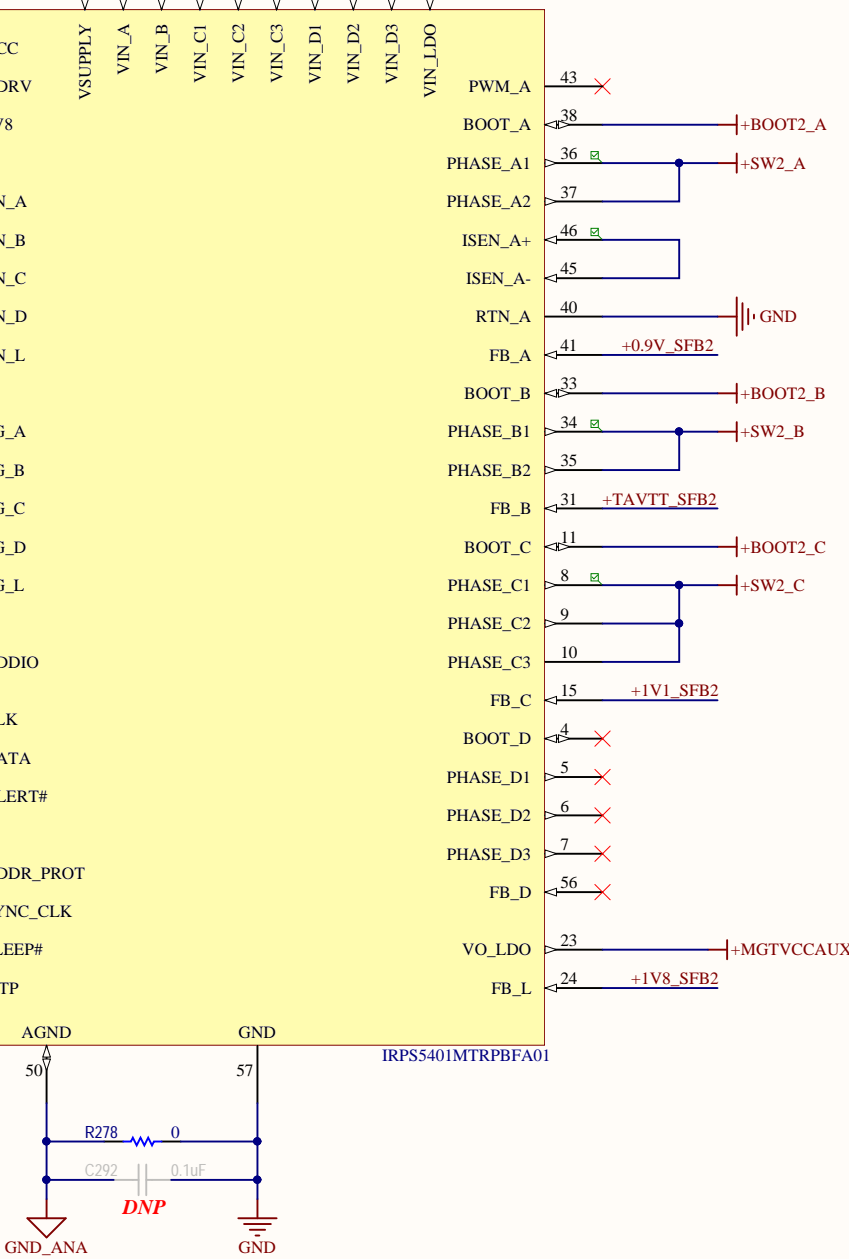


POWER 3




I2C Address Offset = +10.
Addresses: 0x1A, 0x4A.

NOTE: MTP resistor set
to allow +9 address skip.



Assembly: Mechanicals:

Label1
BD-XXXX-XXXX-G
Label, Product

Label2

XXXXXXX
Label, Serial Number

ESD1

ESD Bag

Label_ESD1

Label, ESD

SS2

Trash Can-Silk Screen

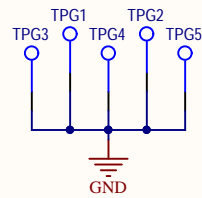
SS1

RoHS 2-Silk Screen



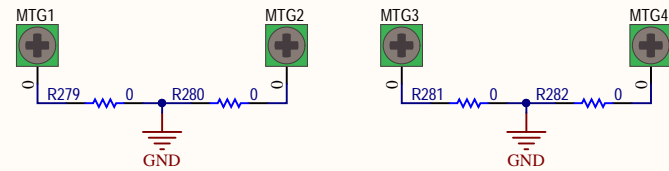
PCB

XXX-XXX-PCB-X
PCB PN (In Copper)

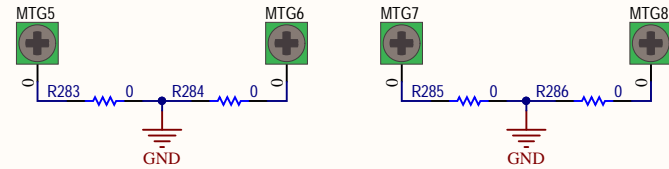


BACKPAGE

SOM Mounting Holes:



Edge stand-off mounting holes:



BMPR1
BUMPER CYLIN 0.375" DIA BLK
BMPR3
BUMPER CYLIN 0.375" DIA BLK
BMPR5
BUMPER CYLIN 0.375" DIA BLK
BMPR7
BUMPER CYLIN 0.375" DIA BLK
BMPR9
BUMPER CYLIN 0.375" DIA BLK

BMPR2
BUMPER CYLIN 0.375" DIA BLK
BMPR4
BUMPER CYLIN 0.375" DIA BLK
BMPR6
BUMPER CYLIN 0.375" DIA BLK
BMPR8
BUMPER CYLIN 0.375" DIA BLK
BMPR10
BUMPER CYLIN 0.375" DIA BLK