

1

2

3

4

A

A

Jetson Orin NX Laptop Motherboard

Revision A - Prototype

Status: Submitted for fabrication on 2025-11-18.

B

B

Note: This is a first-article prototype; design and documentation are subject to change in future revisions.

C

C

D

D

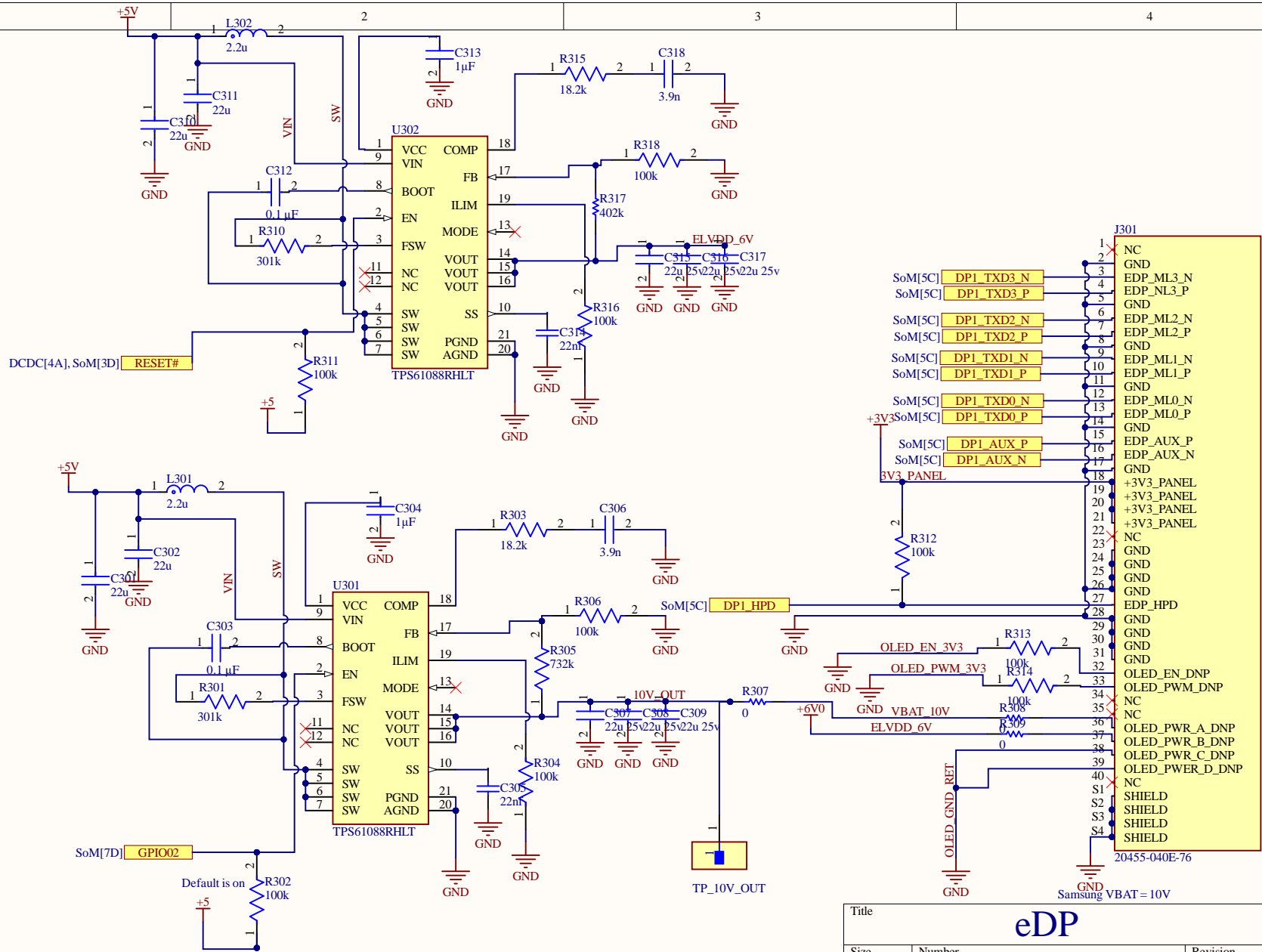
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1

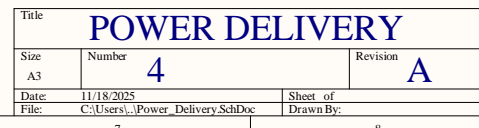
2

3

4



Title			eDP	Revision
Size	Number			
A4	3		A	
Date:	11/18/2025		Sheet of	
File:	C:\Users\...\eDP.SchDoc		Drawn By:	





BATTERY

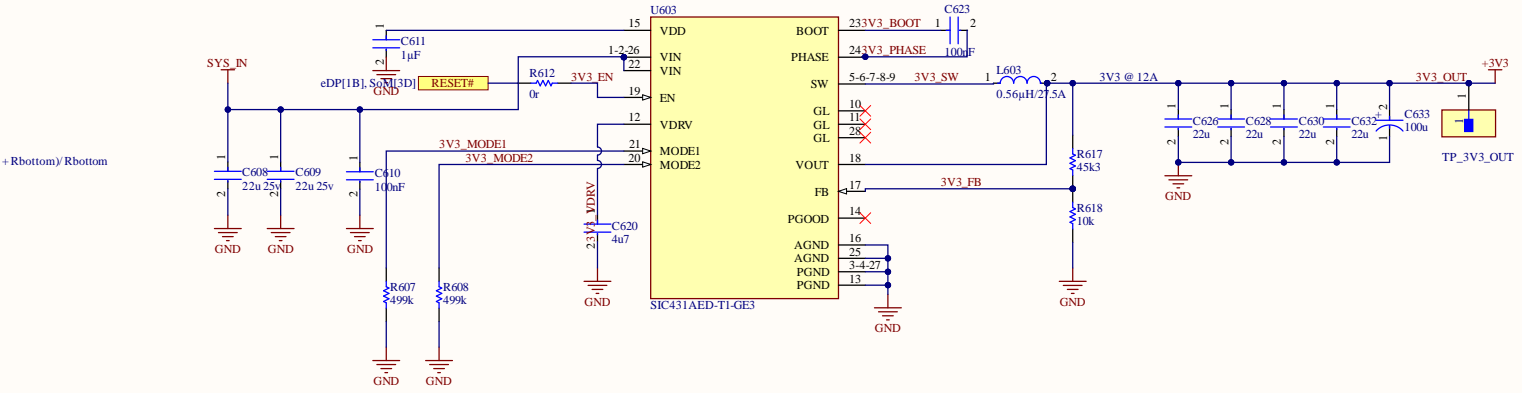
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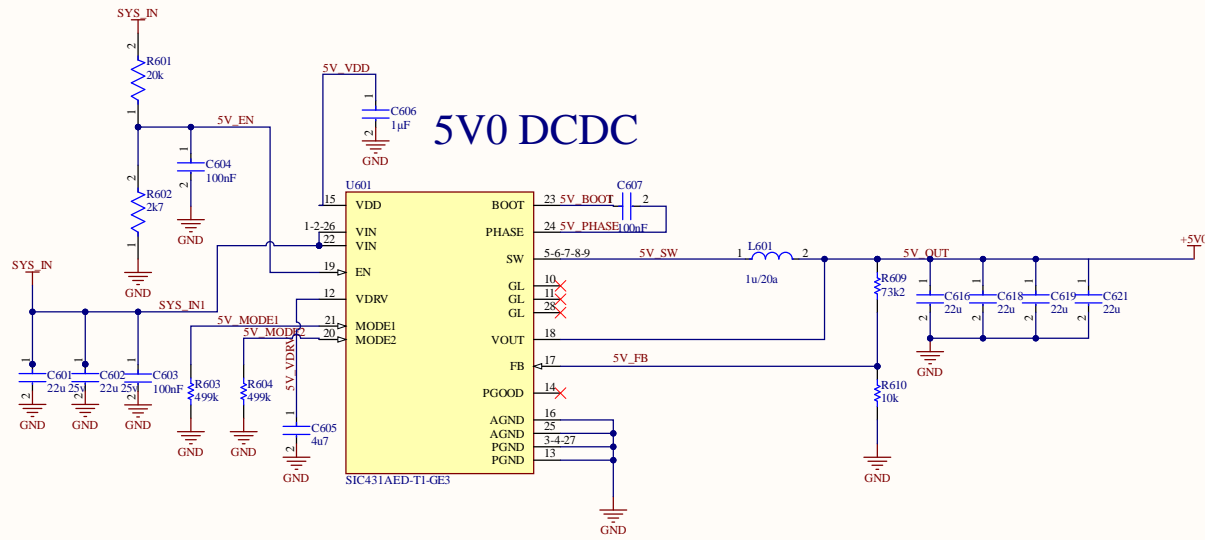
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3V3 DCDC

$$VOUT = 0.6 * (Rtop + Rbottom) / Rbottom$$

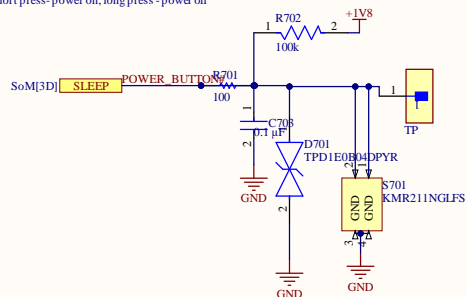


5V0 DCDC



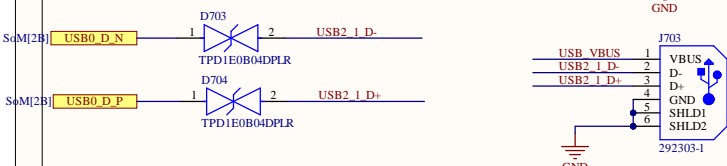
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Size	Number	Revision	
A3	6	A	
Date:	11/18/2025	Sheet of	
File:	C:\Users\...\DCDC.SchDoc	Drawn By:	

Power button
short press - power on, long press - power off

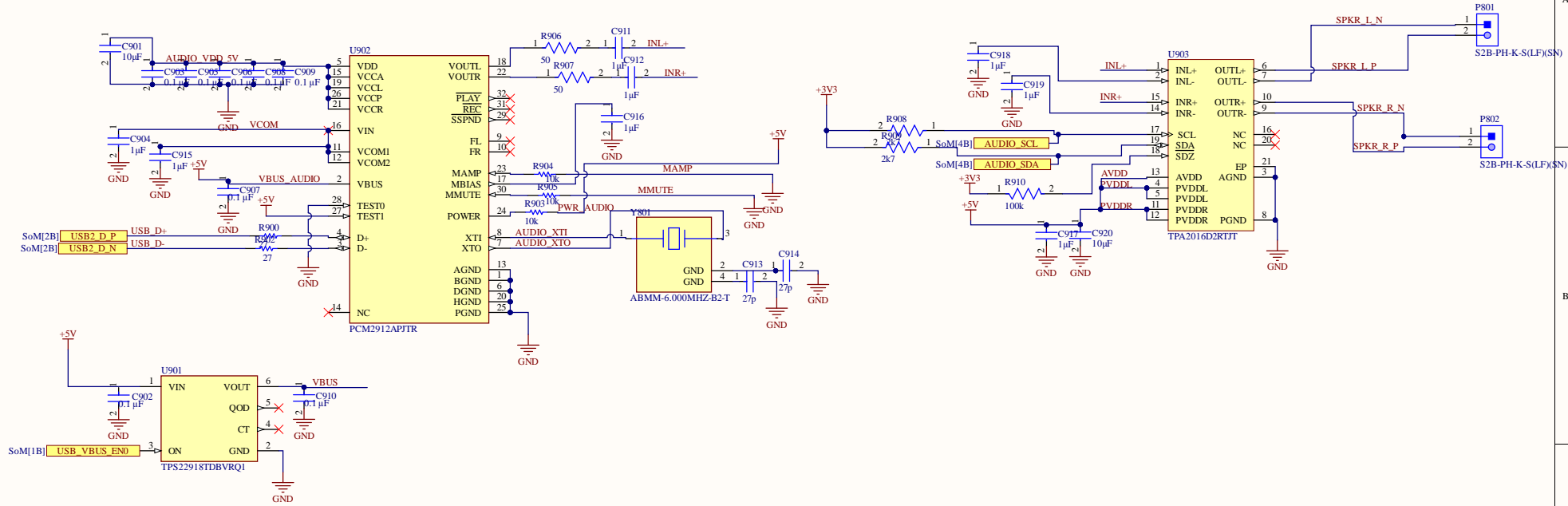


The diagram shows the TC2050-IDC-NL module (J701) with the following connections:

- Pin 1: UART RX (from UART2_TXD)
- Pin 2: UART TX (from UART2_RXD)
- Pin 10: +1V8
- Pin 5: GND

[illegible]

AUDIO



Title			AUDIO	
Size	Number	Revision		
A3	8	A		
Date:	11/18/2025	Sheet of		
File:	C:\Users\...\Audio.SchDoc	Drawn By:		

