

	s0_r	→
	s0_inv_r	→
	s1_r	→
	s1_inv_r	→
	s0_s	→
	s0_inv_s	→
	s1_s	→
	s1_inv_s	→
	inv_s	→
→ oe	inv_r	→
→ q0_data	s0_alu	→
→ q3_data	s0_inv_alu	→
→ 9_pins(i[],...)	s1_alu	→
→ 4_pins(a[],...)	s1_inv_alu	→
→ 4_pins(b[],...)	s0_q	→
→ 4_pins(f[],...)	s0_inv_q	→
→ 4_pins(c[],...)	s1_q	→
→ 4_pins(p[],...)	s1_inv_q	→
→ 4_pins(y_data[],...)	s0_re	→
→ ram0	s0_inv_re	→
→ ram3	s1_re	→
→ q0	s1_inv_re	→
→ q3	s_y	→
→ 4_pins(y_tri[],...)	s_inv_y	→
	g_lo	→
	p_lo	→
	ovr	→
	z	→
	reg_wr	→
	en_q	→
	16_pins(select_a_hi[],...)	→
	16_pins(select_b_hi[],...)	→