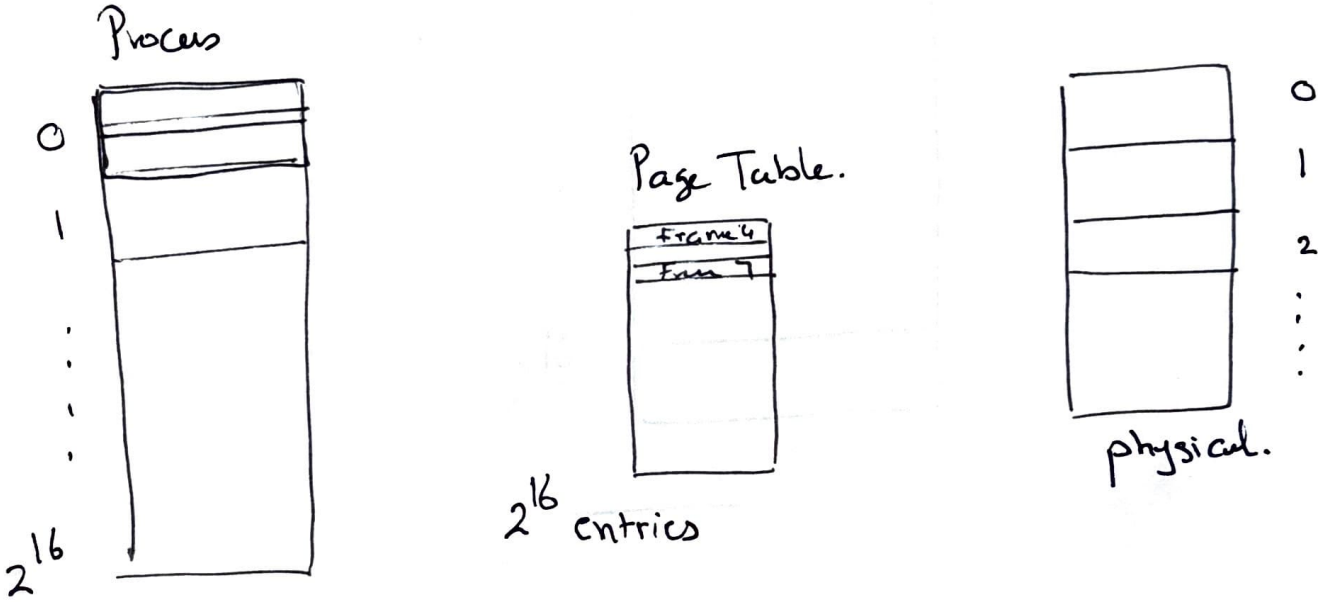
Hw# 4 P1

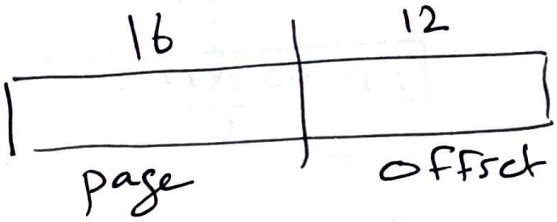
2^{32} bytes of Physical memory.

page size is 2^{12} bytes,

Logical address space is 2^{16} pages.

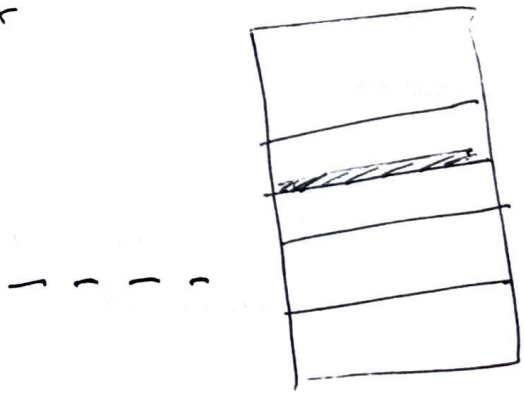


(a) 28



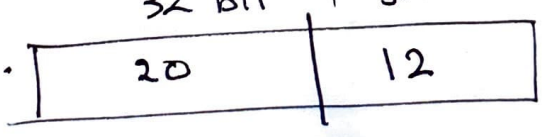
000 . . . 0

111 . . . 1



(b) 2^{12}

(c) 20



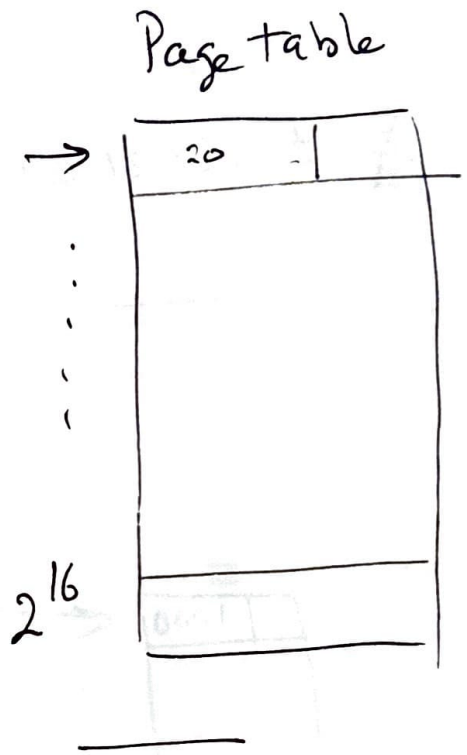
4 pages.

Evy page 4 bytes

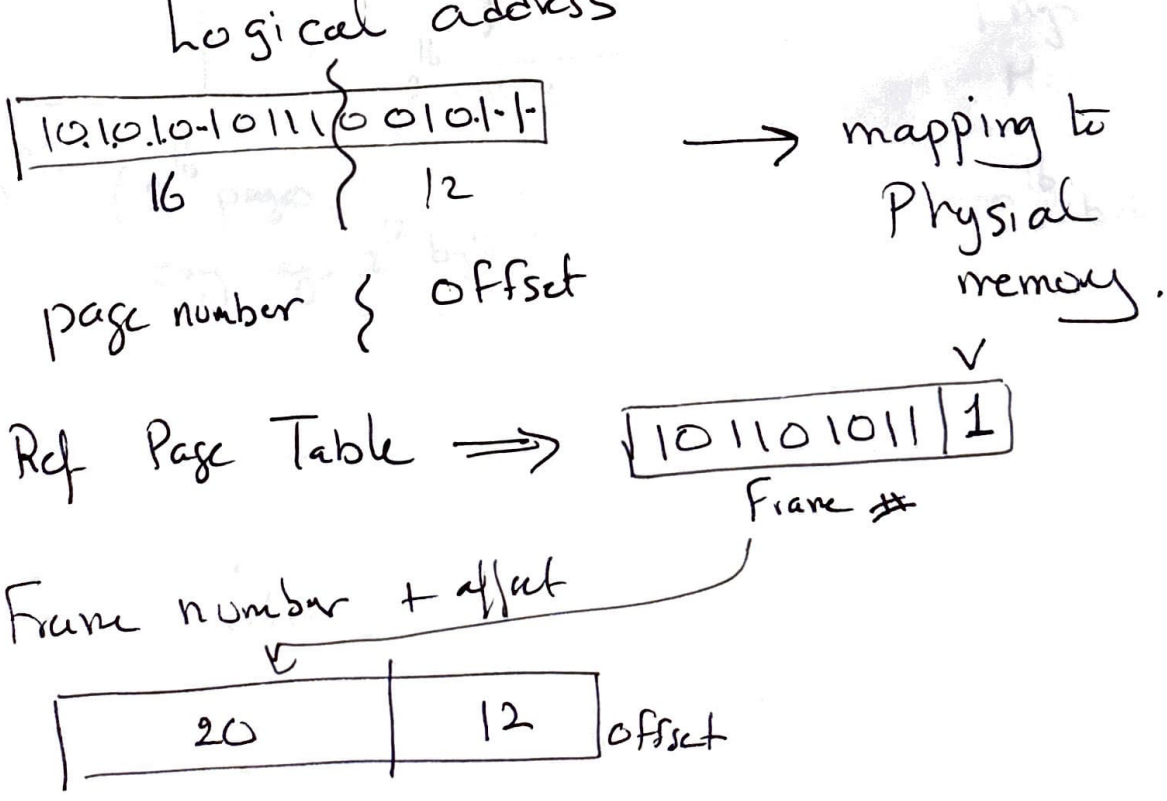
16 bytes.

[d] 2^{16} entries.

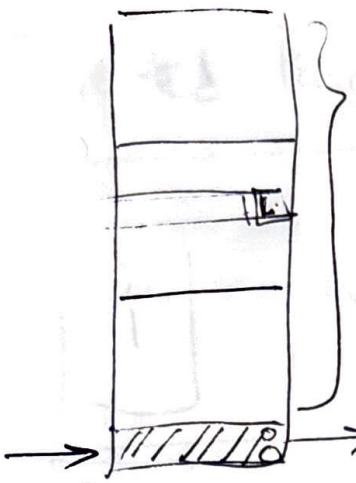
[e] 20 + 1 = 21



[E]

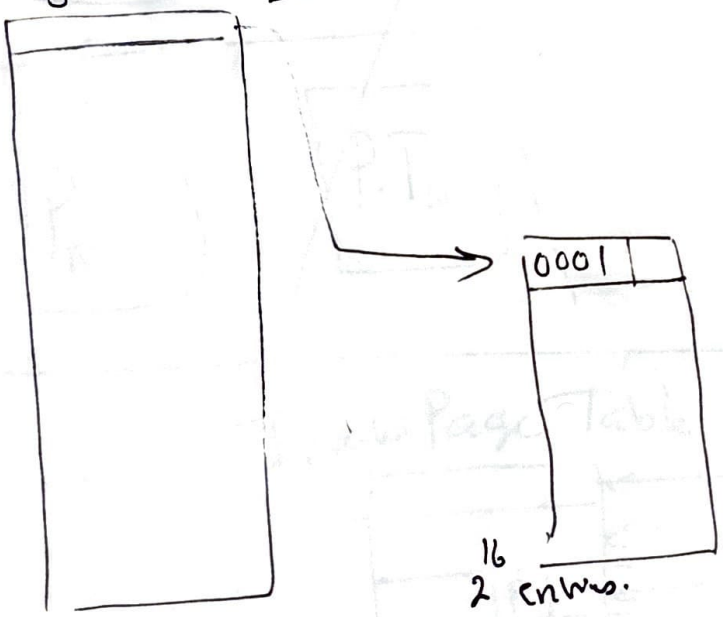


P.T



→ valid is set to ϕ.

Logical memory



2^{16} entries.

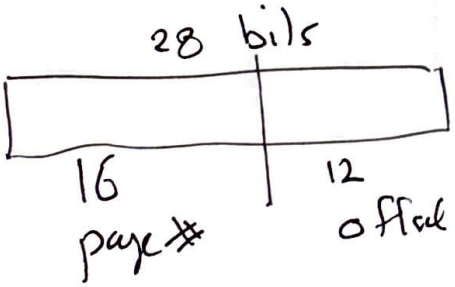
2^{28} (2^{16} pages, every page 2^{12} bytes)

Physical Memory.

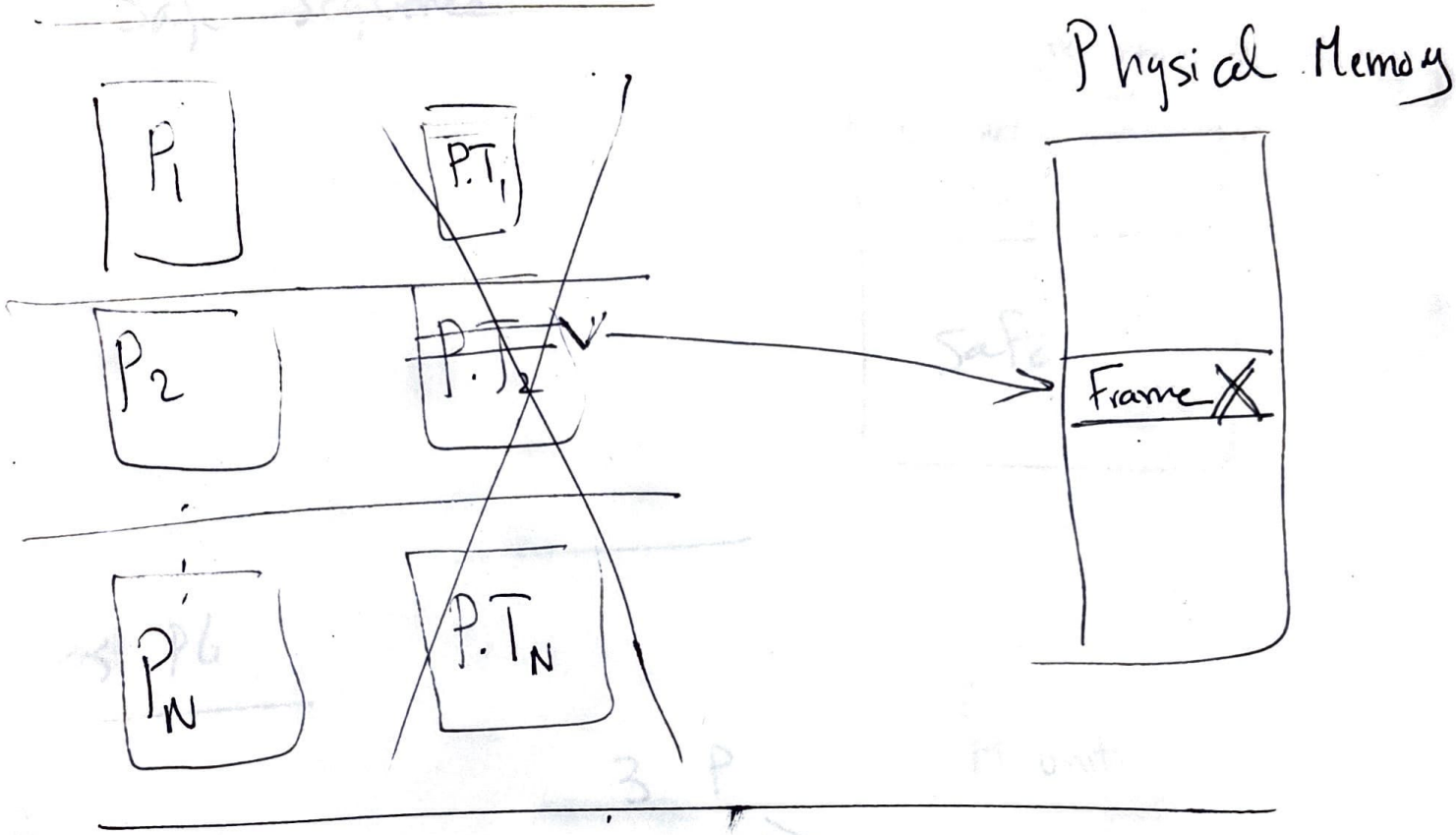
2^{16} bytes.

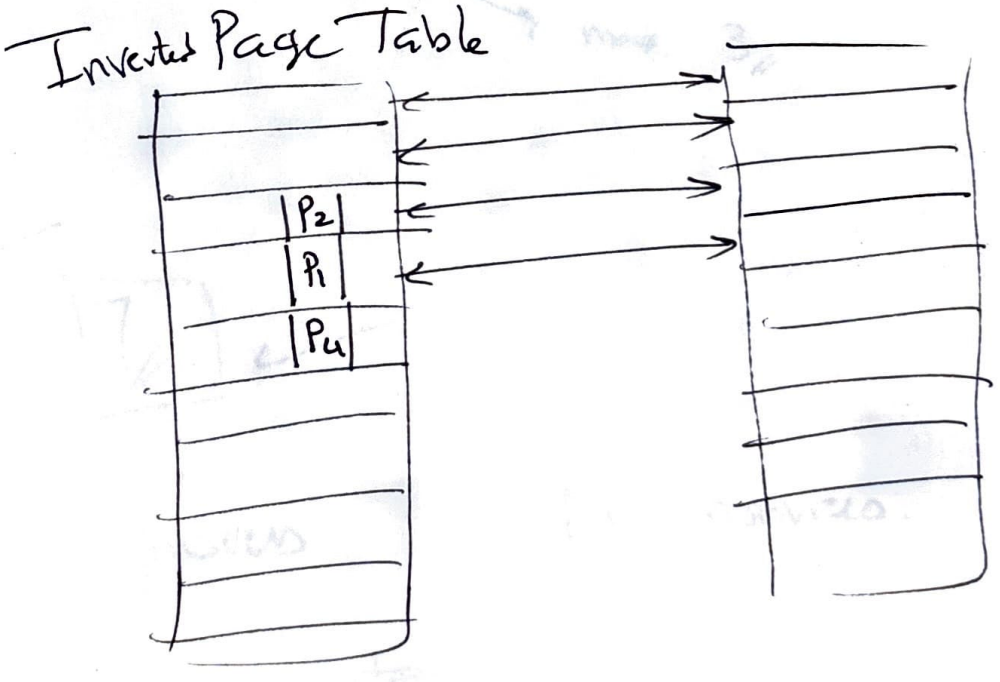
2^4 Frames.

$\frac{2^{16}}{2^{12}} = 2^4$



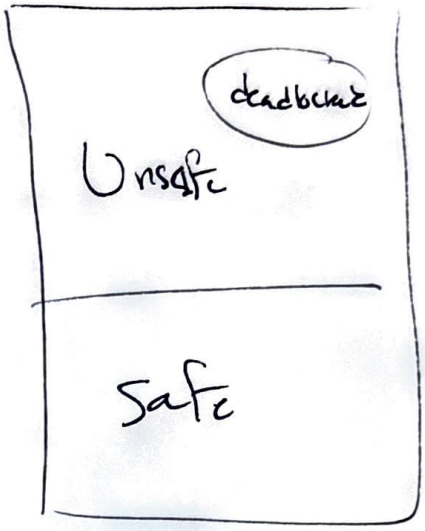
Inverted Page Table.





HW#3 P4

Safe Sequence



HW#3 P6

(a) 3 P M units

max 3

M = 9

(b) N procs M resources.

max need ~~does~~ < M.

Total need < M + N