

# Vishay General Semiconductor

# **High Voltage Trench MOS Barrier Schottky Rectifier**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	2.0 A		
$V_{RRM}$	200 V		
I <sub>FSM</sub>	40 A		
$V_F$ at $I_F = 2.0 A$	0.65 V		
T <sub>J</sub> max.	150 °C		
Package	DO-204AL (DO-41)		
Diode variations	Single		

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses



- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106 FREE
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in high frequency rectifier of switching mode power supplies, freewheeling diodes, DC/DC converters or polarity protection application.

#### **MECHANICAL DATA**

Case: DO-204AL (DO-41)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test **Polarity:** Color band denotes the cathode end

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	VSB2200S	UNIT
Max. repetitive peak reverse voltage	V <sub>RRM</sub>	200	V
Max. average forward rectified current (fig. 1) (1)	I <sub>F(AV)</sub>	2.0	А
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	-SM 40	
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	dV/dt 10 000	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C

#### Note

 $^{(1)}$  Units mounted on PCB with 2 mm x 2 mm copper pad areas 0.375" (9.5 mm) lead length, free air

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Breakdown voltage	$I_R = 1.0 \text{ mA}$	T <sub>A</sub> = 25 °C	$V_{BR}$	200 (min.)	-	
Instantaneous forward voltage (1)	1 204	T <sub>A</sub> = 25 °C	V <sub>F</sub>	0.97	1.23	V
	$I_F = 2.0 \text{ A}$	T <sub>A</sub> = 125 °C		0.65	0.73	
Reverse current per diode (2)	V 200 V	T <sub>A</sub> = 25 °C	I <sub>R</sub>	0.8	40	μΑ
	V <sub>R</sub> = 200 V	T <sub>A</sub> = 125 °C		0.6	4	mA
Typical juntion capacitance	4.0 V, 1 MHz		CJ	110	-	pF

#### Notes

 $^{(1)}$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSB2200S	UNIT	
Typical thermal resistance (1)	$R_{ heta JA}$	88	°C/W	
Typical trieffial resistance (7)	$R_{ heta JL}$	20	G/ VV	

#### Note

<sup>(1)</sup> Units mounted on PCB with 2 mm x 2 mm copper pad areas 0.375" (9.5 mm) lead length, free air

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
VSB2200S-M3/54	0.34	54	5500	13" diameter paper tape and reel	
VSB2200S-M3/73	0.34	73	3000	Ammo pack packaging	

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

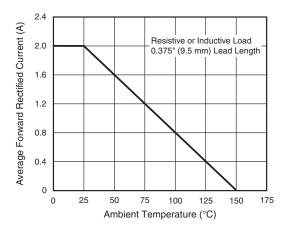


Fig. 1 - Maximum Forward Current Derating Curve

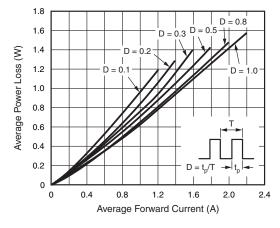


Fig. 2 - Forward Power Loss Characteristics

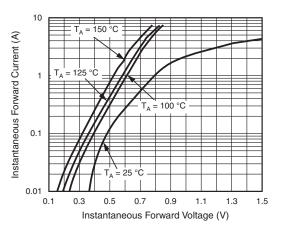


Fig. 3 - Typical Instantaneous Forward Characteristics

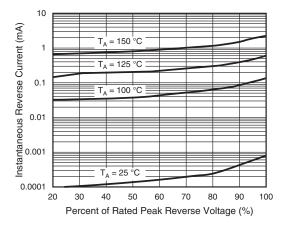


Fig. 4 - Typical Reverse Characteristics

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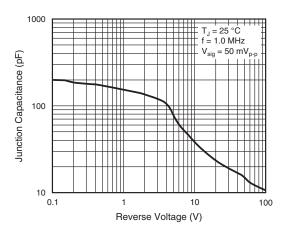


Fig. 5 - Typical Junction Capacitance

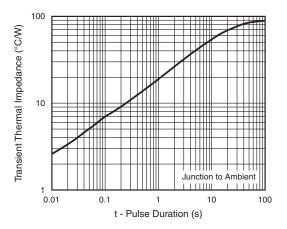


Fig. 6 - Typical Transient Thermal Impedance

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

# 0.034 (0.86) 0.034 (0.86) 0.028 (0.71)



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