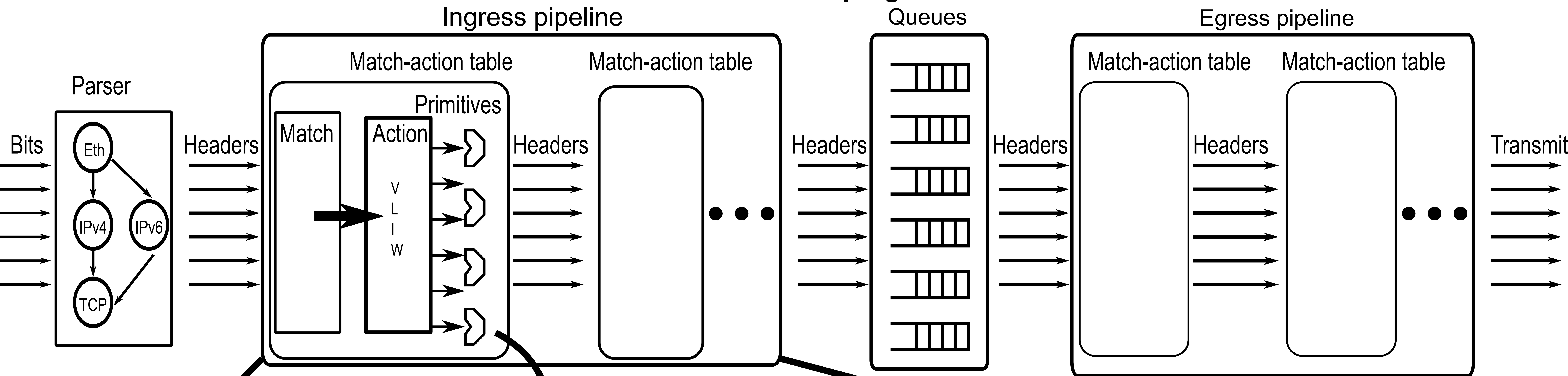


The architecture of a programmable switch



The PISA abstract machine

