Reducer-Based Construction of Conditional Verifiers

Marie-Christine Jakobs

Joint work with Dirk Beyer, Thomas Lemberger, and Heike Wehrheim

LMU Munich, Germany

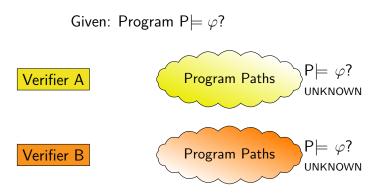




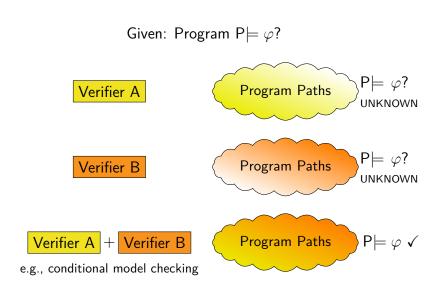




Facing Hard Verification Tasks

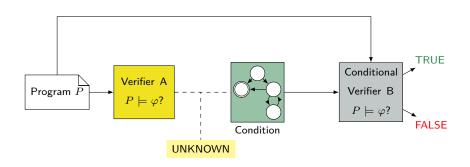


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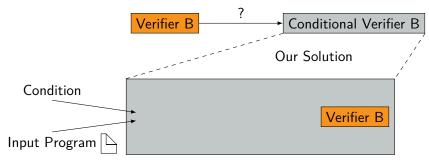


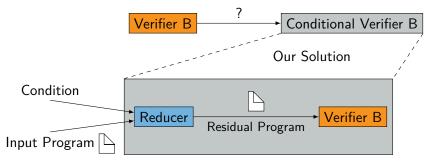
Conditional Model Checking

[Beyer/Henzinger/Keremoglu/Wendler FSE'12]



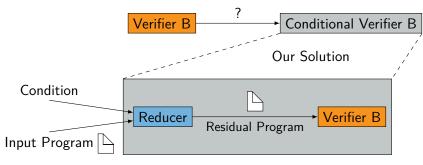
Verifier B ? Conditional Verifier B





Reducer (preprocessor)

- Builds standard input (C program)
- ► Representing a subset of paths
- Contains at least all non-verified paths



Reducer (preprocessor)

- Builds standard input (C program)
- Representing a subset of paths
- Contains at least all non-verified paths
- + Verifier-unspecific approach
- + Many conditional verifiers possible

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Example Program and Condition

Program

0: if (notThursday)

1: discount=day%7;
else

2: discount=5;
3: assert(0<=discount<7);

4:

Program

notThursday

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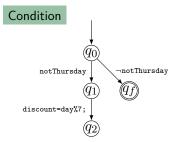
2: discount=5;

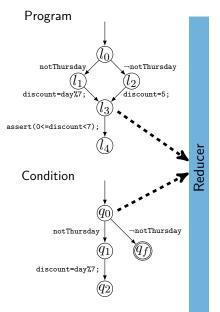
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notThursday $\begin{array}{c} & & & & \\ \hline l_0 & & & \\ \hline l_1 & & & \\ \hline l_2 & & & \\ \hline discount=day \%7; & & & \\ \hline discount=5; & & \\ \hline assert(0<=discount<7); & & \\ \hline \end{array}$

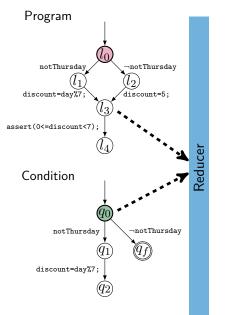
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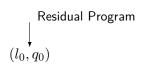




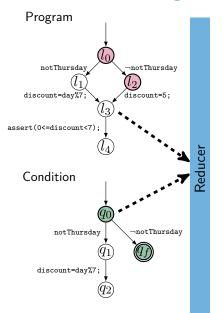
Residual Program

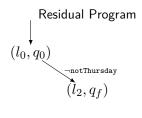
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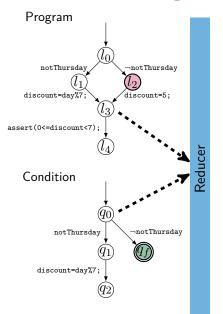


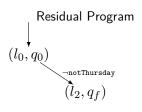
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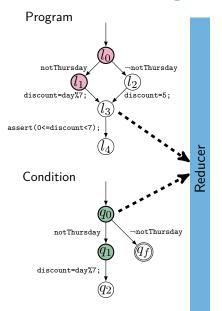


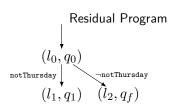


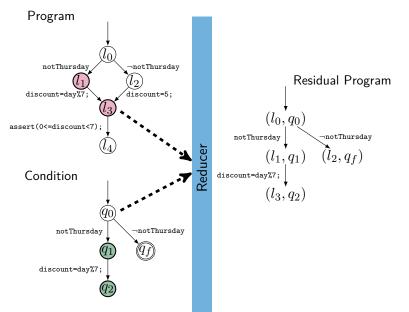
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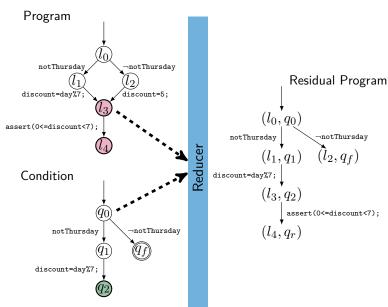






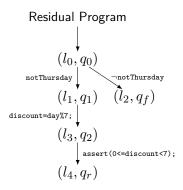




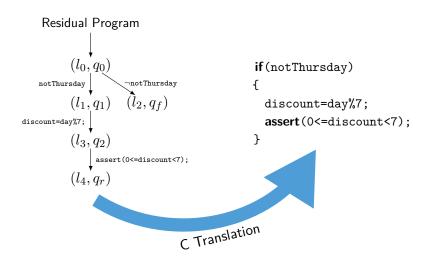


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Reducer: C Transformation

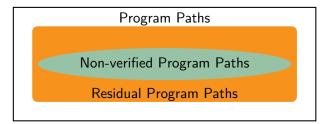


Reducer: C Transformation



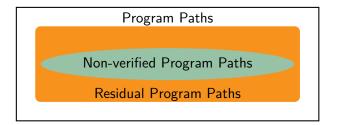
Reducer: Soundness

Residual Condition



Reducer: Soundness

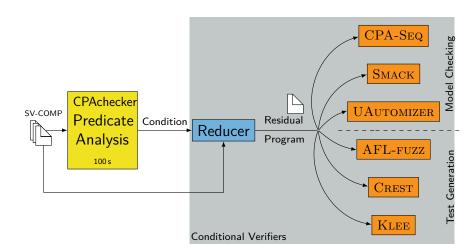
Residual Condition



Theorem

Presented reducer fulfills residual condition.

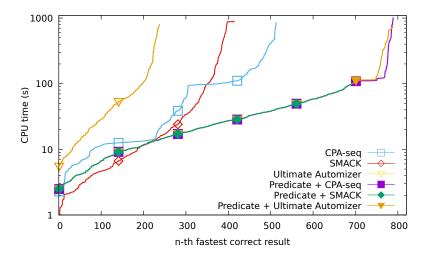
Evaluation Setup

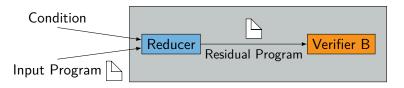


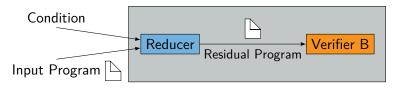
Small Extract of Results

						PREDICATE		PREDICATE	
		CPA-SEQ		UAUTOMIZER		+Reducer		+Reducer	
						+CPA-SEQ		+UAUTOMIZER	
Task	R	S	t(s)	S	t(s)	S	t(s)	S	t(s)
P15l01	Т	X	910	X	900	1	120	1	130
flood4	Т	X	910	X	910	1	450	X	1100
newt3_6	F	X	950	X	490	X	910	1	260
P07l38	Т	X	950	X	910	X	1100	1	470

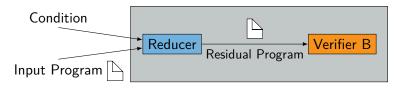
Effectiveness on Hard Tasks



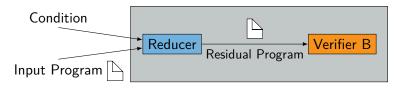




- One Reducer
 - Proven sound
 - Used in many conditional verifiers

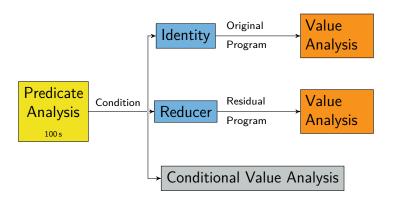


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 - Proven sound
 - Used in many conditional verifiers
- Effective on hard tasks for verifiers and test tools

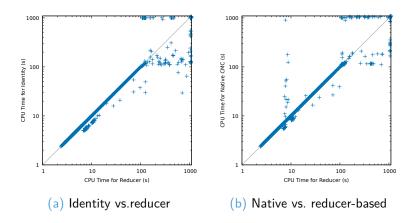


- One Reducer
 - Proven sound
 - Used in many conditional verifiers
- Effective on hard tasks for verifiers and test tools
- Future WorkMore reducersUsing conditions from other tools

Comparison Setup



Comparison Results



References I



D. Beyer, T. A. Henzinger, M. E. Keremoglu, and P. Wendler. Conditional Model Checking: A Technique to Pass Information Between Verifiers. In Proc. FSE.ACM, 2012.