

# Refinement of SCXML state-charts via translation to Event-B

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**Abstract.** State-chart modelling notations, such as Start Chart XML (SCXML), with so-called ‘run to completion’ semantics and simulation tools for validation, are popular with engineers for designing machines. However, they do not support refinement and they lack formal verification methods and tools. Properties concerning the synchronisation between different parts of a machine may be difficult to verify for all scenarios. Event-B, on the other hand, is based on refinement from an initial abstraction and is designed to make formal verification by automatic theorem provers feasible, obviating the need for instantiation and testing. We would like to combine the best of both approaches by incorporating a notion of refinement, similar to that of Event-B, into SCXML and leveraging Event-B’s tool support for proof. We describe some the pitfalls in translating ‘run to completion’ models to Event-B refinements, and suggest a solution and propose extensions to the SCXML syntax to describe refinements. We illustrate the approach using our prototype translation tools and show by example, how a synchronisation property between parallel state-charts can be automatically proven at an incomplete refinement level by translation into Event-B.

**Keywords:** SCXML, State-charts, Event-B, iUML-B, refinement

## 1 Introduction

Formal verification of high consequence systems requires the analysis of formal models that capture the properties and functionality of the system of interest. Proof obligations for systems’ properties or requirements can be made more tractable using refinement, where properties are expressed in terms of variables that are introduced at different abstraction levels.

A hierarchical development of a system model uses refinement concepts to link the different levels of abstraction. Each subsequent level increases model complexity by adding details in the form of functionality and implementation method. As the model complexity increases in each refinement level, tractability of the detailed model can be improved by the use of a graphical representation, with rich semantics that can support an infrastructure for formal verification.

The Event-B language [1] provides the logic and refinement theory required to formally analyze a system model. The open-source Rodin tool [2] provides support for Event-B including automatic theorem provers. iUML-B [4] augments the Event-B language with a graphical interface including state-machines.

## 2 Background

### 2.1 SCXML

*State Chart eXtensible Markup Language* (SCXML) is a modelling language based on Harel state-charts with facilities for adding data elements that are manipulated by transition actions and used in conditions for their firing. SCXML follows the usual ‘run to completion’ semantics of such state-chart languages, where trigger events may be needed to enable transitions. Trigger events are queued when they are raised and then one is de-queued and consumed by firing all the transitions that it enables, followed by any (un-triggered) transitions that then become enabled due to the change of state caused by the initial transition firing. This is repeated until no transitions are enabled and then the next trigger is de-queued and consumed. There are two kinds of triggers: internal triggers are raised by transitions and external triggers are raised by the environment (spontaneously as far as our model is concerned). An external trigger may only be consumed when the internal trigger queue has been emptied. Listing 1 shows a pseudocode representation of the run to completion semantics as defined within the latest WC3 Recommendation document [3]. Here IQ and EQ are the internal and external trigger present in the queue respectively.

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1 while running:
2   while run2completion = false
3     if untriggered_enabled
4       execute(untriggered())
5     elseif IQ != {}
6       execute(internal(IQ.dequeue))
7     else
8       run2completion = true
9     endif
10  endwhile
11  if EQ != {}
12    execute(EQ.dequeue)
13    run2completion = false
14  endif
15 endwhile

```

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Listing 1: Pseudocode for ‘run to completion’

### 2.2 Event-B

Overview of Event-B... semantics, refinement, proof obligations, tools..

...

In Event-B the run to completion pseudocode of Listing 1 could be represented (somewhat abstractly) as

Colin: we can copy from a previous paper

<pre> FireUntriggered : when   UC = FALSE then   execute(untriggered()) end </pre>	<pre> FireInternallyTriggered : when   UC = TRUE   IQ ≠ ∅ then   execute(IQ.dequeue)   UC := FALSE end </pre>	<pre> FireExternallyTriggered : when   UC = TRUE   IQ = ∅   EQ ≠ ∅ then   execute(EQ.dequeue)   UC := FALSE end </pre>
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Note that this is an abstract representation where each event would be specialised to select a particular set of transitions that can be fired in parallel and execute would be replaced by actions that encode the state changes made by those transitions. Representing the condition untriggered enabled is cumbersome since we would need to write a conjunction of all the possible untriggered guards. Instead we introduce a dummy untriggered event that is only fired when no other selection of untriggered transitions are available and sets a boolean flag, UC, to indicate that none of the real untriggered events was fired and a trigger needs to be consumed.

### 2.3 iUML-B State-machines

Overview of iUML-B State-machines... semantics from Event-B, translation...

Colin: we can copy from a previous paper

### 2.4 Intrusion Detection System

The simple intrusion detection system is designed using an Application-Specific Integrated Circuit (ASIC) which connects to a buzzer and a sensor over a Serial Peripheral Interface (SPI) bus. The system is controlled via the ASIC on the SPI bus. At power-up, the ASIC sends commands over the SPI bus to initialize the sensor and the buzzer. After waiting for 50 milliseconds the ASIC enters its main routine, which makes the buzzer respond to the sensor. The statechart model of this system is limited to the ASIC and captures the initialization of the peripherals and the 50 ms wait. In the interest of simplicity we elide the details of the main routine.

The ASIC starts by initializing the buzzer. This involves sending a message over the SPI bus, and at the highest level of abstract we can additional implementation details. Once the message is sent (which will be indicated by some event saying that the SPI system is done), the ASIC moves on to initializing the sensor. After the ASIC moves into a waiting state for 50 ms, and finally moves into the state which represents normal operation.

A subsequent level of refinement adds a parallel state representing the SPI subsystem. The SPI subsystem is usually on an **Idle** state until the **send\_message** event occurs, at which point the SPI subsystem enters a state **Sending Message**, which represents sending the message, byte by byte. When the last byte of the message is sent, it raises the **spi\_done** event, allowing the other parallel state to continue, while SPI subsystem returns to idle.

The model can be farther refined by incorporating more details on how the initialization states, the wait state, and the SPI subsystem operate, including how they interact with each other. The **Initialize Buzzer** state constructs the SPI message to send, then it raises the **send\_message** event, and then it waits. After **send\_message** is raised, the SPI subsystem reacts. It spins for a while in the **send\_byte** state, looping as many times as it takes to get to the last byte in the message. When the last byte in the message is sent, it goes back to idle and raises an event which allows the state machine on the left to proceed. The sensor is then initialized in a very similar manner to the buzzer. After both peripherals are initialized, the state machine goes into the **Wait 50 ms** state, where it increments a counter until it reaches some maximum, then exits.

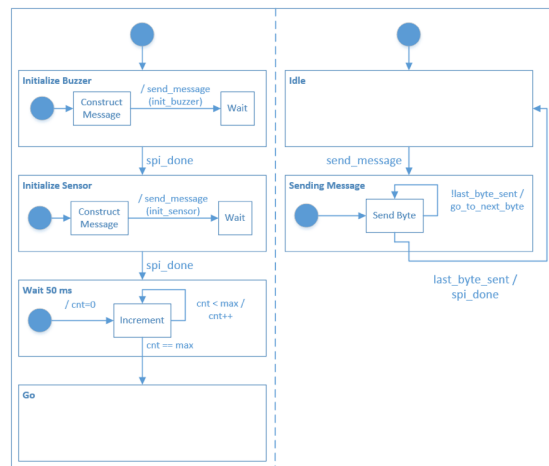


Fig. 1: Statechart diagram for SecBot intrusion detection system

### 3 Discussion

This is the discussion... Compare the behaviour of a visually similar statechart (SecBot?) in iUML-B and SCXML Based on their Event-B translations Using the theorem prover to test whether they are equivalent (they are not) Using the model checker to compare traces Using LTL to show that certain temporal properties are not achieved by the iUML-B version.

Early attempts 1) Simple next step based on negated guards negation is weakened 2) Engine improves the R2C semantics but still suffers from the negated guards problem (refinement of the user model kinda works simulations at the user level).

Karla: Not sure if we should include these figures or the IUMLB generated ones. If we include this figure maybe we can simplify it by using color for each of the refinement levels

Colin: introduce the secbot example showing how we would like to develop it in refinements

Colin: IF POSSIBLE? use the secbot example to illustrate the problem of guard strengthening on refinement

The solution: 3) Transition combinations approach works because there is always one event to completion. Invariants as well as simulations. a. Explosion mitigated by mutual exclusions same trigger different state-chart regions.

What can we do with it? Why is RTC needed? Refinement in RTC

## 4 Tooling

A tool to automatically translate SCXML models into iUML-B has been produced. The tool is based on the Eclipse Modelling Framework (EMF) and uses a an SCXML metamodel provided by Sirius [3] which has good support for extensibility. The tooling for iUML-B and Event-B already contains EMF metamodels and provides a generic translator framework which has been specialised for the SCXML to iUML-B translation.

Colin: Things to cover: EMF metamodel, translation tool, anything else?

The following syntax extensions are added to SCXML models to support modelling features needed in iUML-B/Event-B. These extensions are prefixed with 'iumlb:' in order to distinguish them from the scxml XML parser. (So that they are ignored by SCXML simulation tools).

- **iumlb:refinement** - an integer attribute representing the refinement level at which the parent element should be introduced.
- **iumlb:invariant** - an element that generates an invariant in iUML-B. This provides a way to add invariants to states so that important properties concerning the synchronisation of state with ancillary data and other statemachines can be expressed.
- **iumlb:guard** - an element that generates a transition guard in iUML-B. This provides a way to add new guard conditions to transitions over several refinement as well as providing an element with attributes such as derived (for Event-B theorems), name and comment.
- **iumlb:predicate** - a string attribute used for the predicate of a guard or invariant.
- ...other attributes useful for iUML-B elements: name, derived, type, comment.

The Hierarchical nested state charts are translated to similar corresponding state-machine structures in iUML-B according to the rules described in REF??.

## 5 Example

The SecBot example : pics from slides

Note why it would not be a refinement

## 6 Related Work

This is the related work...

## 7 Conclusion

Colin: maybe consider the note below (from our previous plans for a paper)

Note: Things to highlight with the choice of example 1. Look at a system that is better model with SCXML run to completion semantics than iUML-B semantics 2. Look at how you can check for violations of refinement in a SCXML model construction 3. Look at the sort of invariant properties you can verify about a SCXML model

This is the conclusion...

All data supporting this study are openly available from the University of Southampton repository at <http://doi.org/10.????/SOTON/D0???>

## References

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