FINAL SCHEME

02000CST206052102

			Total Pages: 3
		Scheme of Valuation/Answ	er Key
(Sc	hem	e of evaluation (marks in brackets) and answers	_
		APJ ABDUL KALAM TECHNOLOGICA	L UNIVERSITY
FO	OUR.	ГН SEMESTER B.TECH DEGREE (S) EXAMINATIO	ON, JUNE 2023 (2019 SCHEME)
		Course Code: CST 206	
		Course Name: OPERATING SYSTI	
Max	x. Ma	arks: 100	Duration: 3 Hours
	1	PART A	1
		(Answer all questions; each question ca	rries 3 marks) Marks
1		Operations during system call – 3 marks	3
2		Multiprocessor system(1)+ Any two advantages (2)	3
3		Difference between process & thread – 3 marks	3
4		Message passing (1.5) + Shared memory(1.5)	3
5		Mutual exclusion, progress and bounded wait, each ca	arry 1 mark 3
6		Priority inheritance protocol – 3 marks	3
		Full credits may be given if attempted	
7		Internal – 1.5 marks + external fragmentation – 1.5	marks 3
8		Demand paging - 3marks	3
9		Linked allocation (1) + Merits (1) + Demerit(1)	3
10		C-SCAN – 1.5 marks + C-LOOK – 1.5 marks	3
		PART B	
		(Answer one full question from each module, each qu	uestion carries 14 marks)
		Module -1	
11	a)	Explaining each carry 2.5 marks (2.5x4)	10
		Full credits may be given if attempted	
	b)	Layered approach with figure	4
12	a)	Any 4 kernel data structures with example (2x4)	8
		Stack, queue, list, tree, bitmap, hash table	
12	(a)		

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		Full credits may be given if attempted	
	b)	Justification as government (2) + resource allocator(2) + control program(2)	6
	'	Module -2	
13	a)	Fig – 2 mark + Explanation of Operations – 5 marks	7
	b)	Direct communication – 3.5 marks + indirect communication – 3.5 marks	7
14	a)	Drawing Gantt chart (1 mark) + Finding the average waiting time (2 mark).	9
		3 marks for each scheduling	
		i) FCFS – 3.75 (ii) SJF – 3.5 (iii) SRTF – 3.25	
	b)	Explanation	5
		Full credits may be given if attempted	
		Module -3	
15	a)	Solution using banker's algorithm.	8
		Need matrix – 2 marks, Available matrix(3,3,2,0) - 1 mark	
		Steps showing solution – 4 marks	
		Order of processing (Safe Sequence): <p4, p1,="" p2="" p3,=""> - 1 marks.</p4,>	
	b)	Explanation of problem (2 marks) + solution using semaphore (4 marks)	6
16	a)	Dining philosopher's problem (2 marks) and give a solution using semaphore (4	8
		marks) and structure of philosopher. (2 marks)	
	b)	Breaking the conditions for deadlock	6
		Mutual exclusion – 1.5	
		Hold and wait – 1.5	
		No preemption -1.5	
		circular wait – 1.5	
		Module -4	
17	a)	i) LRU-10 faults	9
		ii) FIFO-14 faults	
		iii) Optimal-8 faults,	
		each of these steps showing faults carry 3 marks	
	b)	Shared pages – Diagram – 2 marks + Explanation – 3 marks	5

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18	a)	Diagram	of each	allotting	the proce	ss- 2 mai	rks (2*3	=6 marks	s)		8
		Best fit is	the bes	t algoritl	hm – 2 ma	ırks					
	b)	Hashed p	age tabl	e – diagı	ram -2 ma	rks + exp	lanatio	n – 4 mar	ks.		6
	<u> </u>	I			N	Module -	5				
19	a)	Explanati	on on F	CFS (3 1	marks), SS	STF (3 m	arks) an	nd SCAN	(3 marks) disk	9
		scheduling algorithms using the given disk queue of request.									
		FCFS: 410									
		SSTF : 26	52	(100->8	89->130->	180->45	>20)				
		SCAN: 2	278 (for	the head	l moving t	owards c	ylinder	no 199)			
		1	00->13	0->180-	>199->89	->45->20)				
		SCAN: 2	280 (for	the head	l moving t	owards c	ylinder	0 initiall	y)		
		100->89->45->20->0->130->180									
]	100-/69	->43->2	.0-/0-/13	0-> 100					
	b)	Bit vector	r – 2mai	rks, Link	ked list – 2		First fr	ee block l	keeps the	address	of 5
	b)	Bit vector	r – 2mai Icks – 1	rks, Link mark	ked list – 2		First fr	ee block l	keeps the	address o	
20	b) a)	Bit vector	r – 2mai Icks – 1	rks, Link mark	ked list – 2		First fr	ee block l	keeps the	address	of 5
20		Bit vector n-1 free b Access m	r – 2mai Icks – 1	rks, Link mark	ked list – 2		First fr	ee block l	keeps the	address o	
20		Bit vector n-1 free b Access m	r – 2mar dcks – 1 atrix –	rks, Link mark 7 marks	s	2 marks ,			1	1	
20		Bit vector n-1 free b Access m	r – 2mar olcks – 1 atrix – F1	rks, Link mark 7 marks	s F3	2 marks ,		D2	1	D4	
20		Bit vector n-1 free b Access m D1	r – 2man olcks – 1 atrix – F1 R, W	rks, Link mark 7 marks	s F3	2 marks ,		D2	1	D4	
20		Bit vector n-1 free b Access m D1 D2	r – 2man olcks – 1 atrix – F1 R, W	rks, Link mark 7 marks	s F3	P4 R, W		D2	1	D4	
20		Bit vector n-1 free b Access m D1 D2 D3 D4	r – 2mar olcks – 1 atrix – F1 R, W R	rks, Link mark 7 marks F2	s F3 R, W	P4 R, W X	D1	D2 S	D3	D4 S	