

Reg No.: _____

Name: _____

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY
Fourth Semester B.Tech Degree Examination June 2022 (2019 scheme)

Course Code: CST206
Course Name: OPERATING SYSTEMS

Max. Marks: 100

Duration: 3 Hours

PART A*(Answer all questions; each question carries 3 marks)*

Marks

- | | | |
|----|--|---|
| 1 | Describe the role of bootstrap loader in booting a computer system. | 3 |
| 2 | How is distinction of kernel code from user code achieved at hardware level? | 3 |
| 3 | How many times will 'Forked' get printed by the below code and justify your answer.

<pre>int main() { fork(); fork(); printf("Forked\n"); return 0;}</pre> | 3 |
| 4 | List and explain the various synchronous and asynchronous methods of message passing in IPC. | 3 |
| 5 | What is meant by race condition? Explain with the help of an example. | 3 |
| 6 | Explain the two strategies used to recover from a deadlock. | 3 |
| 7 | Differentiate between compile time and load time address binding. | 3 |
| 8 | Consider a logical address space of 256 pages with a 4-KB page size, mapped onto a physical memory of 64 frames. Find the (i) number of bits in the logical address (ii) number of bits in the physical address (iii) number of bits in the offset part of logical address | 3 |
| 9 | Define seek time, rotational latency and disk bandwidth of disks. | 3 |
| 10 | Explain two level directory structure with the help of a diagram. | 3 |

PART B*(Answer one full question from each module, each question carries 14 marks)***Module -1**

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|----|--|---|
| 11 | a) What is the purpose of a system call? Describe how a system call made by a user application is handled. | 7 |
|----|--|---|

- b) Explain the micro kernel approach to system design with the help of a diagram. 7
How do user programs and kernel services interact in microkernel architecture?
- 12 a) Describe in detail about the various functions of Operating systems. 12
b) What are the advantages of multiprocessor systems? 2

Module -2

- 13 a) What is meant by context switching? Illustrate the timeline of context switching between two processes using PCBs with the help of a diagram. 6
b) Differentiate between the following schedulers. 8
(i) Short-term and long term scheduler
(ii) Pre-emptive and non-preemptive scheduler
- 14 a) Assume you have the following jobs shown in the table to execute with one processor. Draw the Gantt chart and calculate the average waiting time and average turnaround time if the system uses the following scheduling algorithms 9
(i) Preemptive priority scheduling (ii) Nonpreemptive priority scheduling
(iii) Non-preemptive Shortest Job first scheduling.
Assume higher priority is indicated with lower numbers.

Process	Arrival Time(ms)	CPU Burst Time(ms)	Priority
P0	0	4	3
P1	2	5	2
P2	3	1	1
P3	4	3	4

- b) Explain the different states of a process and transition between them with the help of a diagram. 5
- ### Module -3
- 15 a) What is a critical section? State and explain the conditions to be satisfied by a solution to the critical section problem. 6
b) Illustrate how resource allocation graph can be used to (i) detect deadlocks and (ii) avoid deadlocks. 8
- 16 a) Write the algorithm or pseudocode for solving the Dining-Philosophers problem using semaphores. Is the solution prone to deadlocks or starvation? Discuss. 5
b) Given the following snapshot of a system at time T0. 9

	Allocation				Max Required				Available			
	A	B	C	D	A	B	C	D	A	B	C	D
P0	0	0	1	2	0	0	1	2	1	5	2	0
P1	1	0	0	0	1	7	5	0				
P2	1	3	5	4	2	3	5	6				
P3	0	6	3	2	0	6	5	2				
P4	0	0	1	4	0	6	5	6				

Find whether the system is in a safe state using Banker's algorithm. If P1 generates a request for (0,4,2,0), can the request be granted immediately?

Module -4

- 17 a) Calculate the number of page faults for the following reference string with three page frames, using the following algorithms. 9
 9, 2, 3, 1, 2, 5, 3, 4, 6, 9, 9, 1, 0, 5, 4, 6, 2, 3, 0, 1
 (i) FIFO (ii) Optimal (iii) LRU
- b) Compare the memory organization schemes of pure paging and pure segmentation with respect to the following issues: (i) External Fragmentation (ii) Internal Fragmentation 5
- 18 a) Explain how a process larger than the physical memory can be executed with the help of virtual memory. Describe the concept using demand paging. 6
- b) How does translation look-aside buffer (TLB) help to speed up the page access? Illustrate address translation using TLB. 8

Module -5

- 19 a) List and explain the different access methods for files. 4
- b) Describe linked and indexed allocation methods for files with the help of neat diagrams. 10
- 20 a) Suppose that a disk drive has 200 cylinders numbered from 0 to 199 and the current position of the head is at cylinder 100. For the given disk queue of requests: - 20, 89, 130, 45, 120 and 180, draw the head movement in FCFS, SSTF, CSCAN disk scheduling algorithms and compute the total head movements (in cylinders) in each. 9
- b) Describe the general scheme of using three classifications of users in connection with file access control with the help of an example. 5
