XME0724				XME0724CB		
XME0724 and XME0724CB	Xilinx Zynq-7000 Z-7010/7020 CLG400 R1.1 Core Board 9 Dec 2018				R1.1 Carrier Board 9 Dec 2018	
Pin Number	Signal Name	Pin Number	Description	Signal Name	Domain	Pin Name Description
1	GND			GND		
2	B34_L1_P	T11	Bank34 IO	ETH1_TXCTL	Ethernet1	
3	B34_L1_N	T10	Bank34 IO	ETH1_TXD3	Ethernet1	
4	B34_L2_P	T12	Bank34 IO	ETH1_TXD2	Ethernet1	
5	B34_L2_N	U12	Bank34 IO	ETH1_TXCK	Ethernet1	
6	GND			GND		
7	B34_L3_P	U13	Bank34 IO	ETH1_TXD1	Ethernet1	
8	B34_L3_N	V13	Bank34 IO	ETH1_TXD0	Ethernet1	
9	B34_L4_P	V12	Bank34 IO	ETH1_RXD3	Ethernet1	
10	B34_L4_N	W13	Bank34 IO	ETH1_RXD2	Ethernet1	
11	GND			GND		
12	B34_L6_P	P14	Bank34 IO	ETH1_RXD1	Ethernet1	
13	B34_L6_N	R14	Bank34 IO	ETH1_RXD0	Ethernet1	
14	B34_L5_P	T14	Bank34 IO	ETH1_RXCTL	Ethernet1	
15	B34_L5_N	T15	Bank34 IO	ETH1_nRST	Ethernet1	
16	GND			GND		
17	B34_L8_P	W14	Bank34 IO	ETH1_RGMII_MDC	Ethernet1	
18	B34_L8_N	Y14	Bank34 IO	ETH1_RGMII_MDIO	Ethernet1	
19	B34_L11_P_SRCC	U14	Bank34 IO	ETH1_RXCK	Ethernet1	
20	B34_L11_N_SRCC	U15	Bank34 IO	ETH2_nRST	Ethernet2	
21	GND			GND		
22	B34_L10_P	V15	Bank34 IO	ETH2_TXCTL	Ethernet2	
23	B34_L10_N	W15	Bank34 IO	ETH2_TXD3	Ethernet2	
24	B34_L18_P	V16	Bank34 IO	ETH2_TXD2	Ethernet2	
25	B34_L18_N	W16	Bank34 IO	ETH2_TXCK	Ethernet2	
26	GND			GND		
27	B34_L9_P	T16	Bank34 IO	ETH2_TXD1	Ethernet2	
28	B34_L9_N	U17	Bank34 IO	ETH2_TXD0	Ethernet2	
29	B34_L7_P	Y16	Bank34 IO	ETH2_RXD3	Ethernet2	
30	B34_L7_N	Y17	Bank34 IO	ETH2_RXD2	Ethernet2	
31	VCCIO34		电源输入，Bank34 IO电压	VCC_3V3		
32	B34_L21_P	V17	Bank34 IO	ETH2_RXD1	Ethernet2	
33	B34_L21_N	V18	Bank34 IO	ETH2_RXD0	Ethernet2	
34	B34_L17_P	Y18	Bank34 IO	ETH2_RXCTL	Ethernet2	
35	B34_L17_N	Y19	Bank34 IO	ETH2_RGMII_MDC	Ethernet2	
36	GND			GND		
37	B34_L22_P	W18	Bank34 IO	ETH2_RGMII_MDIO	Ethernet2	
38	B34_L22_N	W19	Bank34 IO	ETH3_nRST	Ethernet3	
39	B34_L16_P	V20	Bank34 IO	ETH3_CRS	Ethernet3	
40	B34_L16_N	W20	Bank34 IO	ETH3_COL	Ethernet3	
41	GND			GND		
42	B34_L12_P_MRCC	U18	Bank34 IO	ETH2_RXCK	Ethernet2	
43	B34_L12_N_MRCC	U19	Bank34 IO	ETH3_TXD3	Ethernet3	
44	B34_L15_P	T20	Bank34 IO	ETH3_TXD2	Ethernet3	

45	B34_L15_N	U20	Bank34 IO		ETH3_TXD1	Ethernet3	
46	**GND**				**GND**		
47	B34_L20_P	T17	Bank34 IO		ETH3_TXD0	Ethernet3	
48	B34_L20_N	R18	Bank34 IO		ETH3_TXEN	Ethernet3	
49	B34_L23_P	N17	Bank34 IO		ETH3_RXER	Ethernet3	
50	B34_L23_N	P18	Bank34 IO		ETH3_RXDV	Ethernet3	
51	**GND**				**GND**		
52	B34_L19_P	R16	Bank34 IO		ETH3_RXD0	Ethernet3	
53	B34_L19_N	R17	Bank34 IO		ETH3_RXD1	Ethernet3	
54	B34_L24_P	P15	Bank34 IO		ETH3_RXD2	Ethernet3	
55	B34_L24_N	P16	Bank34 IO		ETH3_RXD3	Ethernet3	
56	**GND**				**GND**		
57	**B34_L14_P_SRCC**	N20	Bank34 IO		ETH3_TXCK	Ethernet3	
58	**B34_L14_N_SRCC**	P20	Bank34 IO		ETH3_MII_MDC	Ethernet3	
59	**B34_L13_P_MRCC**	N18	Bank34 IO		ETH3_RXCK	Ethernet3	
60	**B34_L13_N_MRCC**	P19	Bank34 IO		ETH3_MII_MDIO	Ethernet3	
61	**GND**				**GND**		
62	B35_L21_P	N15	Bank35 IO		nMII_LINK3	Ethernet3	
63	B35_L21_N	N16	Bank35 IO		nMII_LINK4	Ethernet4	
64	B35_L22_P	L14	Bank35 IO		ETH4_TXD3	Ethernet4	
65	B35_L22_N	L15	Bank35 IO		ETH4_TXD2	Ethernet4	
66	**GND**				**GND**		
67	**B35_L12_P_MRCC**	K17	Bank35 IO		FPGA_GCLK1		
68	**B35_L12_N_MRCC**	K18	Bank35 IO		PL_KEY		
69	B35_L24_P	K16	Bank35 IO		ETH4_TXD1	Ethernet4	
70	B35_L24_N	J16	Bank35 IO		ETH4_TXD0	Ethernet4	
71	**VCCIO35**		电源输入，Bank35 IO电压		**VCC_3V3**		
72	B35_L17_P	J20	Bank35 IO		ETH4_TXEN	Ethernet4	
73	B35_L17_N	H20	Bank35 IO		ETH4_RXER	Ethernet4	
74	**B35_L13_P_MRCC**	H16	Bank35 IO		ETH4_TXCK	Ethernet4	
75	**B35_L13_N_MRCC**	H17	Bank35 IO		ETH4_RXDV	Ethernet4	
76	**GND**				**GND**		
77	B35_L7_P	M19	Bank35 IO		ETH4_RXD0	Ethernet4	
78	B35_L7_N	M20	Bank35 IO		ETH4_RXD1	Ethernet4	
79	B35_L9_P	L19	Bank35 IO		ETH4_RXD2	Ethernet4	
80	B35_L9_N	L20	Bank35 IO		ETH4_RXD3	Ethernet4	
81	**GND**				**GND**		
82	B35_L8_P	M17	Bank35 IO		ETH4_CRS	Ethernet4	
83	B35_L8_N	M18	Bank35 IO		ETH4_COL	Ethernet4	
84	B35_L11_P_SRCC	L16	Bank35 IO		ETH4_RXCK	Ethernet4	
85	B35_L11_N_SRCC	L17	Bank35 IO		ETH4_nRST	Ethernet4	
86	**GND**				**GND**		
87	B35_L10_P	K19	Bank35 IO		ETH4_MII_MDC	Ethernet4	
88	B35_L10_N	J19	Bank35 IO		ETH4_MII_MDIO	Ethernet4	
89	**B35_L14_P_SRCC**	J18	Bank35 IO		B35_L14_P	GPIO Header	
90	**B35_L14_N_SRCC**	H18	Bank35 IO		B35_L14_N	GPIO Header	
91	**GND**				**GND**		
92	B35_L18_P	G19	Bank35 IO		B35_L18_P	GPIO Header	
93	B35_L18_N	G20	Bank35 IO		B35_L18_N	GPIO Header	
94	B35_L16_P	G17	Bank35 IO		B35_L16_P	GPIO Header	
95	B35_L16_N	G18	Bank35 IO		B35_L16_N	GPIO Header	
96	**GND**				**GND**		
97	B35_L15_P	F19	Bank35 IO		B35_L15_P	GPIO Header	
98	B35_L15_N	F20	Bank35 IO		B35_L15_N	GPIO Header	

99	B35_L4_P	D19	Bank35 IO	B35_L4_P	GPIO Header	
100	B35_L4_N	D20	Bank35 IO	B35_L4_N	GPIO Header	
101	**GND**			**GND**		
102	B35_L6_P	F16	Bank35 IO	B35_L6_P	GPIO Header	
103	B35_L6_N	F17	Bank35 IO	B35_L6_N	GPIO Header	
104	B35_L19_P	H15	Bank35 IO	B35_L19_P	GPIO Header	
105	B35_L19_N	G15	Bank35 IO	B35_L19_N	GPIO Header	
106	**GND**			**GND**		
107	B35_L2_P	B19	Bank35 IO	B35_L2_P	GPIO Header	
108	B35_L2_N	A20	Bank35 IO	B35_L2_N	GPIO Header	
109	B35_L20_P	K14	Bank35 IO	B35_L20_P	GPIO Header	
110	B35_L20_N	J14	Bank35 IO	B35_L20_N	GPIO Header	
111	**GND**			**GND**		
112	B35_L1_P	C20	Bank35 IO	B35_L1_P	GPIO Header	
113	B35_L1_N	B20	Bank35 IO	B35_L1_N	GPIO Header	
114	B35_L5_P	E18	Bank35 IO	B35_L5_P	GPIO Header	
115	B35_L5_N	E19	Bank35 IO	B35_L5_N	GPIO Header	
116	**GND**			**GND**		
117	B35_L3_P	E17	Bank35 IO	B35_L3_P	GPIO Header	
118	B35_L3_N	D18	Bank35 IO	B35_L3_N	GPIO Header	
119	MODE0		ZYNQ启动模式0，配置模式参考用户手册	MODE0	Config	
120	MODE1		ZYNQ启动模式1，配置模式参考用户手册	MODE1	Config	
121	USB_OTG_CPEN		外部USB电源开关，高使能	USB_OTG_CPEN		
122	USB_OTG_P		USB数据信号	USB_OTG_P		
123	USB_OTG_N		USB数据信号	USB_VBUS		
124	USB_VBUS		串接电阻，参考芯片电路设计	USB_OTG_N		
125	USB_OTG_ID		作为主设备接地，作为从设备悬空	USB_OTG_ID		
126	PHY_LED1		PHY LED1	PHY_LED1		
127	PHY_LED0		PHY LED0	PHY_LED0		
128	**GND**			**GND**		
129	ETH_MDI3_N		MDI3 data	ETH_MDI3_N		
130	ETH_MDI3_P		MDI3 data	ETH_MDI3_P		
131	ETH_MDI2_N		MDI2 data	ETH_MDI2_N		
132	ETH_MDI2_P		MDI2 data	ETH_MDI2_P		
133	**GND**			**GND**		
134	ETH_MDI1_N		MDI1 data	ETH_MDI1_N		
135	ETH_MDI1_P		MDI1 data	ETH_MDI1_P		
136	ETH_MDI0_N		MDI0 data	ETH_MDI0_N		
137	ETH_MDI0_P		MDI0 data	ETH_MDI0_P		
138	**GND**			**GND**		
139	SD_CLK		SD CLK	SD_CLK		
140	SD_CMD		SD CMD	SD_CMD		
141	SD_DATA0		SD DATA0	SD_DATA0		
142	SD_DATA1		SD DATA1	SD_DATA1		
143	SD_DATA2		SD DATA2	SD_DATA2		
144	SD_DATA3		SD DATA3	SD_DATA3		
145	MIO9		PS MIO9	PS_KEY		
146	MIO10		PS MIO10	CAN0_RXD		
147	MIO11		PS MIO11	CAN0_TXD		
148	MIO12		PS MIO12	CAN1_TXD		
149	MIO13		PS MIO13	CAN1_RXD		

150	UART_RX		UART RX	UART_RX		
151	UART_TX		UART TX	UART_TX		
152	**VCC_3V3**		3.3V电源输出，可用于JTAG VREF电压。	**VMOD_3V3**		
153	FPGA_TDI		JTAG TDI信号	FPGA_TDI		
154	FPGA_TDO		JTAG TDO信号	FPGA_TDO		
155	FPGA_TCK		JTAG TCK信号	FPGA_TCK		
156	FPGA_TMS		JTAG TMS信号	FPGA_TMS		
157	RESET_N		Reset信号，低有效，高电平输入低于3.6V，不用的时候，悬空。	RESET_N		
158	PG_SYS		Power Good信号，电源系统正常时输出高，open drain输出，4.7K上拉，上拉电压低于5.5V，不用的时	PG_SYS		
159	**VINMOD**		电源输入，5V+/-10%	**VCC_5V**		
160	**VINMOD**		电源输入，5V+/-10%	**VCC_5V**		