

RAN

$$U_{BE0} \leq U_{BE} < U_{BEsat}$$

$$U_{BE} = U_B = U_E$$

$$0 \leq I_B < I_{BS1}$$

$$0 \leq I_C < I_{CSAT}$$

$$I_C = \beta_0 I_B$$

$$U_{CE} > U_{CESAT}$$

SAT

$$U_{BE} = U_{BEsat}$$

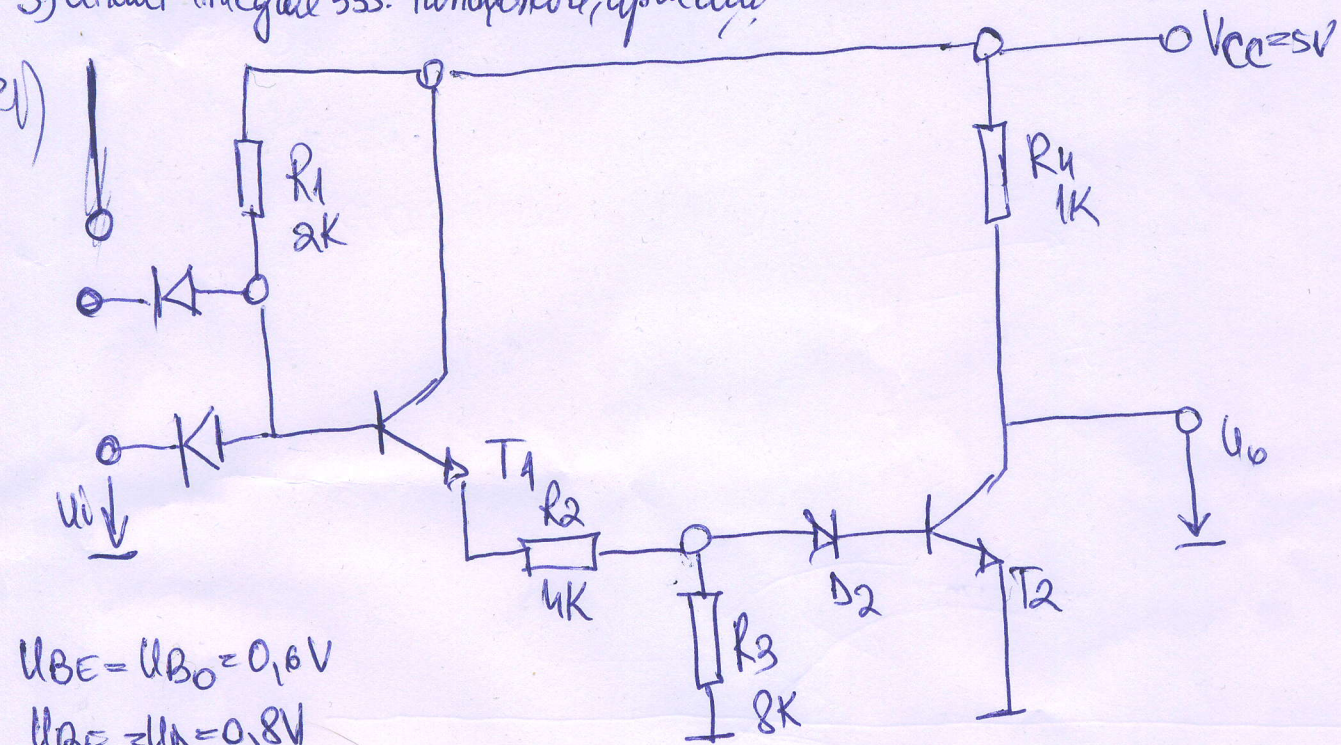
$$I_B \geq I_{BS1}$$

$$I_C = I_{CSAT}$$

$$U_{CE} = U_{CESAT}$$

$$M \geq 0$$

- 1) Variante ale portii TTL standard,
- 2) Caracteristica de transfer a inversorului CMOS (fără margini de zgomot)
- 3) Circuit integral 555. Funcționare, aplicații



$$U_{BE} = U_{B0} = 0,6V$$

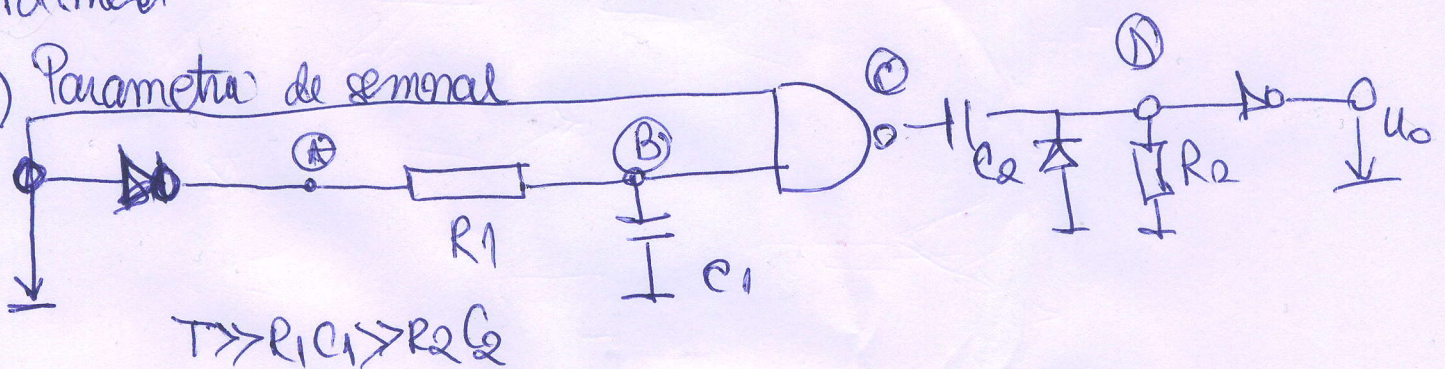
$$U_{BE} = U_{B1} = 0,8V$$

$$\beta_0 = 40$$

1) Caracteristica de transfer (U_{OL} , U_{OH} , U_L , U_{LH} , U_{LH} , $M \geq 0$)

2) Pd med

P2) Parametri de semnal



$$T \gg R_1 C_1 \gg R_2 C_2$$

