

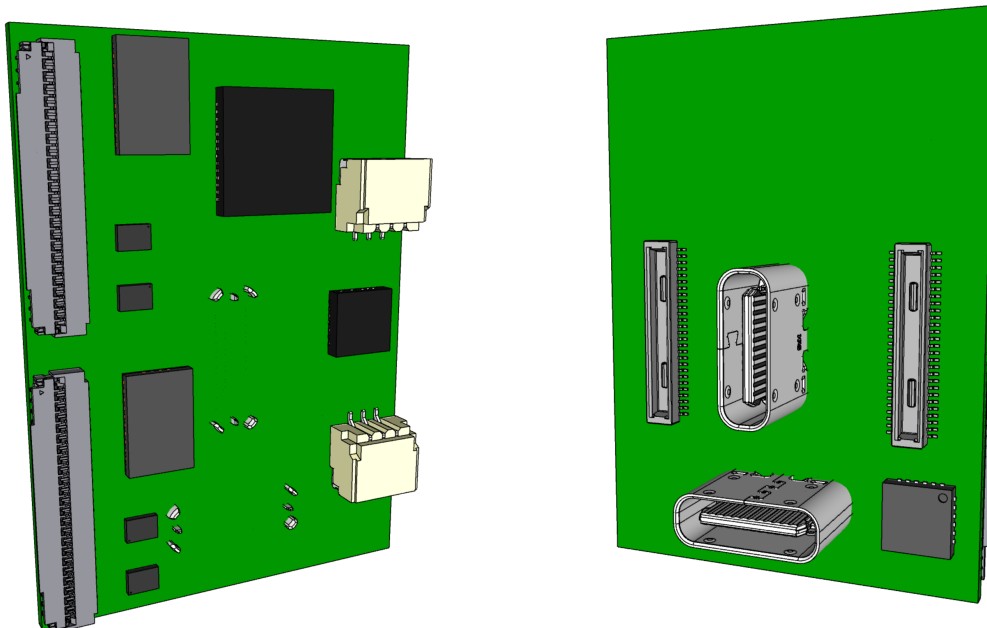
292 Power Module

The power module provides VSOM power to the Faceboard and routes data signals from the Faceboard through two USB plugs. A set of plugs are front facing and another set is back facing. Only one set can be used at a time.

The Power Module has three functions

- Supply the system with VSOM power
- Receive power from one of the two USB-C connectors or a LiPo battery
- Charge the connected LiPo battery
- Provide data signals from the Faceboard in the system over two USB-C connectors
- Manage autonomous system functions and waking state with the MSP430 MCU

The Power Module exposes two vertical USB-C sockets and connects to the carrier/face board through two 50 pin B2B connectors. Two 45 pin debug connectors provides options to experiment with USB-C Alt. mode and connect a Stem MCU for Autonomous functions. The signal voltage on the board is 3.3V.



Components

- 2 * 50 pin connectors [Hirose DF40-50DP-0.4V](#) mated height 1.5mm [Mouser](#)
- 4 * [Hirose USB-C CX80B1-24P](#)
- 1 * [TPS65988](#) Dual Port USB Type-C® and USB PD Controller, Power Switch, and High-Speed Multiplexer. [Mouser](#)
- 1 * [MSP430 FR2032 IG56 TSSOP DGG56](#). Inventory 4870 at TI. @1000 \$0.766. 52 IO pins, 1 UART/I2C, 1 UART/SPI.
- 1 * [BQ24250RGER](#) battery charger \$2 JLCPCB (4x4 mm package) [Mouser](#)

- 2 * [3 pin JST SH socket SM03B-SRSS-TB - JLCPCB - Farnell](#) (Matched by JST PHR-3)
- 2 * [TE Connectivity 45PIN 0.3MM 571-4-2328724-5 FPC 3-2328724-5](#) \$0.41
- 2 * [TPD6S300ARUKR ESD protection for USB-C port](#)
- 2 * [HD3SS3220IRNHT 10-Gbps USB 3.1 Type-C 2:1 mux with DRP Controller WQFN \(RNH\) | 30 pin 250 tray Mouser](#)
- 1 * [TUSB546 Alt. Mode switch](#)

Future Components

- 4 * [TS5USBC410 Dual 2:1 USB 2.0 Mux/DeMux Switch. Mouser](#)

I2C Bus

The board has 3 I2C busses. SYS, Stem and Power. Key chipsets on the board are on the Power bus, which by default is bridged onto the SYS I2C, so the two must take care to not clash on addresses. The MSP430 controls the bridge between the two and can turn it off.

Chips on the Power I2C bus

- TPS65988 PD Controller
- BQ24250 LiPo Charger
- TUSB546 Alt Mode Control
- HD3 SS3320 USB-C switch
- USB 2.0 switches
- MSP430

By default the chipsets can be controlled by Linux Device Driver Bindings(on i.MX SoM) via the SYS I2C. The future direction is to control them by the local MSP430 MCU, which exposes information in the STEM I2C bus.

SYS I2C addresses

Reduced the devices connected to SYS bus

Address	Chipset	Description
0x20	PCA9555	16 bit expander EX0
0x25	PCA9450	Reserved 7 bit address
0x26	PCA9555	16 bit expander EX6
0x47	TUSB546	Power I2C - Host Alt. Mode
0x4A 0x4B	PCA9450	Power Management IC
0x68	PI6CG18200	PCIe clock generator
0x6A	BQ24250	LiPO Battery Charger
0x70 0x71	TPS65988	RESERVED for PD Controller Port 1 / SYS
0xD2/D3	RTC	AM1805 real time clock (RTC)

HD3SS3220

| No 0x25 on faceboard or power module !! |

Device Tree

device tree bindings for

- [TPS65988](#)
- [BQ24250](#)

Linux kernel support

- [BQ24250](#)
- [TPS65988 Linux](#)
- [BQ2425x Linux](#)
- [Linux Getting Driver For USB Type-C DisplayPort Alternate Mode](#)
- [API for USB Type-C Alternate Mode drivers](#)