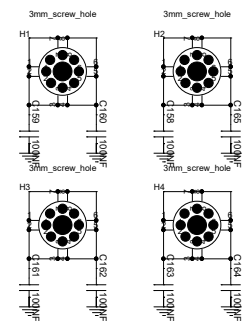


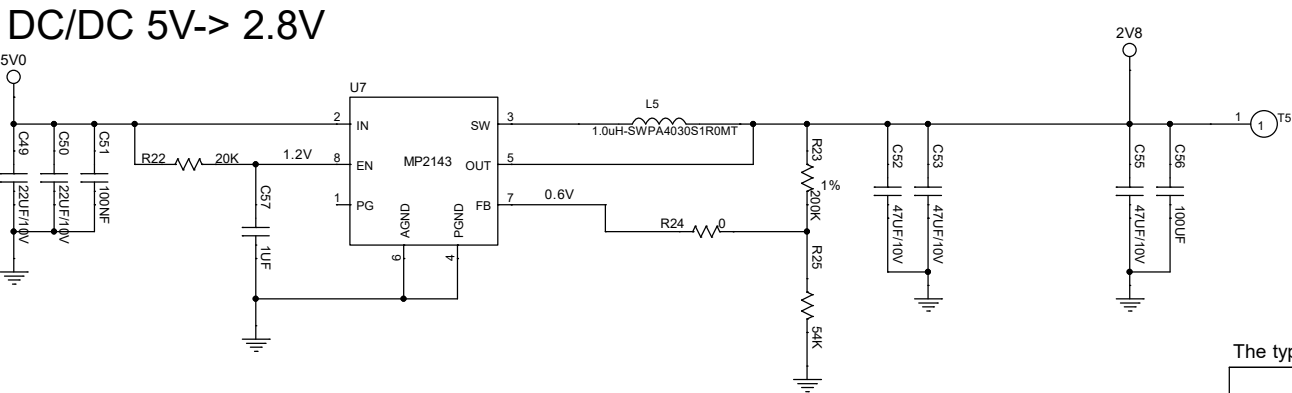
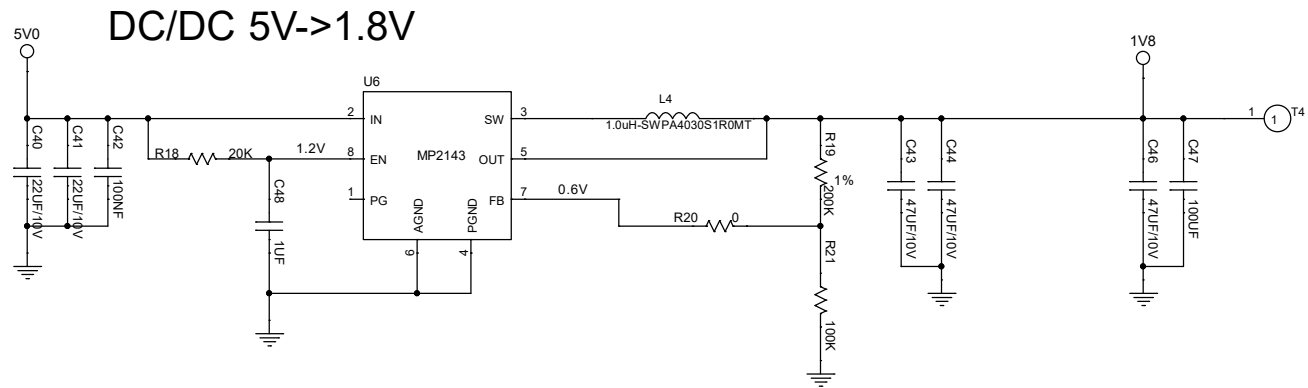
Power Supply1



The type and specification of the components refer to the BOM

				NA		
				ECA NO	DATE	
DESIGNED	XKC20201222	CM4 MODE				
REVIEWED	XKC					
			VER	PART NUMBER	SHEET 5 OF 34	
		B				

Power Supply2

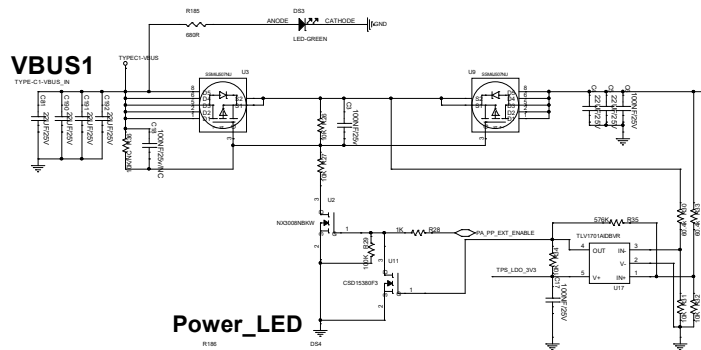


The type and specification of the components refer to the BOM

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REVIEWED	XKC				
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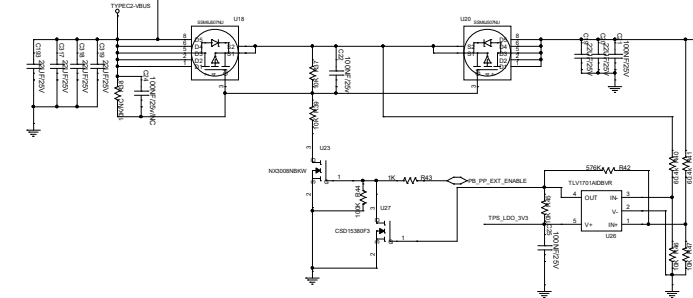
Power_LED

VBUS1
TYPE-C1-VBUS_IN



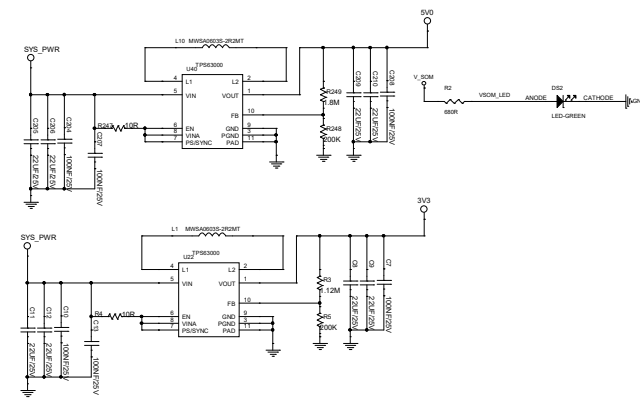
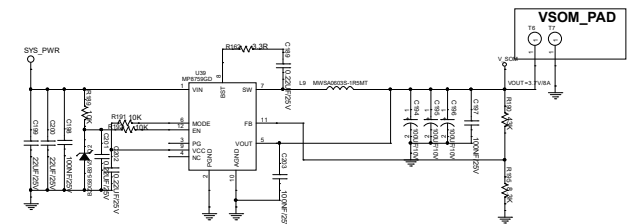
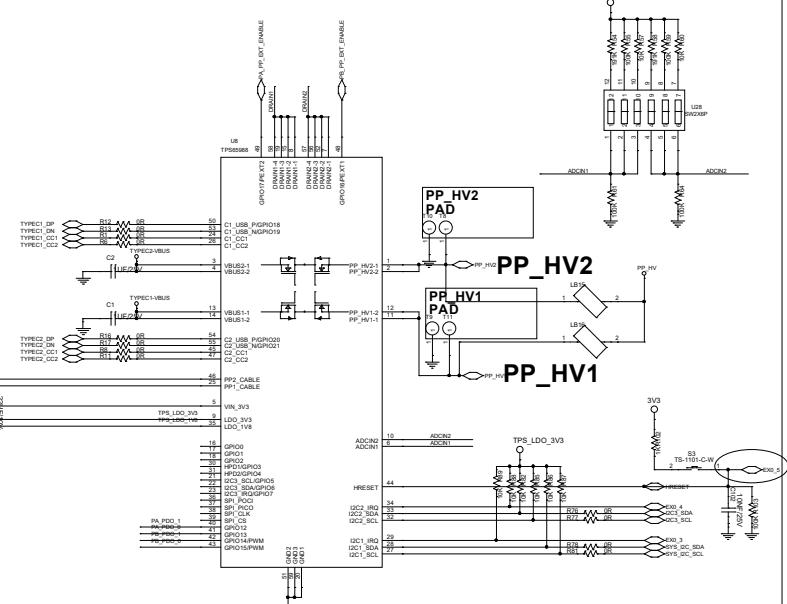
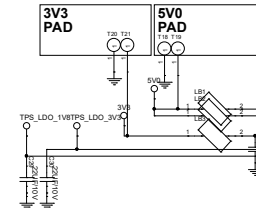
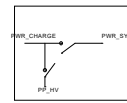
Power_LED

VBUS2
TYPE-C2-VBUS_IN



PA_PP_EXT

PB_PP_EXT



The type and specification of the components refer to the BOM

DESIGNED	KKC20201222	CM4 MODE	NA	DATE
REVIEWED	KKC	VER	PART NUMBER	SHEET 9 OF 34
		B		

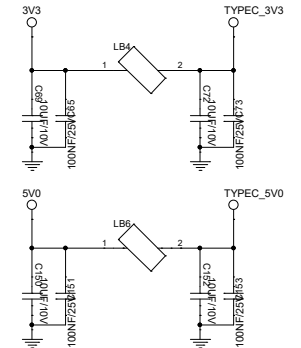
[illegible][illegible]

Pinout diagram for the J17 connector:

- TYPE1_VBUS: A4, A5, A6, A7, A8, A9, A10, A11, A12, A13
- TYPE1_CC2: B5
- TYPE1_CC1: B6
- TYPE1_DP: B7
- TYPE1_DN: B8
- TYPE1_SSTXP2: B2
- TYPE1_SSTRX2: B3
- TYPE1_SSTRXP: B10
- TYPE1_SSTRXN: B11
- TYPE1_SSTXP1: A3
- TYPE1_SSTRXN1: A4
- TYPE1_SSTRXP2: A11
- TYPE1_SSTRXN2: A10
- Type-C PORT 1: B1, B2, B3, B4, B5, B6, B7, B8, B9, B10, B11, B12, B13

Ground connections are shown for the Type-C PORT 1 and the connector pins B1, B2, B3, B4, B5, B6, B7, B8, B9, B10, B11, B12, and B13.

Pin-to-pin connection diagram for the USB2-UART bridge module. The diagram shows a central module with pins 1 through 47. On the left, pins 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 33, 35, 37, 39, 41, 43, and 45 are connected to a 3V3 supply. On the right, pins 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38, 40, 42, and 44 are connected to a 3V3 supply. The module has several signal pins: 1 (TYPE1_SBU1), 3 (TYPE1_SBU2), 5 (OTG_AP), 7 (OTG_AN), 9 (OTG_BP), 11 (OTG_BN), 13 (OTG_CP), 15 (OTG_CN), 17 (OTG_DP), 19 (EXTRA_USB1_DN), 21 (EXTRA_USB1_DP), 23 (HXB1_SEK), 25 (TRCLK), 27 (TRCLK), 29 (ETH0_MDIO0N), 31 (ETH0_MDIO1P), 33 (ETH0_MDIO2N), 35 (ETH0_MDIO3P), 37 (ETH0_MDIO3N), 39 (ETH0_LED_ACT/PHY), 41 (ETH0_LINK-LED_10_100), 43 (SYS_IDC_SDA), 45 (UART1_RXD), 47 (UART1_TXD). The module is connected to a 4-2328724-5 connector at the bottom.

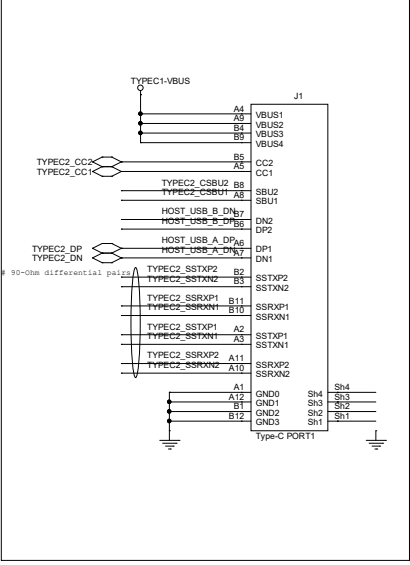
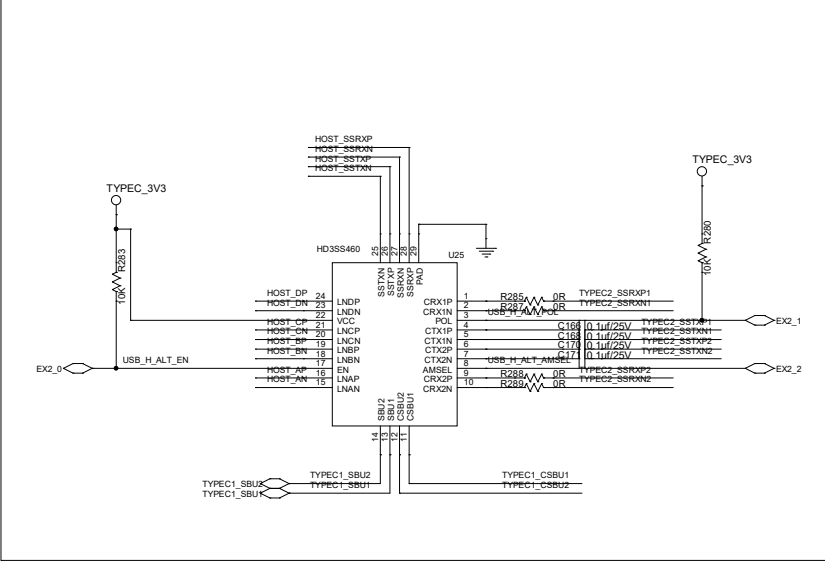
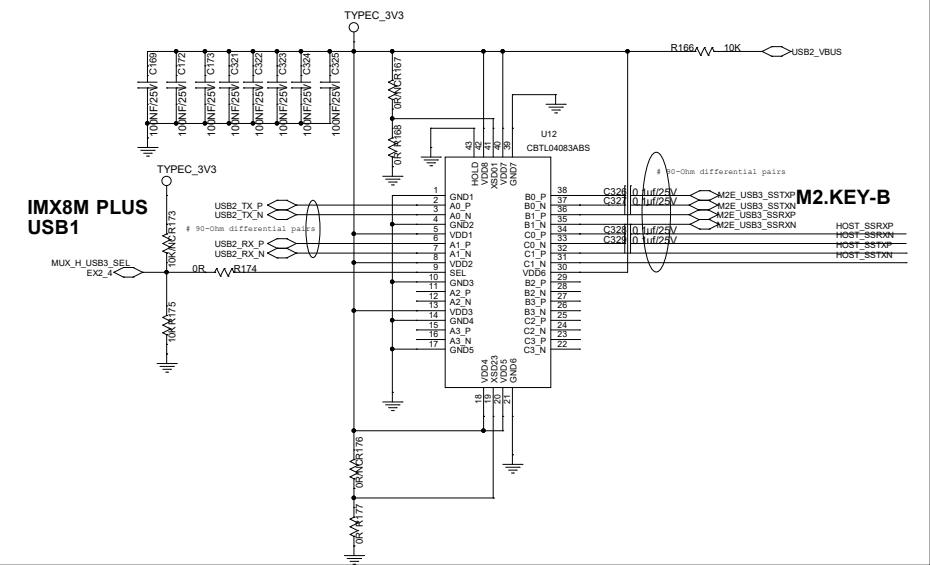


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DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 9	OF 34
		B			

CBTL04083

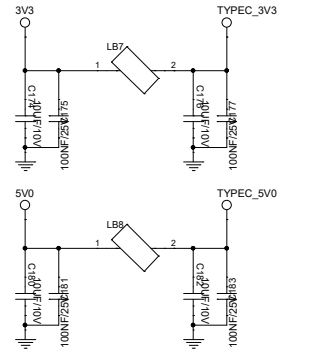
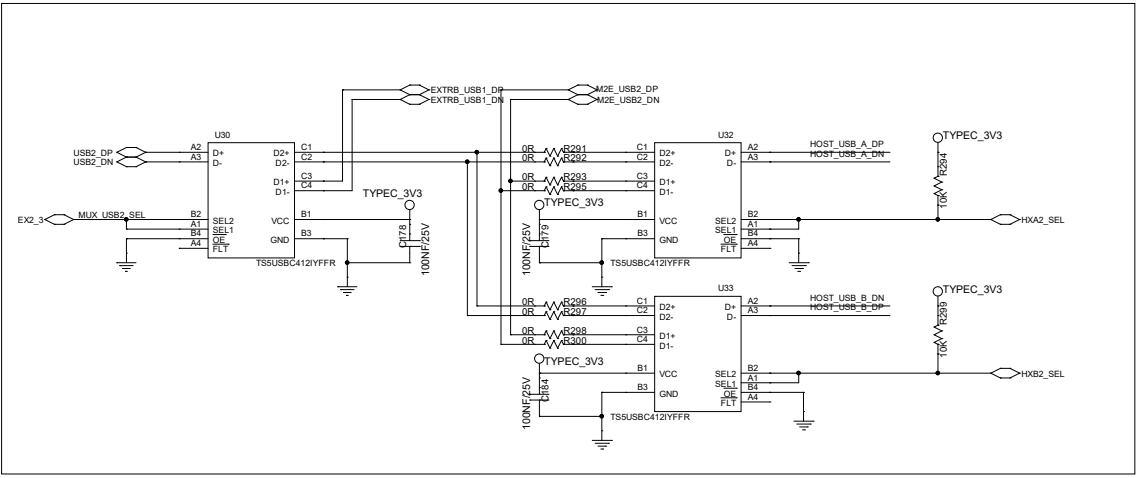
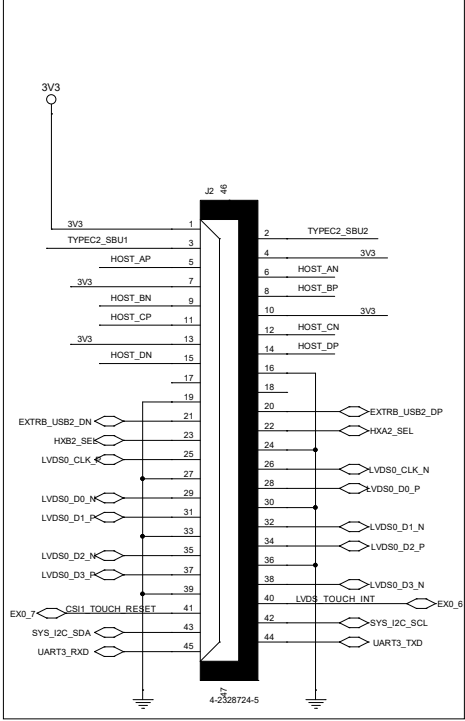
HD3SS460

TYPE-C Host



Host ALT

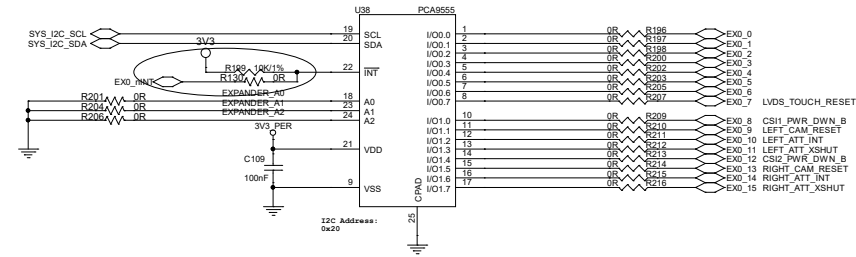
Host USB2.0



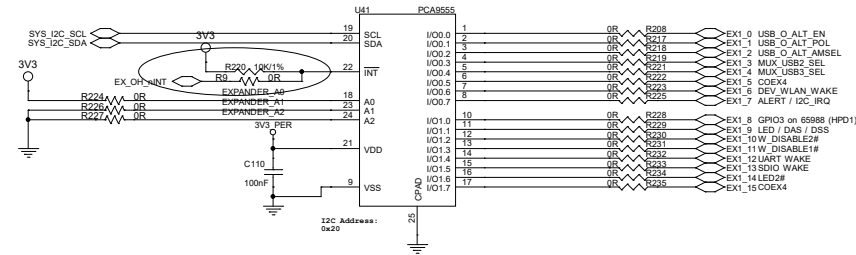
The type and specification of the components refer to the BOM

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DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC	VER	PART_NUMBER	SHEET 9 OF 34	
		B			

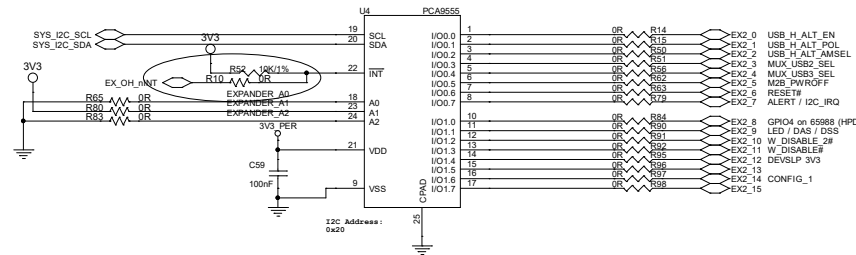
SYS I2C to GPIO expander-EX0
Addres:0X40



SYS I2C to GPIO expander-EX1
Addres:0X41



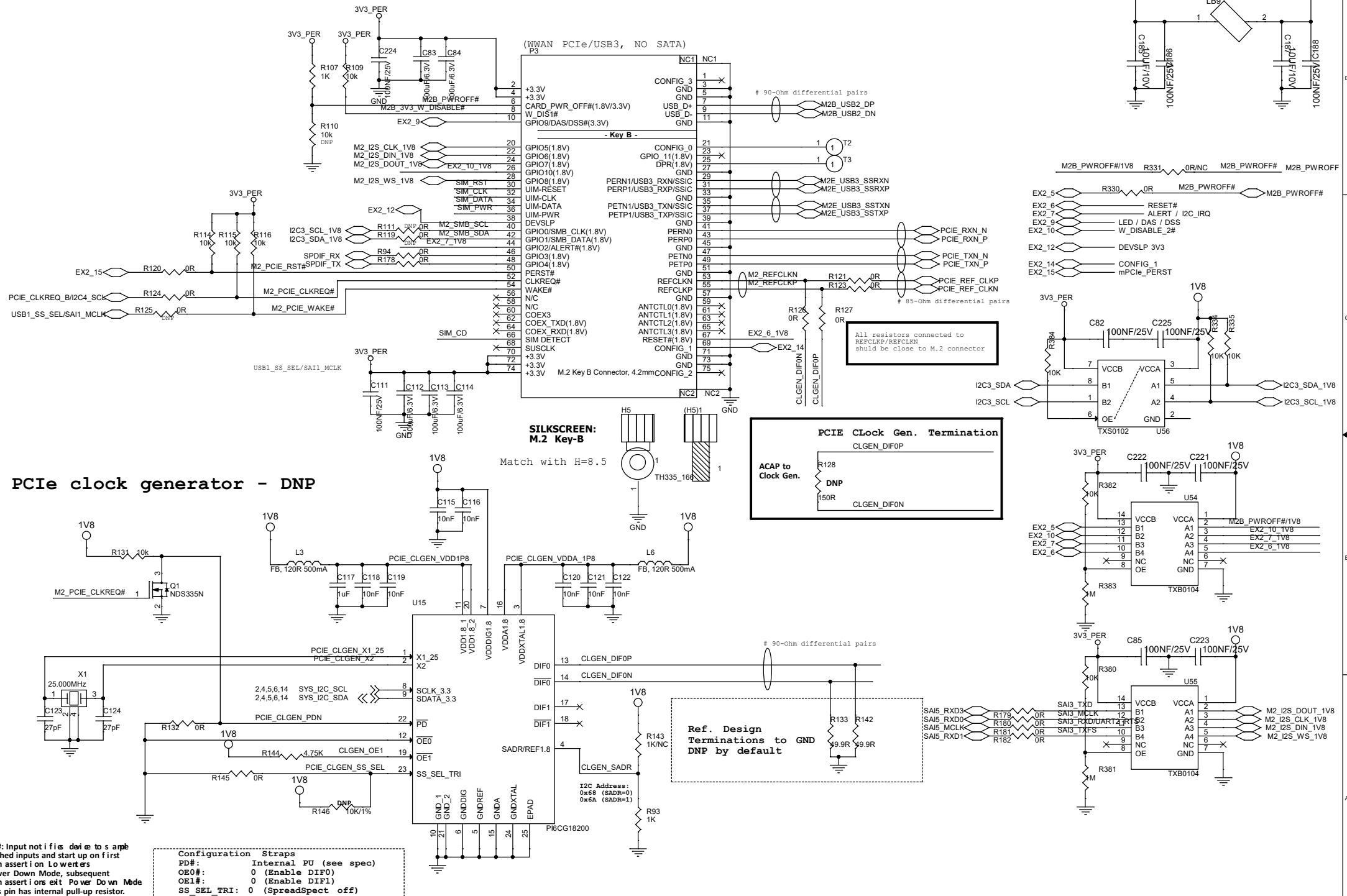
SYS I2C to GPIO expander-EX2
Addres:0X42



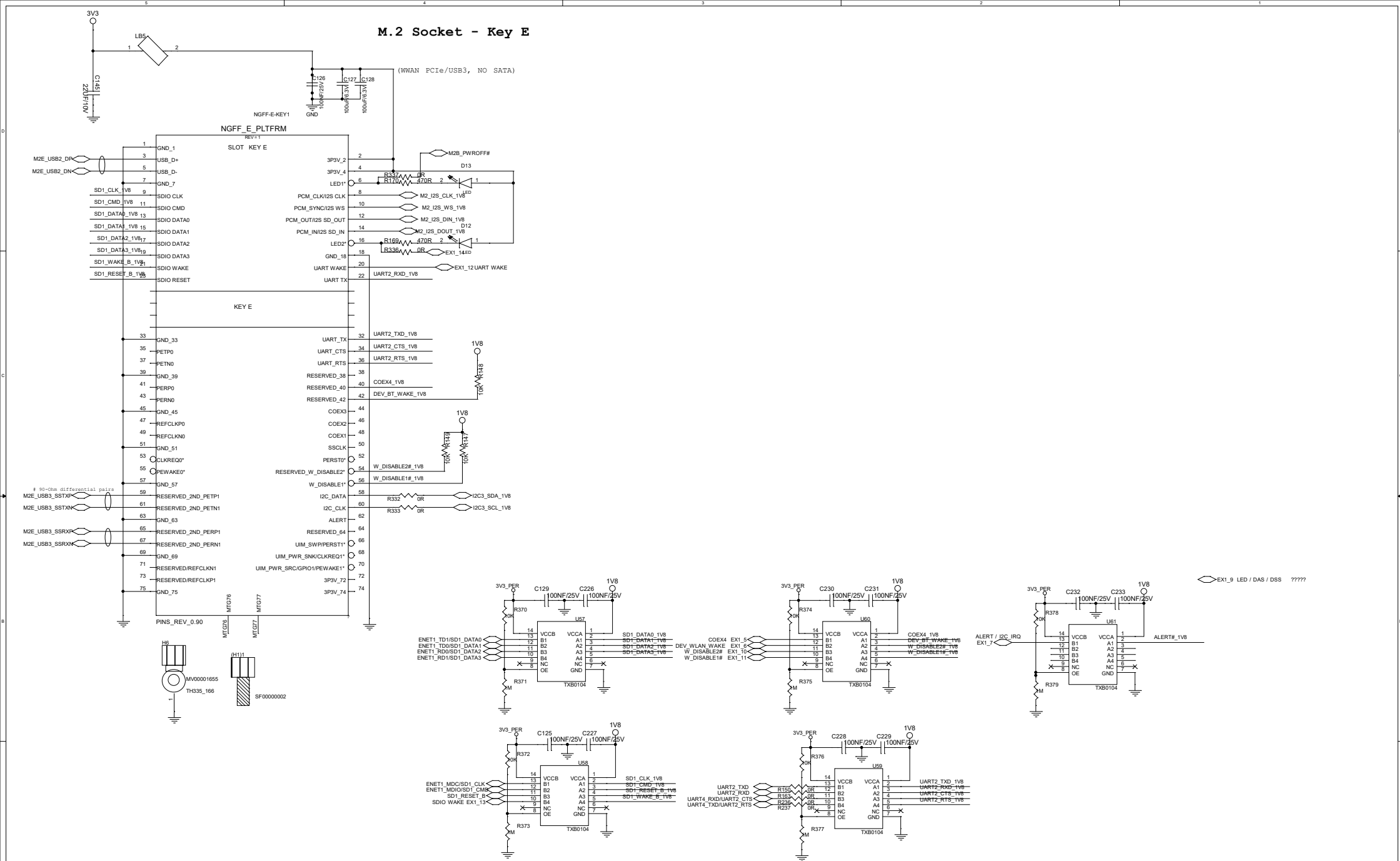
The type and specification of the components refer to the BOM

				NA	
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DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 9 OF 34	
		B			

M.2 Socket - Key B

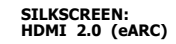


M.2 Socket - Key E



The type and specification of the components refer to the BOM

				NA	
				ECA NO	DATE
DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 9 OF 34	
		B			



100-Ohm diff pairs

EARC_P_UTIL

EARC_N_HPDI

C107 1uF

C108 1uF

EARC_P

EARC_N

TCE1210U L14

HDMI_UTILTY_CN

HDMI_HPDI_CN

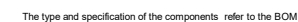
HDMI_HPDI_5V

R193 1.2K

R194 47K

The schematic diagram illustrates the HDMI HPD and CEC driver circuit. The circuit is powered by an HDMI_HP_D_5V supply and a 3V3_PER supply. The HDMI_HP input is connected to the driver IC (DMG1026UV-7) through a 1M resistor (R243). The driver IC controls two MOSFETs (Q1 and Q2) which drive the HDMI_CEC output line. A 20K resistor (R244) is connected to the HDMI_CEC output. A Schottky diode (D17, 1A) is connected in parallel with the output line to protect against reverse polarity. The HDMI_CEC output is also connected to the HDMI_CEC_3P3 pin.

				NA	
				ECA NO	DATE
DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 9 OF 34	
		B			



The type and specification of the components refer to the BOM				NA		
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DESIGNED	XXC20201222			CM4 MODE		
REVIEWED	XXC			VER	PART NUMBER	SHEET 9 OF 34
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