

Camera CSI Connectors

CSI connectors

The CSI connectors data lanes are connected directly together for each side. It is only possible to connect a single left and a single right camera module at a time.

The 22 pin and 30 pin CSI connectors are intended to be used without a daughter board and instead a separate i.MX8 development board is used. The CSI lanes on 34 pins connector is connected directly to the equivalent lines on the 30 pins. This assumes that a camera is connected to either a 34 pins connector or a 22 pins connector, not both. If a i.MX8 daughter board is used rather than development board the CSI lines from the daughter board must be connected to the 34 pin camera module connectors. i.MX8 CSI1 is used for left module, CSI2 is used for right module.

The two 34 pin CSI connectors are wired to run in sync via the STROBE pin.

If power isn't connected over the USB-C plugs, the camera modules should be powered over the MIPI CSI connectors. In this case it should be possible to use either the 22 pin connectors or the 30 pin connectors for inputting the signal and power. This means that the 22 pin connectors can be used to input or output MIPI CSI lanes.

Signals on the 34 pins connector are 1V8. SCCB for CSI1 is connected to I2C5 voltage shifted. SCCB for CSI2 is connected to I2C6 voltage shifted.

Power supply Camera Module

Power can be supplied from VSOM stepped down to 3V3, 2V8 and 1V8. 2V8 is only needed for the camera modules.

The 909 board can be used to test camera modules without any other module attached by using the 22 pins and 30 pins connectors. If a T-USB Power Module isn't connected, so VSOM isn't supplied, the power has to come from the 30 pins and 22 pins CSI connectors.

Camera Connectors

RPI FPC 22 pins

On the left(CSI1) side.

Pin	Code	Type	Details	Voltage
1	GND	Power	Ground	
2	CAM_D0_N	Data	MIPI Data Lane 0 Negative	
3	CAM_D0_P	Data	MIPI Data Lane 0 Positive	
4	GND	Power	Ground	

Pin	Code	Type	Details	Voltage
5	CAM_D1_N	Data	MIPI Data Lane 1 Negative	
6	CAM_D1_P	Data	MIPI Data Lane 1 Positive	
7	GND	Power	Ground	
8	CAM_CK_N	Data	MIPI Clock Lane Negative	
9	CAM_CK_P	Data	MIPI Clock Lane Positive	
10	GND	Power	Ground	
11	CAM_D2_N	Data	MIPI Data Lane 2 Negative	
12	CAM_D2_P	Data	MIPI Data Lane 2 Positive	
13	GND	Power	Ground	
14	CAM_D3_N	Data	MIPI Data Lane 3 Negative	
15	CAM_D3_P	Data	MIPI Data Lane 3 Positive	
16	GND	Power	Ground	
17	CAM_IO0	Power	Power Enable	
18	CAM_IO1	LED	LED Indicator	
19	GND	Power	Ground	
20	SCL	I2C	I2C SCL	
21	SDA	I2C	SCCB serial Interface data IO	
22	VCC	Power	3.3V Power Supply	

NVIDIA FPC 30 pins

On the right(CSI2) side.

The connector is an [I-PEX type 20525-030E-02](#) with 0.4mm pitch & 30 pins. Data pins are 1.8V level.

Pin	Code	Details
1	CAM_3V3	3.3V Power Input
2	CAM_3V3	
3	CAM_1V8	1.8V Power Input
4	GND	
5	GND	
6	PWR DWN	PWRDN on 34pin
7	I2C SCL	

Pin	Code	Details
8	I2C SDA	
9	GND	
10	CSI D2-	
11	CSI D2+	
12	TRIGGER	
13	MCLK	EXTCLK on 34pin
14	Reserved	
15	CSI D1-	
16	CSI D1+	
17	GND	
18	GND	
19	CSI D0-	
20	CSI D0+	
21	RESET	RESET on 34pin
22	GND	
23	Reserved	
24	CSI CLK-	
25	CSI CLK+	
26	GND	
27	CSI D3-	
28	CSI D3+	
29	Flash	
30	Reserved	

Refs

- <https://www.leopardimaging.com/product/accessories/cables/faw-1233-03/>
- https://www.mouser.com/datasheet/2/233/LI-TX1-CB-6CAM_datasheet-1395894.pdf
- https://connecttech.com/ftp/pdf/ASG006_Spacely.pdf
- <https://www.i-pex.com/product/cabline-ca>

Ziloo Camera Module 34 pin connector

Just to be clear: All CSI lanes are laid out on one side of the connector with GND between.

Pin 1 is indicated on the board by a dot.

Toward thin part with microphone and other sensors

Pin	Code	Type	Details	Voltage
1	AF_VDD	Power	Reserved for Autofocus	3.3V
2	AVDD_2V8	Power	Analog, Max 500mA	2.8V
3	DOVDD	Power	Power for I/O circuit, Max 500mA	1.8V
4	VCC_1V8	Power	1.8V ,MAX 200mA	1.8V
5	GND	Power	GND	
6	CAM_FSIN	I/O	Frame sync input	
7	CAM_STROBE	I/O	Frame sync output	
8	EXTCLK	Input	External Clock Input (MCLK)	
9	ATT_INT	Output	Interrupt Attached Sensor, Active L	1.8V?
10	ATT_XSHUT	Input	Attached Sensor XSHUTDOWN	1.8V
11	Reserved	AF/PWM	PWM Motor control (NC)	
12	I2C_SCL	I/O	I2C?_SCL(pullup resistor 2.2K)	1.8V
13	I2C_SDA	I/O	I2C?_SDA(pullup resistor 2.2K)	1.8V
14	BCLK / SCK	I2S	Bit clock line	1.8V
15	WS / LRCLK	I2S	Word clock line	1.8V
16	SDATA1	I2S	Input data 1	1.8V
17	SDATA2	I2S	Input data 2 (NC)	1.8V

Towards image sensors

Pin	Code	Type	Details	Voltage
34	AGND	Power	Analog ground	
33	RESET	Input	Camera Reset, Active Low (RSTB)	
32	PWRDN	Input	Camera Power Down	
31	Reserved			
30	Reserved			
29	-		GND	
28	CSI_RX_D0P	Camera	MIPI_CSI_RX_D0+	1.8V
27	CSI_RX_D0N	Camera	MIPI_CSI_RX_D0-	1.8V

Pin	Code	Type	Details	Voltage
26	-		GND	
25	CSI_RX_D1P	Camera	MIPI_CSI_RX_D1+	1.8V
24	CSI_RX_D1N	Camera	MIPI_CSI_RX_D1-	1.8V
23	-		GND	
22	CSI_RX_D2P	Camera	MIPI_CSI_RX_D2+	1.8V
21	CSI_RX_D2N	Camera	MIPI_CSI_RX_D2-	1.8V
20	-		GND	
19	CSI_RX_CLKP	Camera	MIPI_CSI_RX_CLK+	1.8V
18	CSI_RX_CLKN	Camera	MIPI_CSI_RX_CLK-	1.8V