

DisplayPort Source

USB3.0 Source

Debug Connectors

Port A SS MUX

System Power

Port B SS MUX

Tiva & Debug

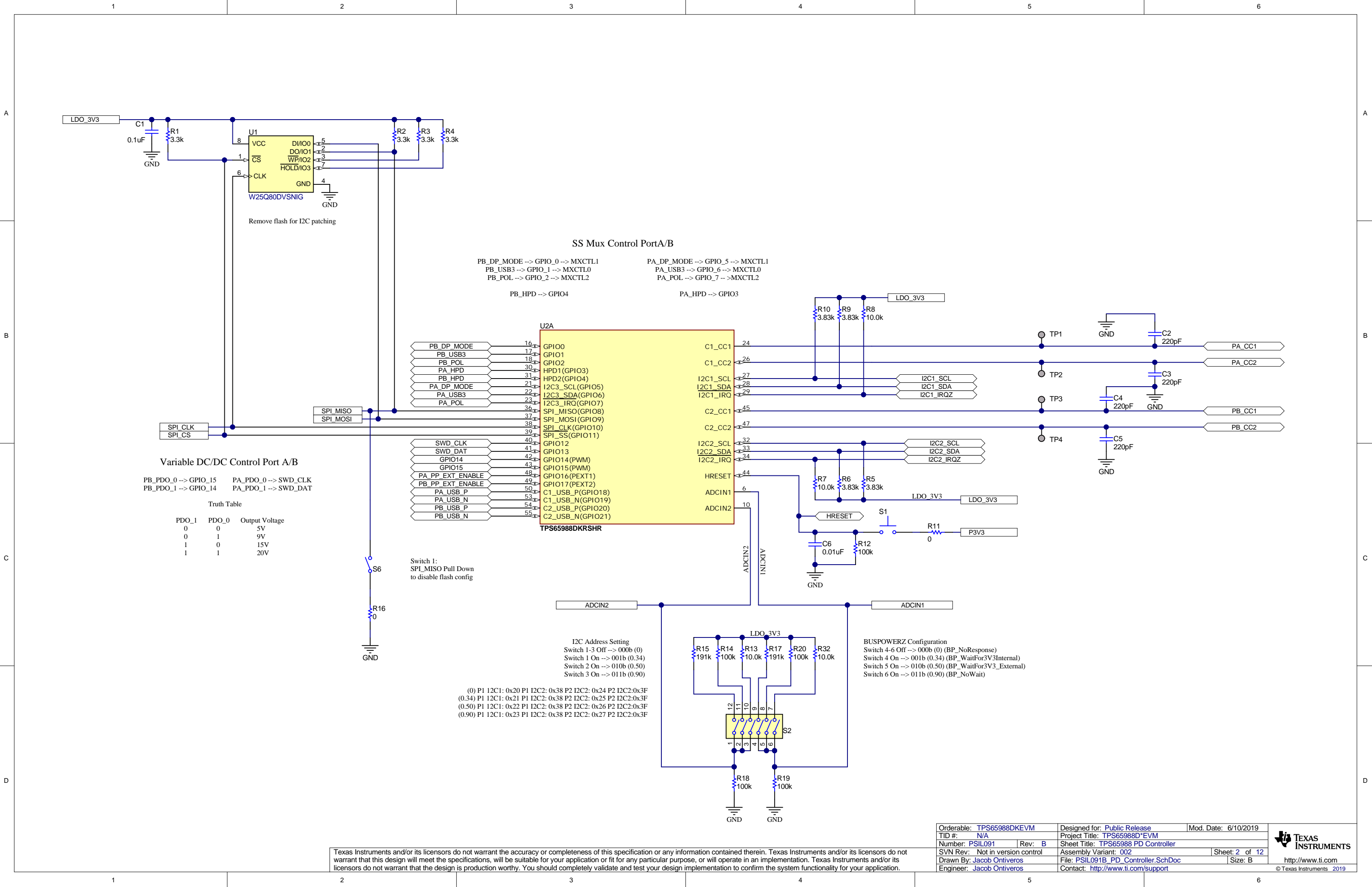
Port A Type-C

Port B Type-C

| | | | | | |
|------------|------------------------|-------------------|-------------------------------|--------------|-----------------------------|
| Orderable: | TPS65988DKEVM | Designed for: | Public Release | Mod. Date: | 8/10/2020 |
| TID #: | N/A | Project Title: | TPS65988DKEVM | | |
| Number: | PSIL091 | Rev: | B | Sheet Title: | TPS65988DKEVM Block Diagram |
| SVN Rev: | Not in version control | Assembly Variant: | 002 | Sheet: | 1 of 12 |
| Drawn By: | Jacob Ontiveros | File: | PSIL091B_Block_Diagram.SchDoc | Size: | C |
| Engineer: | Jacob Ontiveros | Contact: | http://www.ti.com/support | | |



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A

B

C

D

A

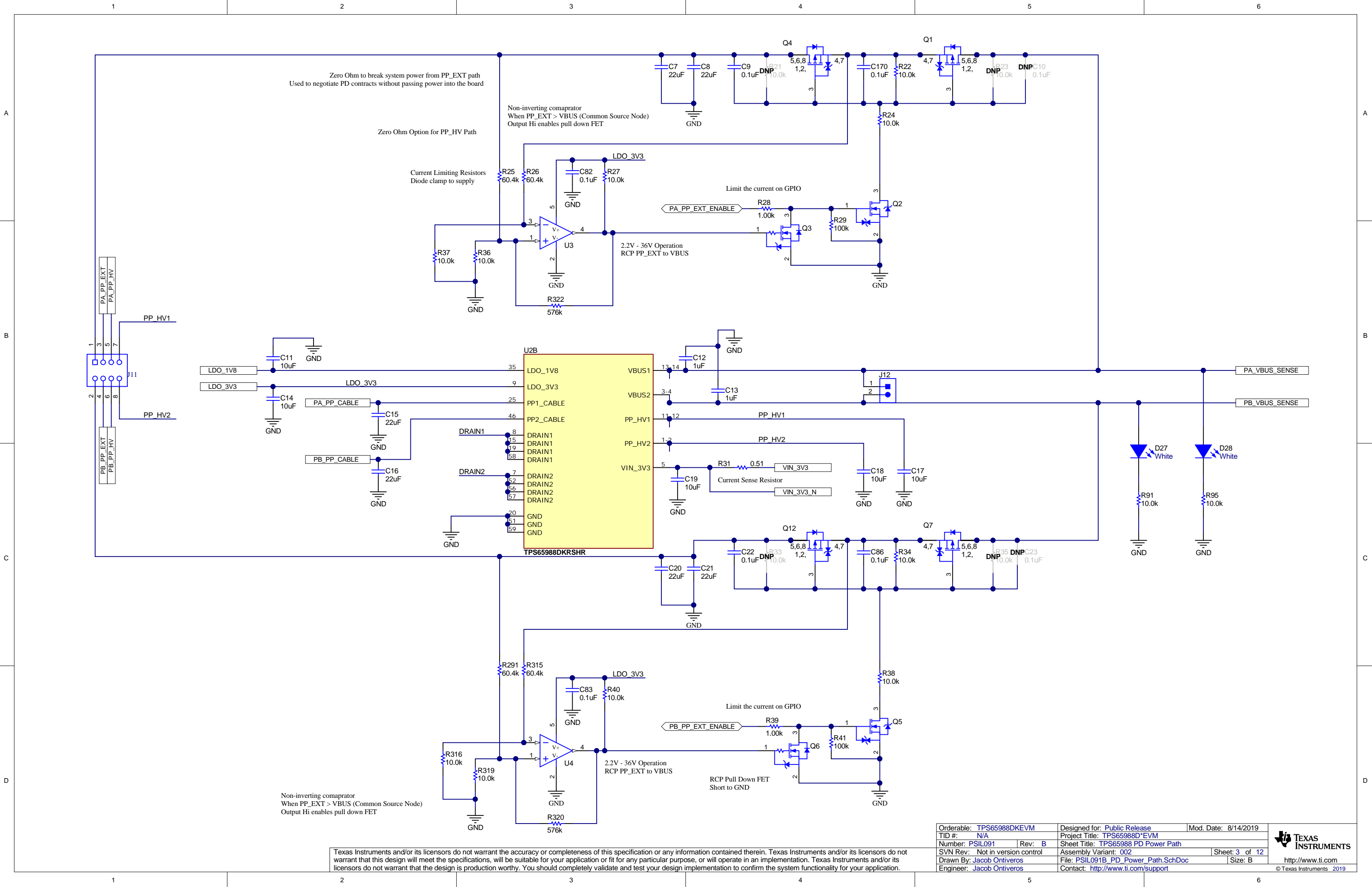
B

C

D

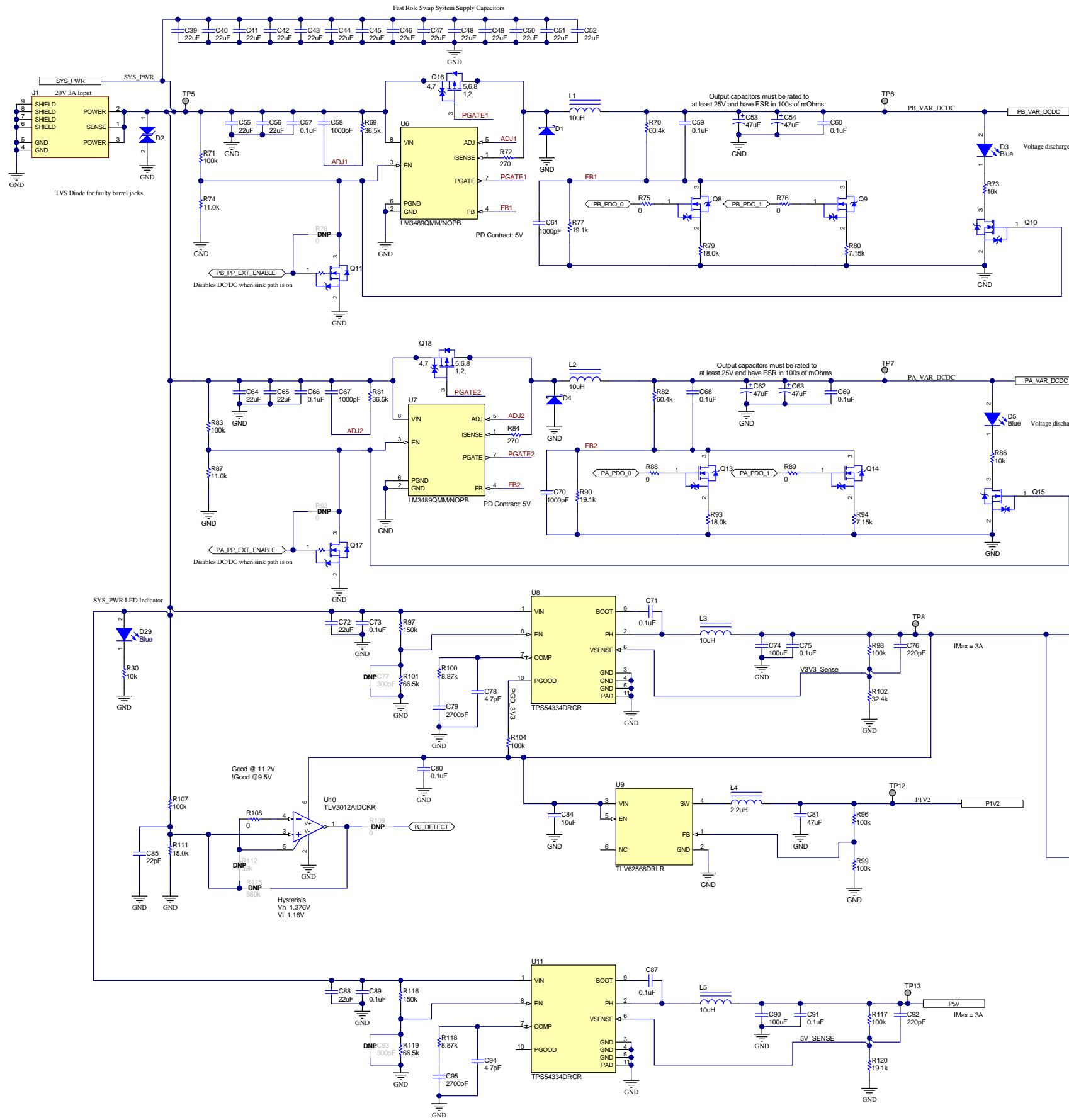
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|---|---|---|
| Orderable: TPS65988DKEVM | Designed for: Public Release | Mod. Date: 6/10/2019 |
| TID #: N/A | Project Title: TPS65988D*EVM | |
| Number: PSIL091 | Rev: B | Sheet Title: TPS65988 PD Controller |
| SVN Rev: Not in version control | Assembly Variant: 002 | Sheet: 2 of 12 |
| Drawn By: Jacob Ontiveros | File: PSIL091B_PD_Controller.SchDoc | Size: B |
| Engineer: Jacob Ontiveros | Contact: http://www.ti.com/support | |



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|---|---|---|
| Orderable: TPS65988DKEVM | Designed for: Public Release | Mod. Date: 8/14/2019 |
| TID #: N/A | Project Title: TPS65988D*EVM | |
| Number: PSIL091 | Rev: B | Sheet Title: TPS65988 PD Power Path |
| SVN Rev: Not in version control | Assembly Variant: 002 | Sheet: 3 of 12 |
| Drawn By: Jacob Ontiveros | File: PSIL091B_PD_Power_Path.SchDoc | Size: B |
| Engineer: Jacob Ontiveros | Contact: http://www.ti.com/support | |

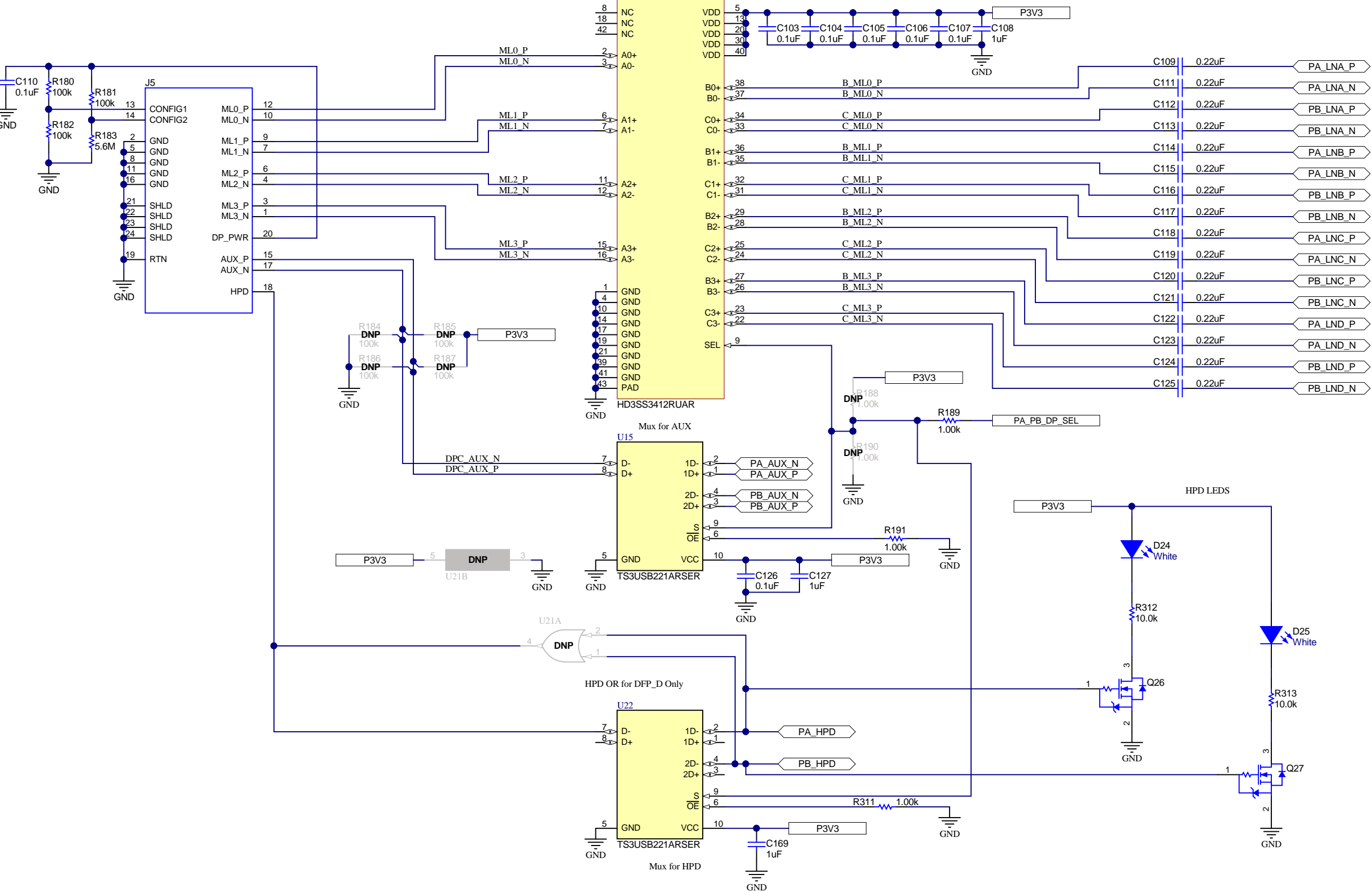


A

B

C

D

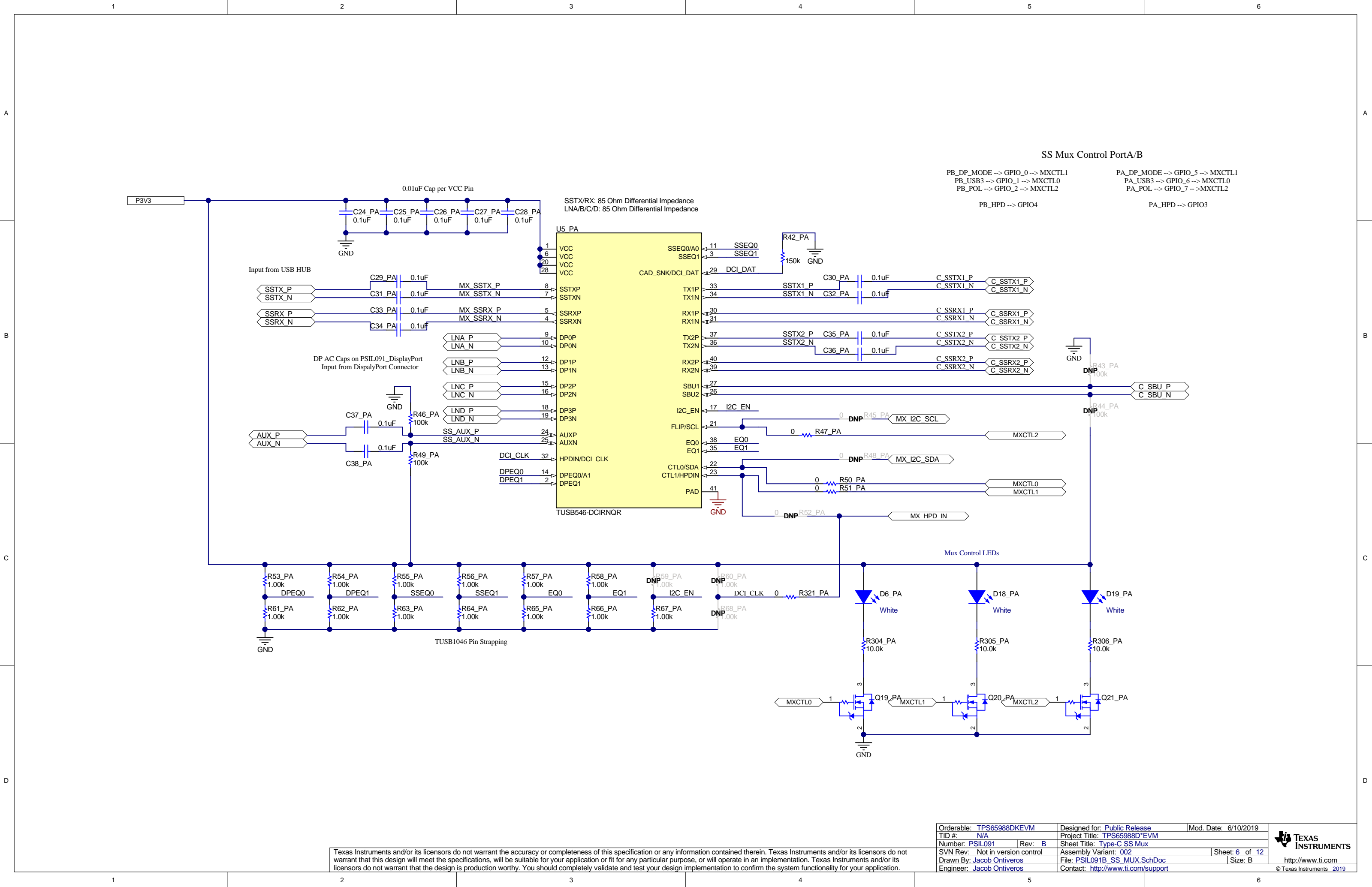


A

B

C

D



SS Mux Control PortA/B

PB_DP_MODE --> GPIO_0 --> MXCTL1
PB_USB3 --> GPIO_1 --> MXCTL0
PB_POL --> GPIO_2 --> MXCTL2
PB_HPD --> GPIO4
PA_DP_MODE --> GPIO_5 --> MXCTL1
PA_USB3 --> GPIO_6 --> MXCTL0
PA_POL --> GPIO_7 --> MXCTL2
PA_HPD --> GPIO3

A

A

B

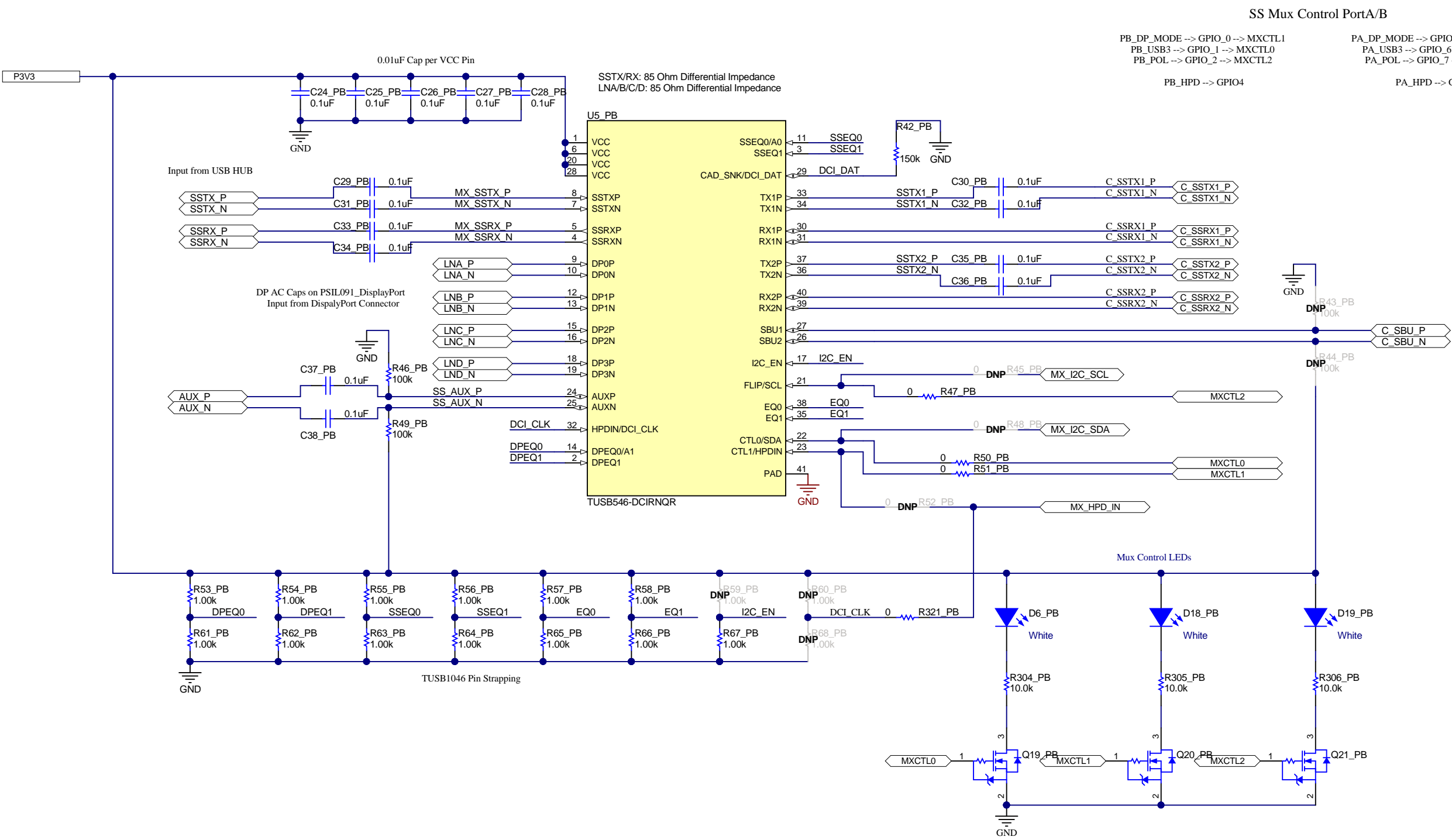
B

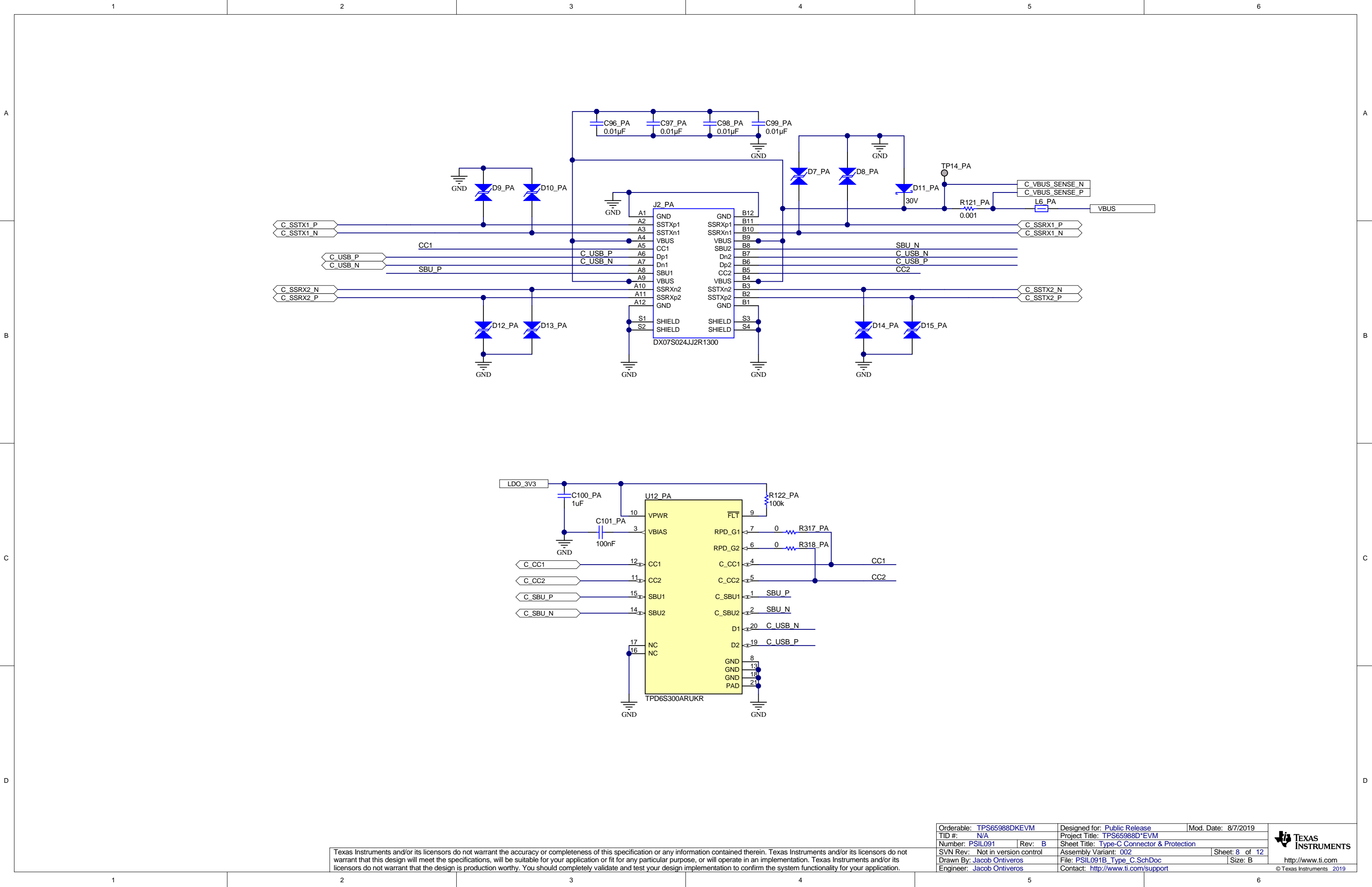
C

C

D

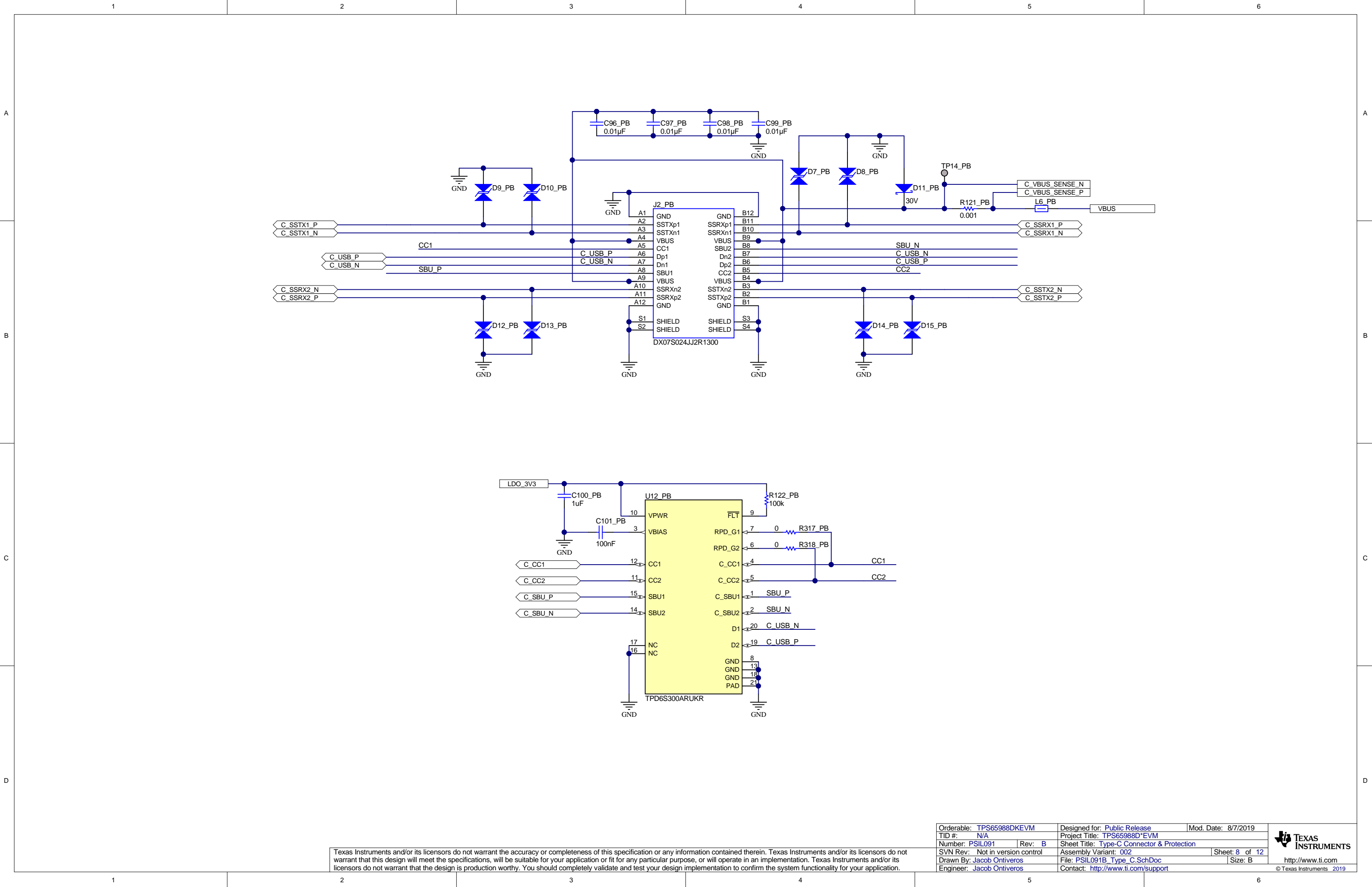
D





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| Orderable: TPS65988DKEVM | | Designed for: Public Release | Mod. Date: 8/7/2019 |
| TID #: | N/A | Project Title: TPS65988D*EVM | |
| Number: PSIL091 | Rev: B | Sheet Title: Type-C Connector & Protection | |
| SVN Rev: Not in version control | Assembly Variant: 002 | | Sheet: 8 of 12 |
| Drawn By: Jacob Ontiveros | File: PSIL091B_Type_C.SchDoc | | Size: B |
| Engineer: Jacob Ontiveros | Contact: http://www.ti.com/support | | |



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