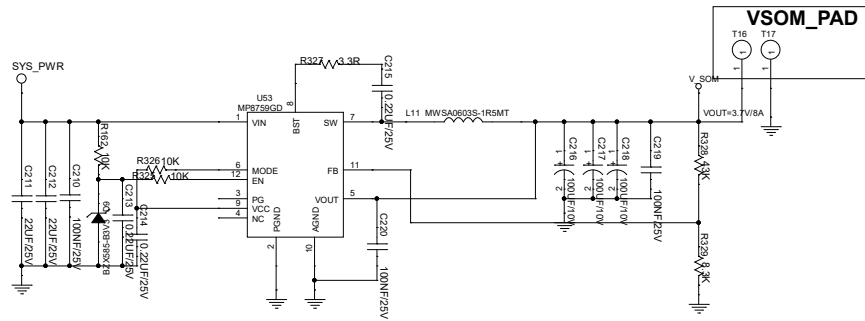
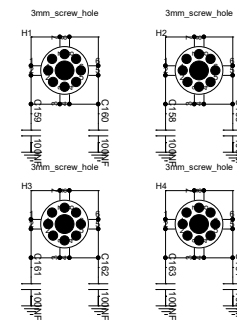
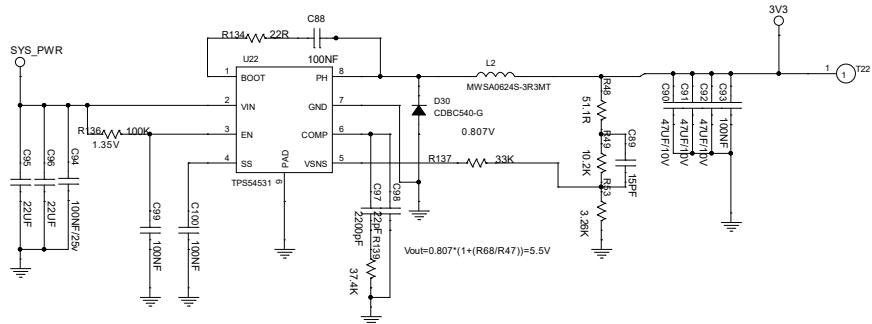
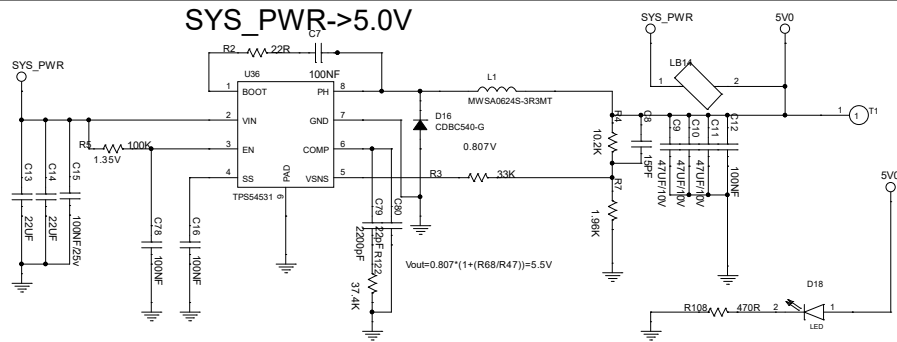
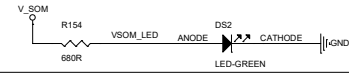


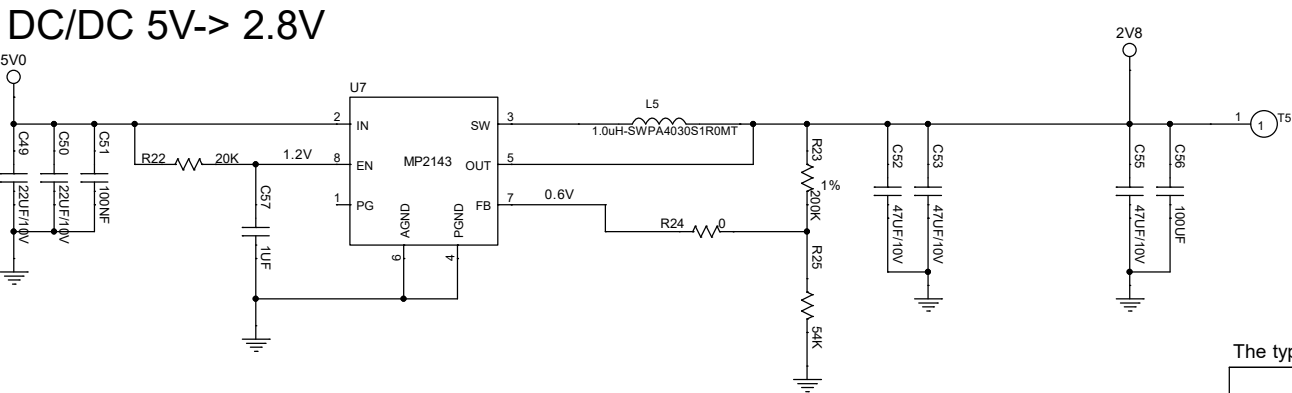
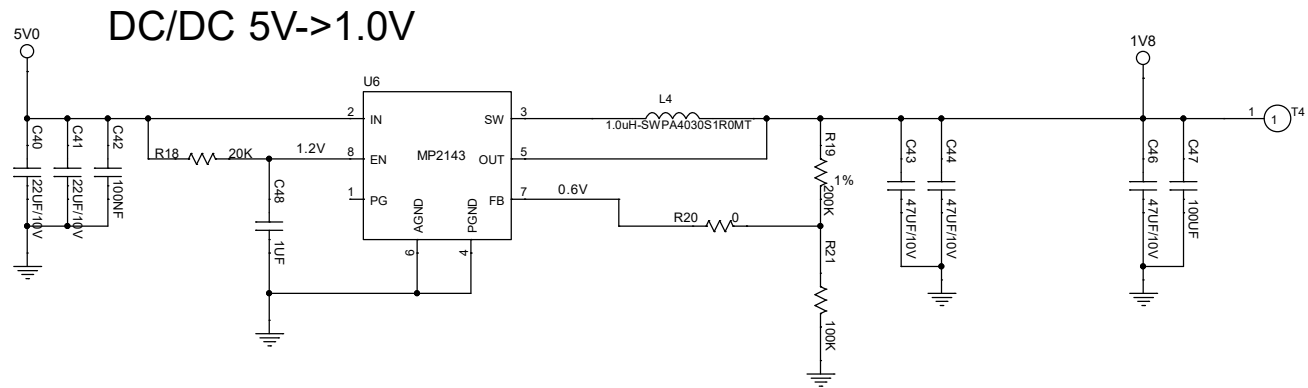
Power Supply1

**Power_LED**

The type and specification of the components refer to the BOM

					NA	
					ECA NO	DATE
DESIGNED	XKC20201222	CM4 MODE				
REVIEWED	XKC					
		VER	PART_NUMBER		SHEET 5	OF 34
		B				

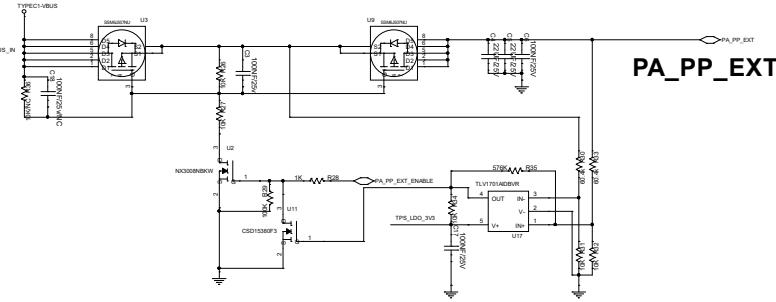
Power Supply2



The type and specification of the components refer to the BOM

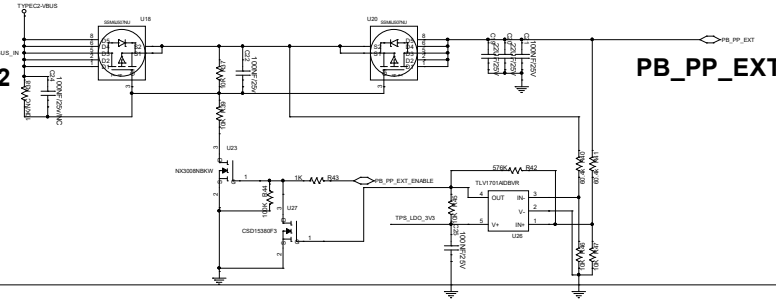
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REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 7 OF 34	
		B			

VBUS1

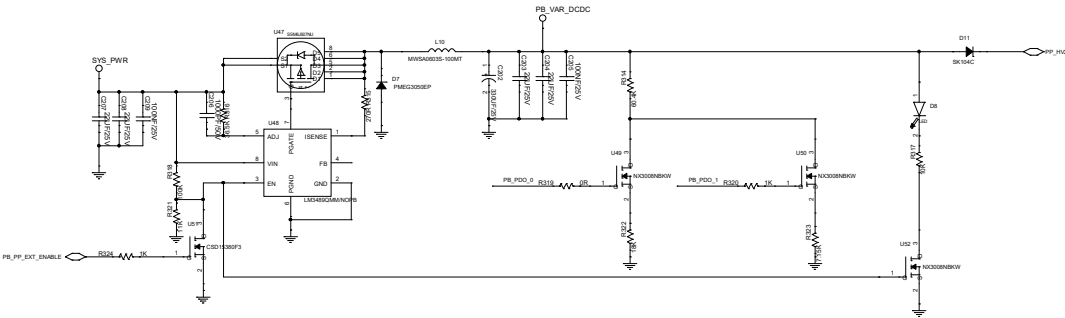
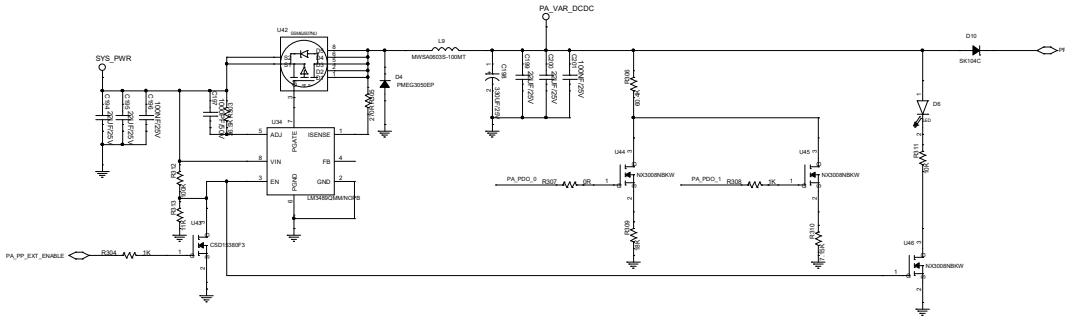
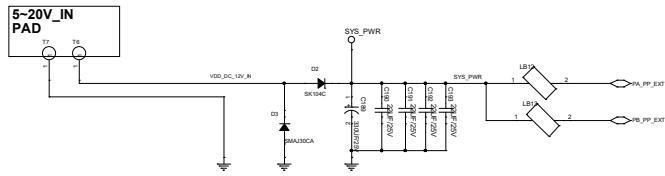
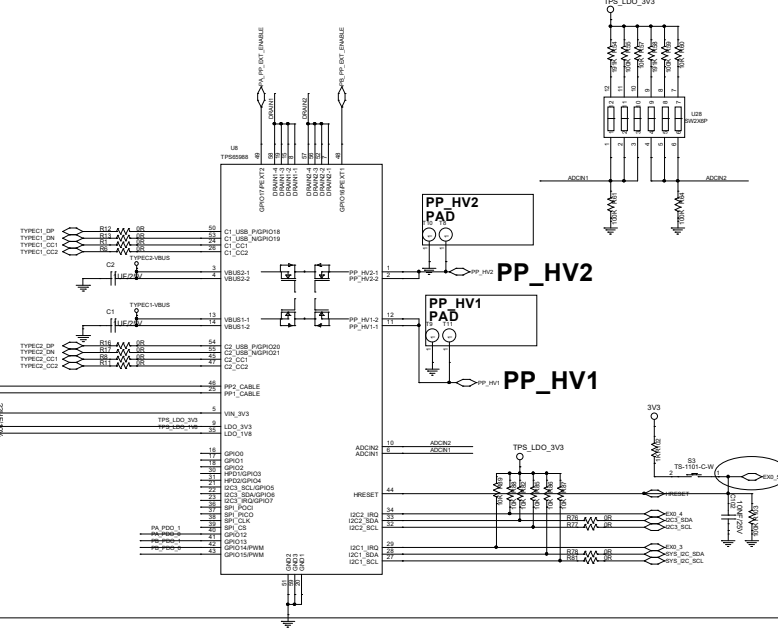
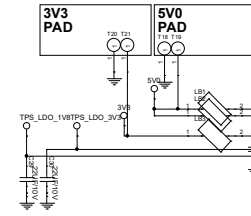


PA_PP_EXT

VBUS2



PB_PP_EXT

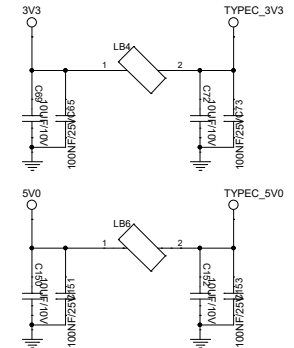


The type and specification of the components refer to the BOM

			NA	
			ECANO	DATE
DESIGNED	KKC20201222	CM4 MODE		
REVIEWED	KXC	VER	PART NUMBER	SHEET 9 OF 34
		B		

[illegible][illegible][illegible]

Pin configuration diagram for the TQ2450 module. The diagram shows a central component with pins numbered 1 to 47. Pins 1-15 are on the left, 16-30 on the right, and 31-47 on the bottom. Power pins (3V3, GND) are indicated at various points. Signal pins are labeled with names like TYPEC1_SBU1, OTG_AP, OTG_AN, etc. Some pins have internal pull-up or pull-down resistors indicated by triangles. A dashed box groups pins 24-27 and 28-31. A note '4-2328724-5' is at the bottom left.

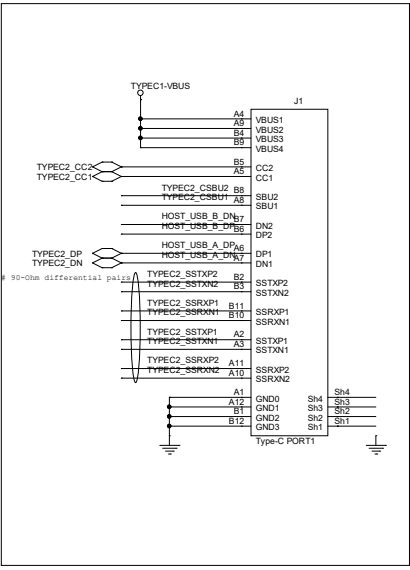
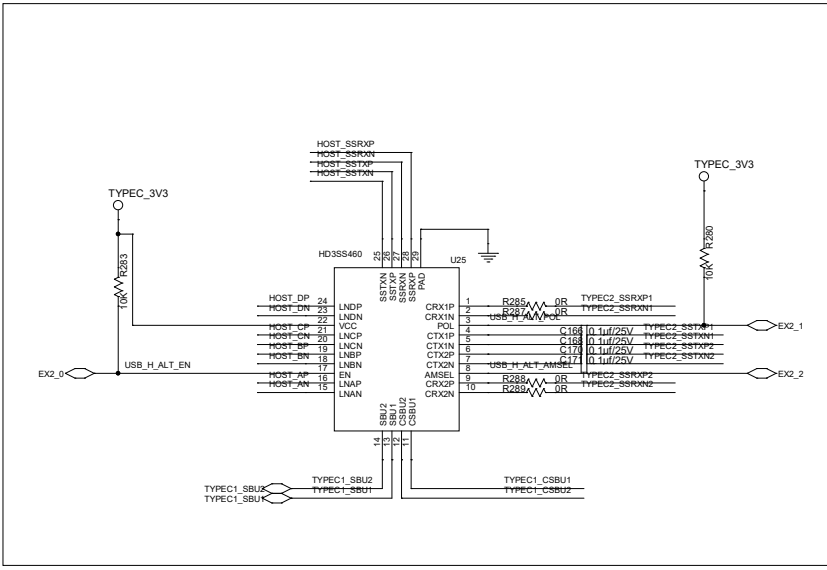
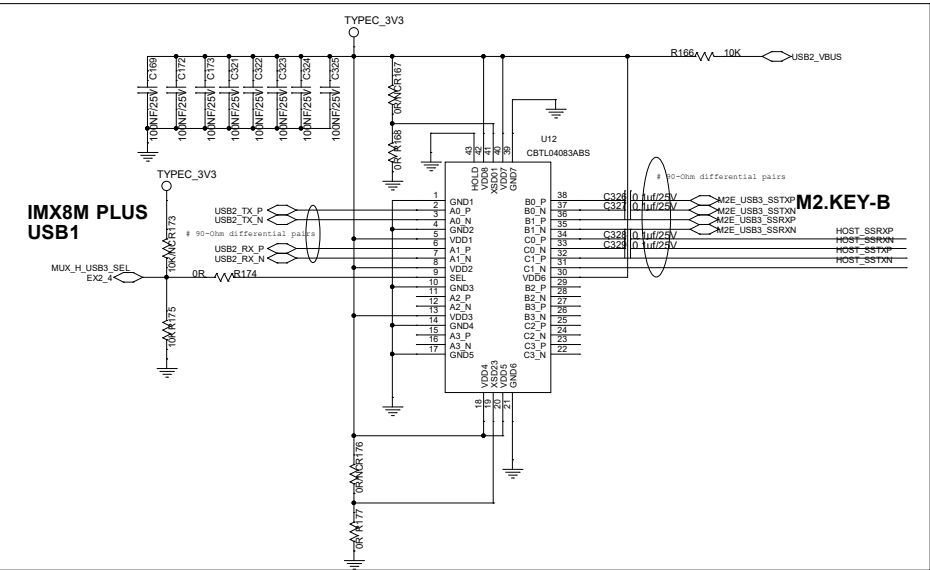


				NA	
				ECA NO.	DATE
DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 9 OF 34	
		B			

CBTL04083

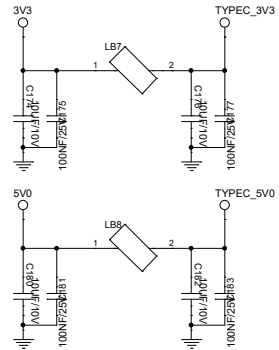
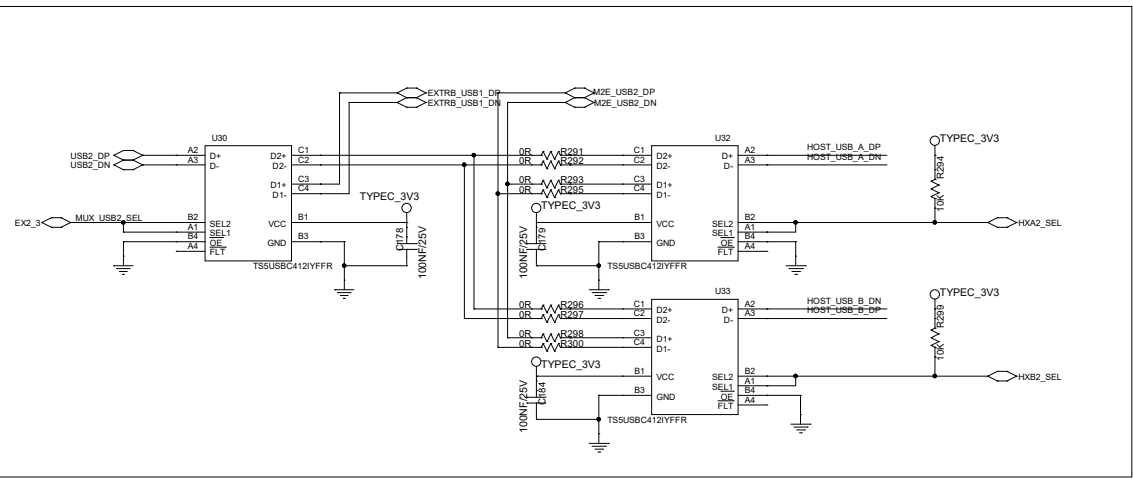
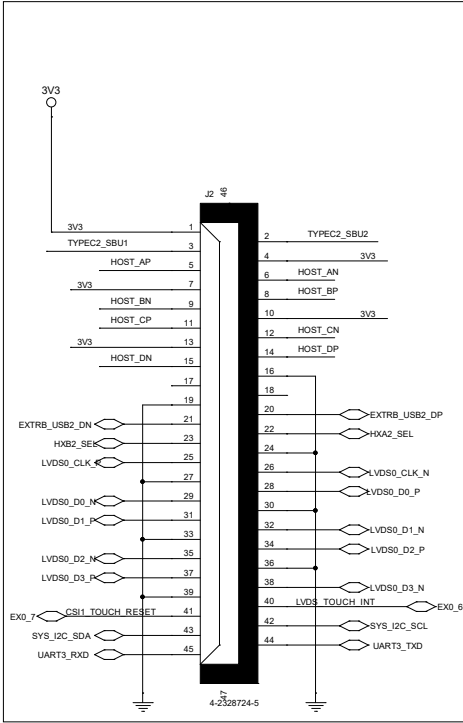
HD3SS460

TYPE-C Host



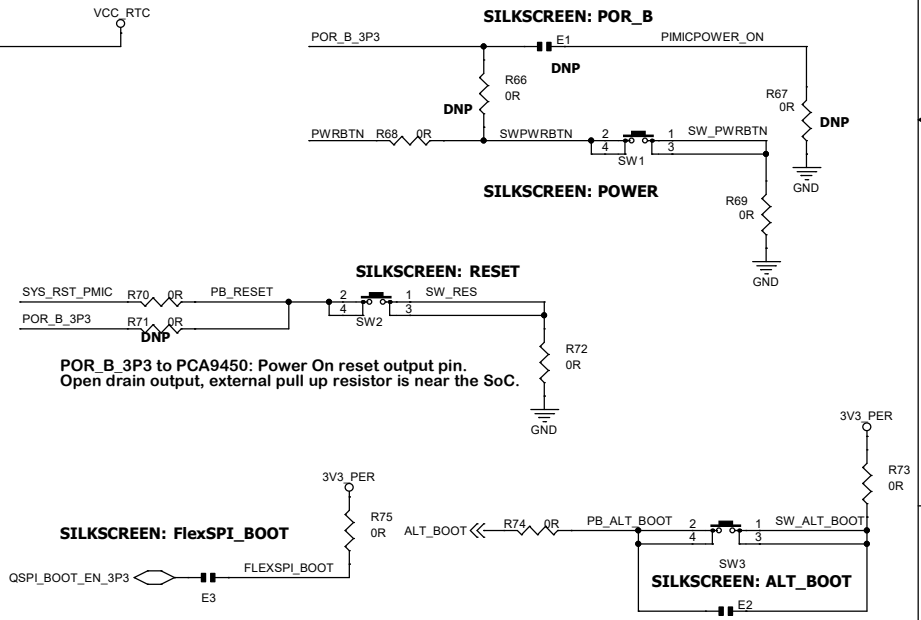
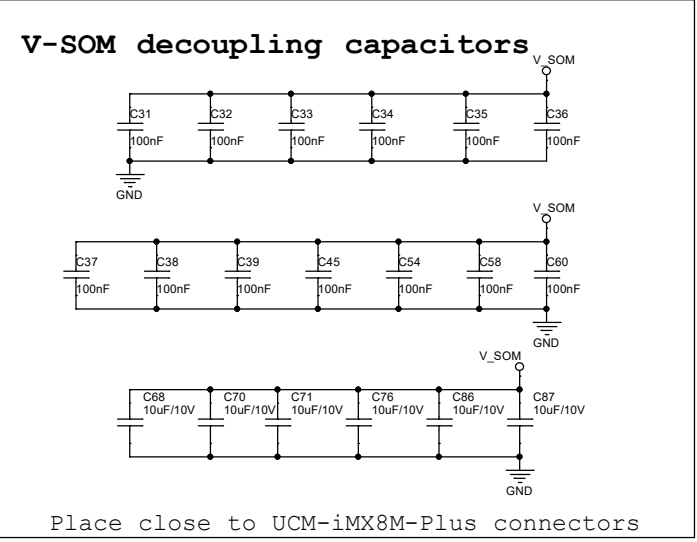
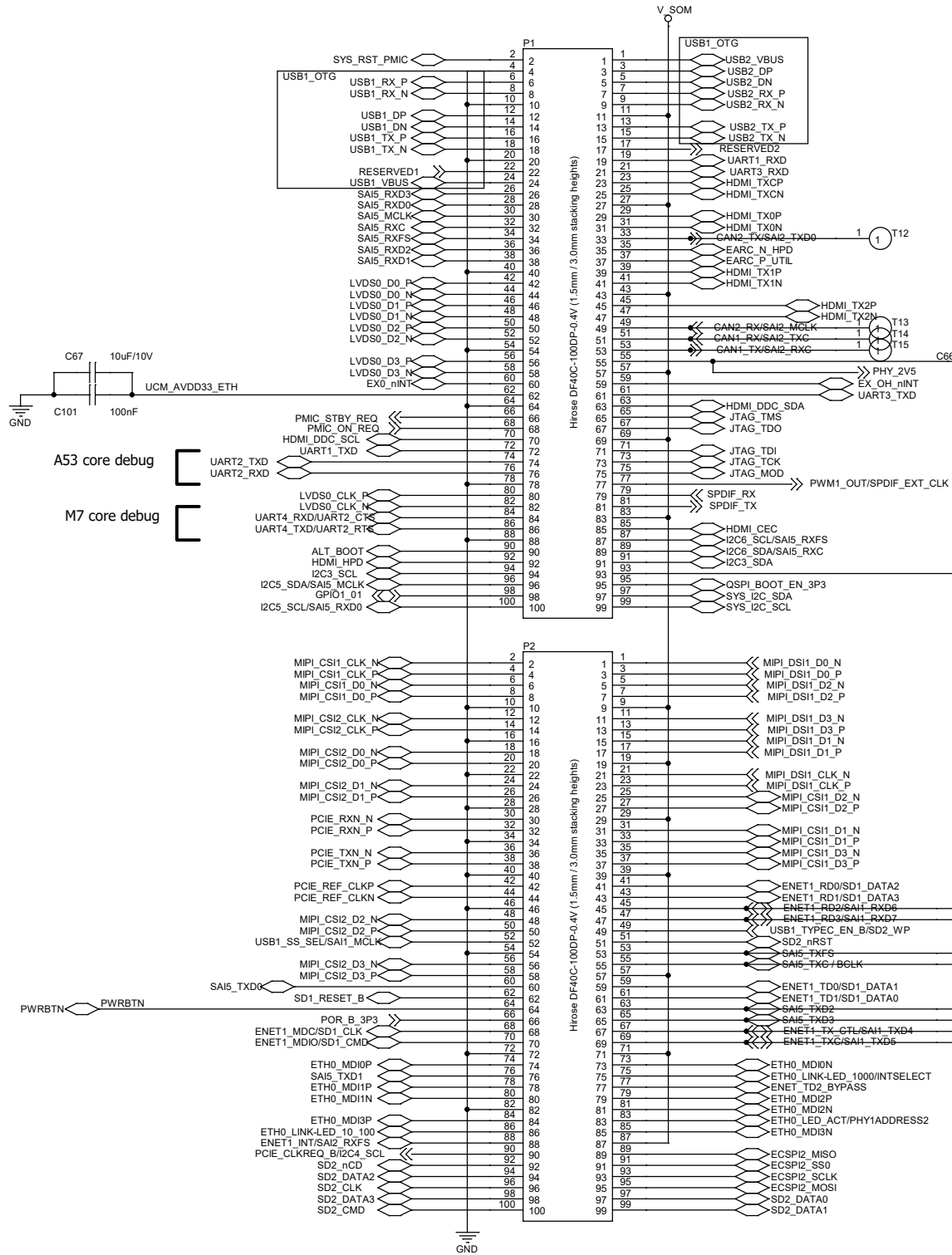
Host ALT

Host USB2.0



The type and specification of the components refer to the BOM

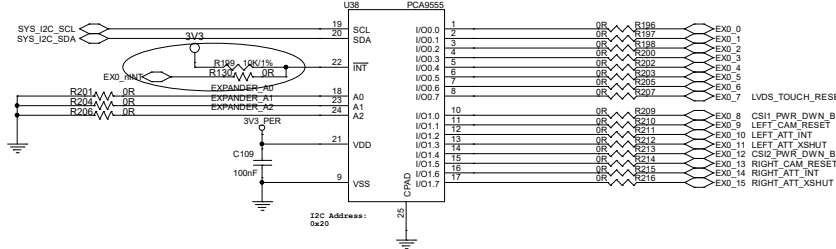
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				ECA NO	DATE
DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC	VER	PART_NUMBER	SHEET 9 OF 34	
		B			



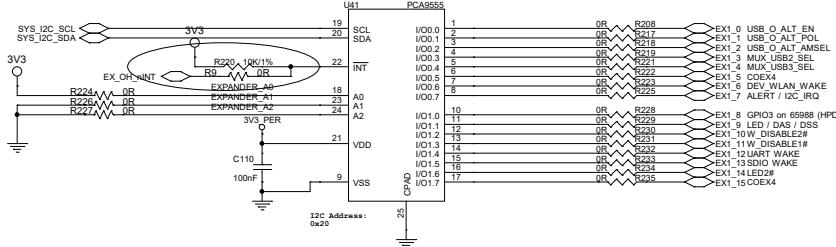
The type and specification of the components refer to the BOM

DESIGNED	XKC20201222	CM4 MODE	VER	PART_NUMBER	SHEET 9 OF 34
REVIEWED	XKC	B			

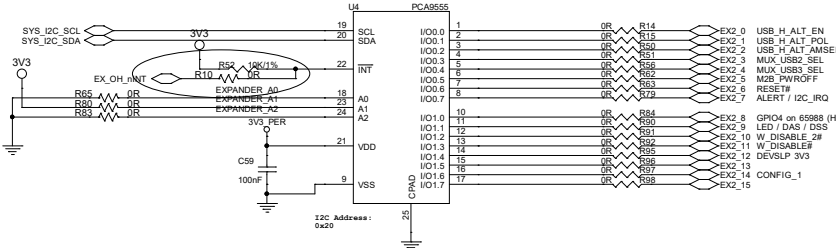
SYS_I2C to GPIO expander-EX0
Addres: 0X40



SYS_I2C to GPIO expander-EX1
Addres:0X41



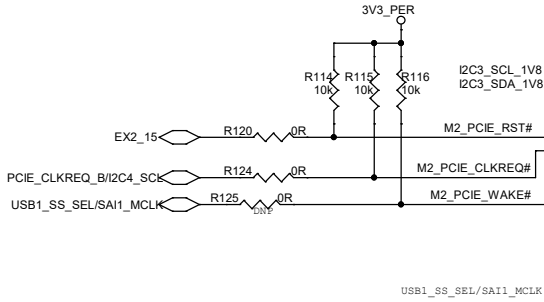
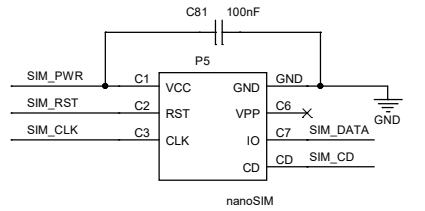
SYS_I2C to GPIO expander-EX2
Addres:0X42



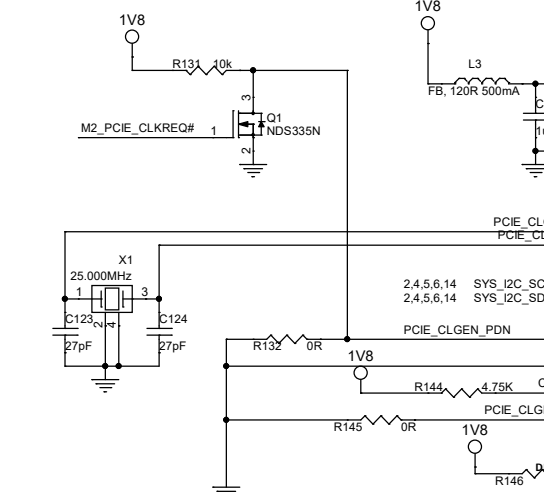
The type and specification of the components refer to the BOM

[illegible]

nanoSIM with detect



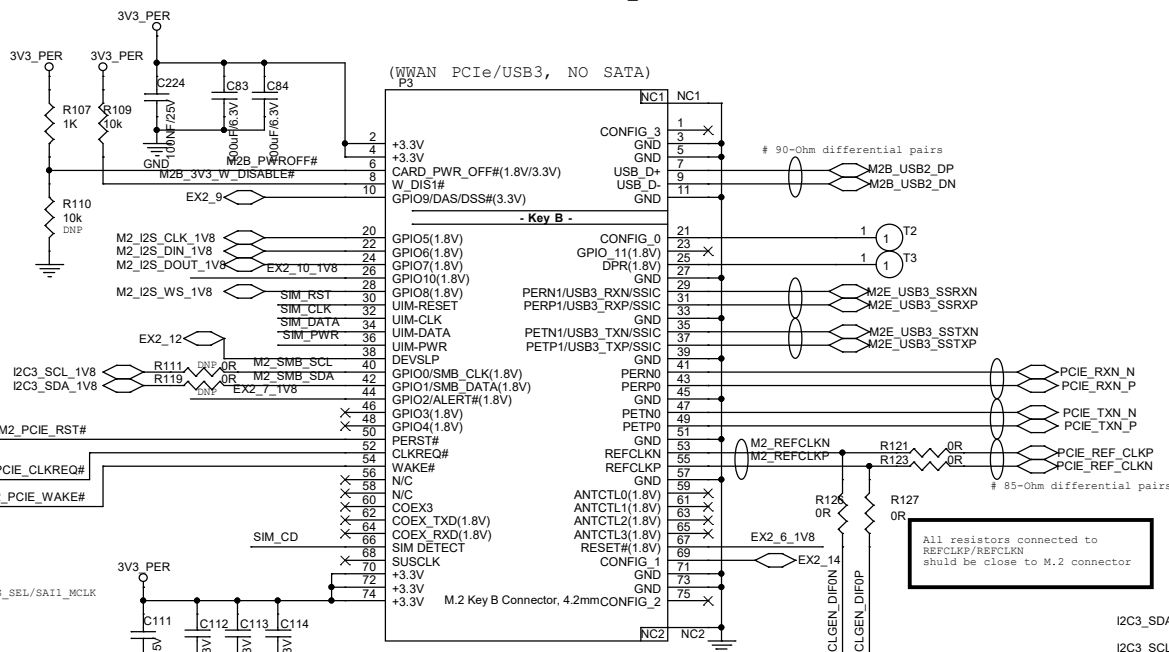
PCIE clock generator - DNP



PD# : Input notifies device to sample latched inputs and start up on first high assertion. Lowerters Power Down Mode, subsequent high assertions exit Power Down Mode. This pin has internal pull-up resistor.

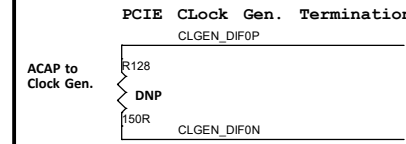
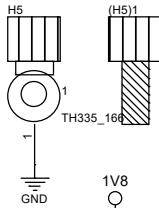
Configuration Straps
 PD# : Internal PU (see spec)
 OE0# : 0 (Enable DIF0)
 OE1# : 0 (Enable DIF1)
 SS_SEL_TRI : 0 (SpreadSpect off)

M.2 Socket - Key B

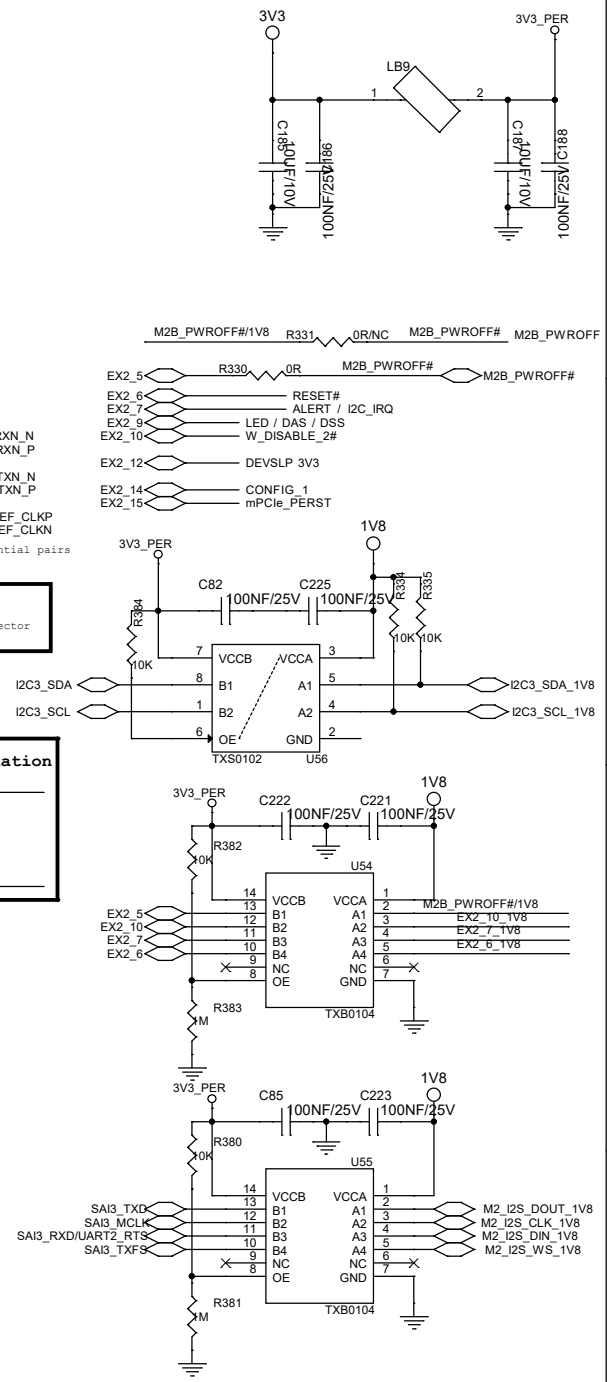


SILKSCREEN: M.2 Key-B

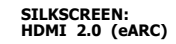
Match with H=8.5



Ref. Design Terminations to GND DNP by default

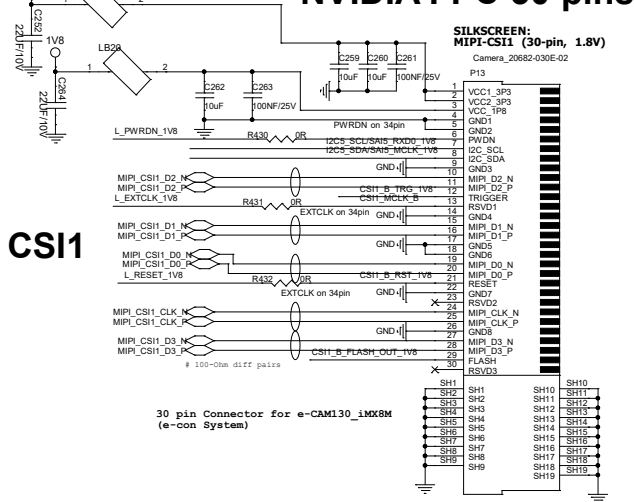


				NA			
				ECA NO	DATE		
DESIGNED	XKC20201222	CM4 MODE					
REVIEWED	XKC						
		VER	PART NUMBER			SHEET 9 OF 34	
		B					

[illegible]

				NA	
				ECA NO	DATE
DESIGNED	XKC20201222	CM4 MODE			
REVIEWED	XKC				
		VER	PART_NUMBER	SHEET 9 OF 34	
		B			

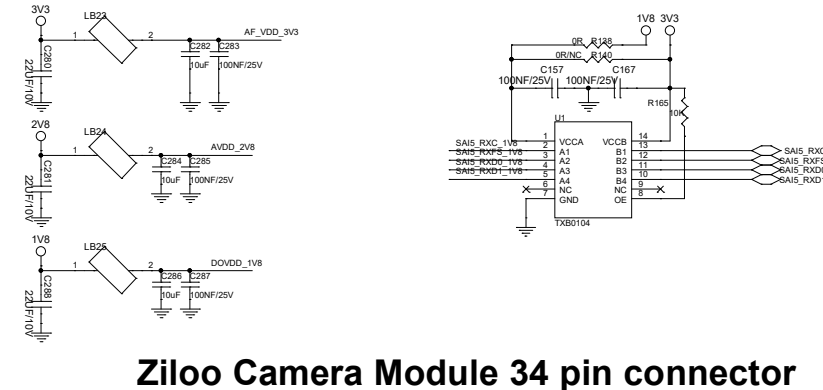
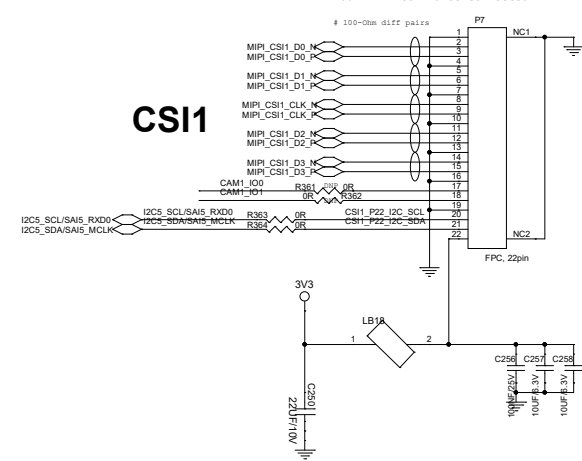
NVIDIA FPC 30 pins



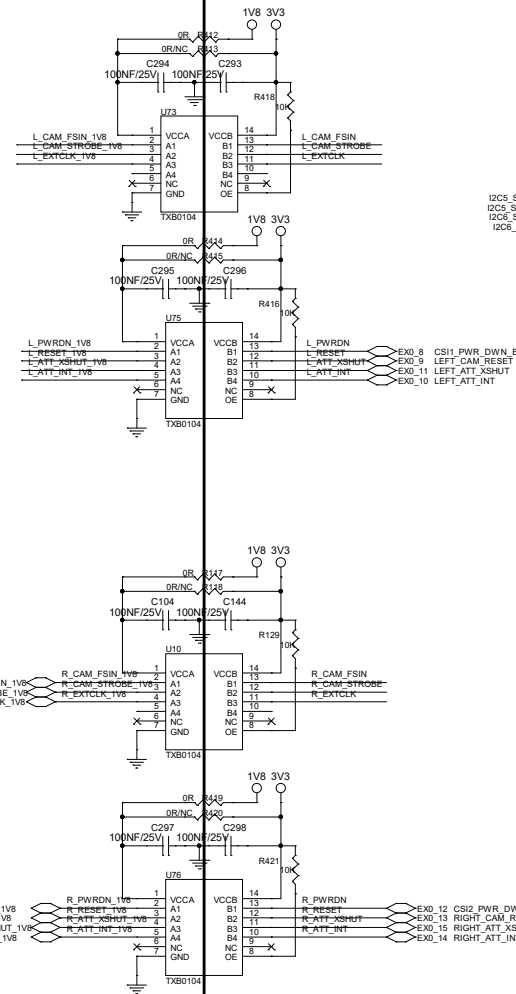
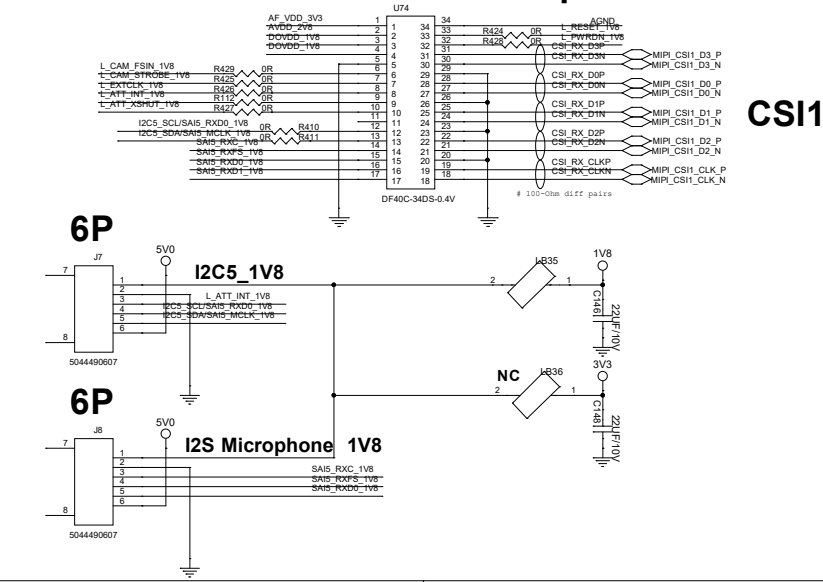
RPI FPC 22 pins

SILKSCREEN:
MIPI-CSI1 (22-pin, 3.3V)
 22-pin Connector for RPi cameras
 Four MIPI-CSI lanes connected

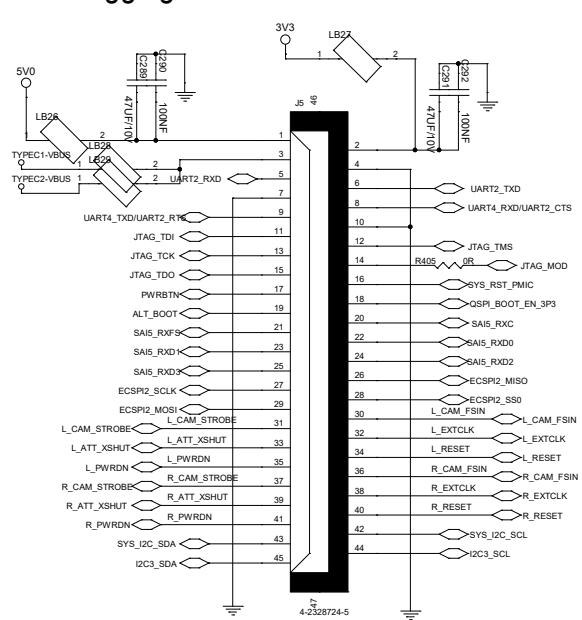
CSI1



CSI1



Debugging Breakout connector 45P





The type and specification of the components refer to the BOM							
				NA			
DESIGNED				XKC20201222		ECA NO	
						DATE	
REVIEWED				XXC		CM4 MODE	
				VER		PART NUMBER	
				B		SHEET 9 OF 34	