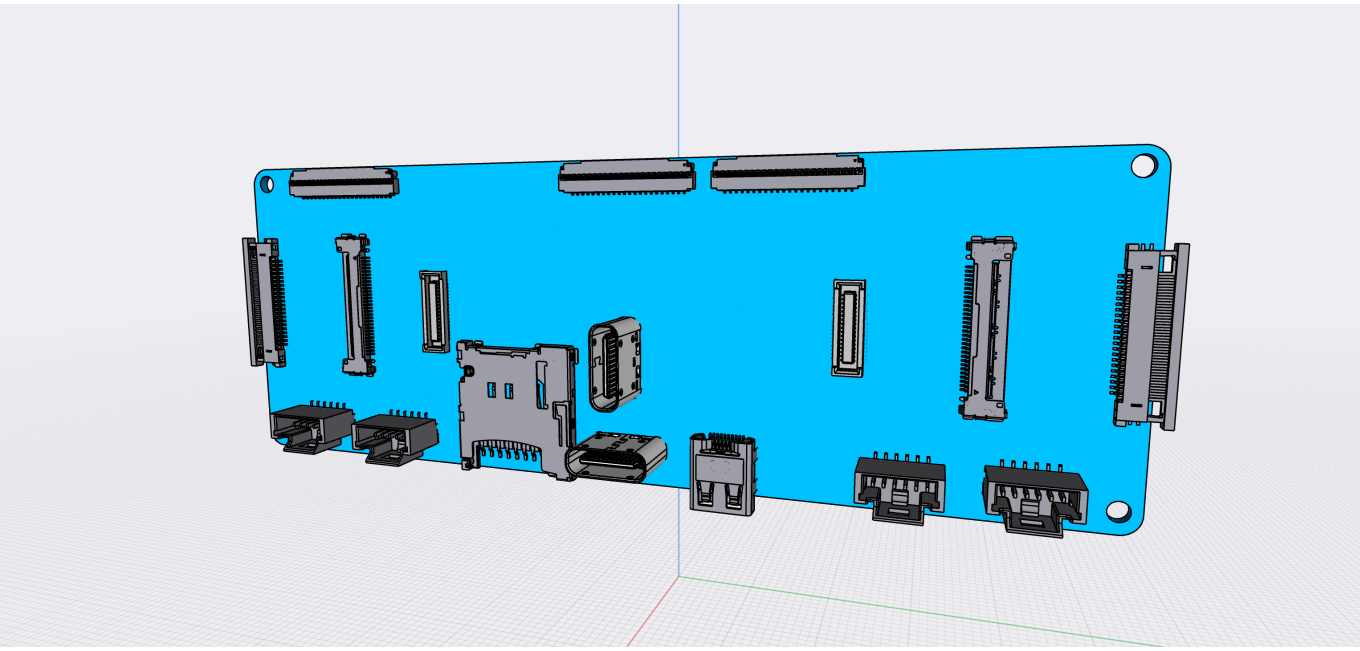
Bridge Board 909b

The 909b is a Bridge Board version made for testing and experiementation with the Ziloo attachments without attaching the SB-UCM i.MX8 board or directly attaching it. The setup enables connecting a Compulab SB-UCM-iMX8PLUS, DART-MX8M-PLUS Evaluation Kit or I-Pi SMARC IMX8M Plus development board. Not all the 909 connectors will be mounted on the 801 production bridge board that mounts the i.MX8 board.

The board provides two key features: Dual USB connectivity for Webcam, Internet, Display & Power + MIPI CSI Stereo Camera. Alt. Mode, HDMI and extra MIPI CSI connectors are not intended for the production board.

Of note in design,

- Some of the UCM-iMX8M-Plus carrier board interface pins are multifunctional. Up to 4 functions (ALT modes) are accessible through each multifunctional pin.
- All of the UCM-iMX8M-Plus digital interfaces operate at 3.3V voltage levels unless noted otherwise.
- RGMII ENET1 signals operate at 1.8V voltage level
- SD/SDIO port #2 can be configured to operate at 3.3V or 1.8V voltage levels. Voltage level is controlled by SoC pin GPIO1_IO04.



Open points

- Plan I2C addresses and which bus is used
- Stem I2C compress GPIO iMX and others, also on I2C3
- Correctly crossing RX/TX signal lines
- Can PD Controller control other chipsets or is it just I2C slave?
- Ensure all pins are connected to GPIO Expander
- Should there be Boot origin switches like EVK? (4 bits? EVK)
- Power LED & Indicator LEDs for M.2 expansions
- Adding second m.2 connector with mounting screw holder glued on

- Second stage designing a 909 Smiley Board
- Adding connectors SCCB, GPIO
- Samtec connector pin 1, and soldering isles
- Optional connectors debug uart / jtag
- Connection option for Varscite board instead of Compulab
- RTC battery connector
- Annotations and Logo on the board
- Mux X pairs
- TEST The Mux pin configurations

Core Components

- SB-UCM-iMX8PLUS System-on-Module
- 2 * Hirose DF40HC(3.0)-100DS-0.4V mated height 3.0mm
- M.2 key B connector H4.20mm Amphenol ICC 10128793001RLF
- M.2 key E connector H4.20mm Amphenol ICC 10128794001RLF
- 2 * Hirose DF40C-34DS-0.4V (Mouser
- 2 * Hirose USB-C CX80B1-24P
- 1 * microSD card slot (suggested Molex 5031821852) push-push, compact. Mouser, Molex
- 1 * TPS65988 Dual Port USB Type-C® and USB PD Controller, Power Switch, and High-Speed Multiplexer. Mouser
- 2 * HD3SS3220 10-Gbps USB 3.1 Type-C 2:1 mux with DRP Controller Mouser
- 2 * HD3SS460 4 x 6 Channels USB Type-C Alternate Mode MUX. Connected to T-USB Host. Mouser. Dock Eval Kit
- 2 * push buttons (RESET / POWER)
- 3 * PCA9555 I/O Expander
- 6 * TS5USBC410 Dual 2:1 USB 2.0 Mux/DeMux Switch. Mouser
-

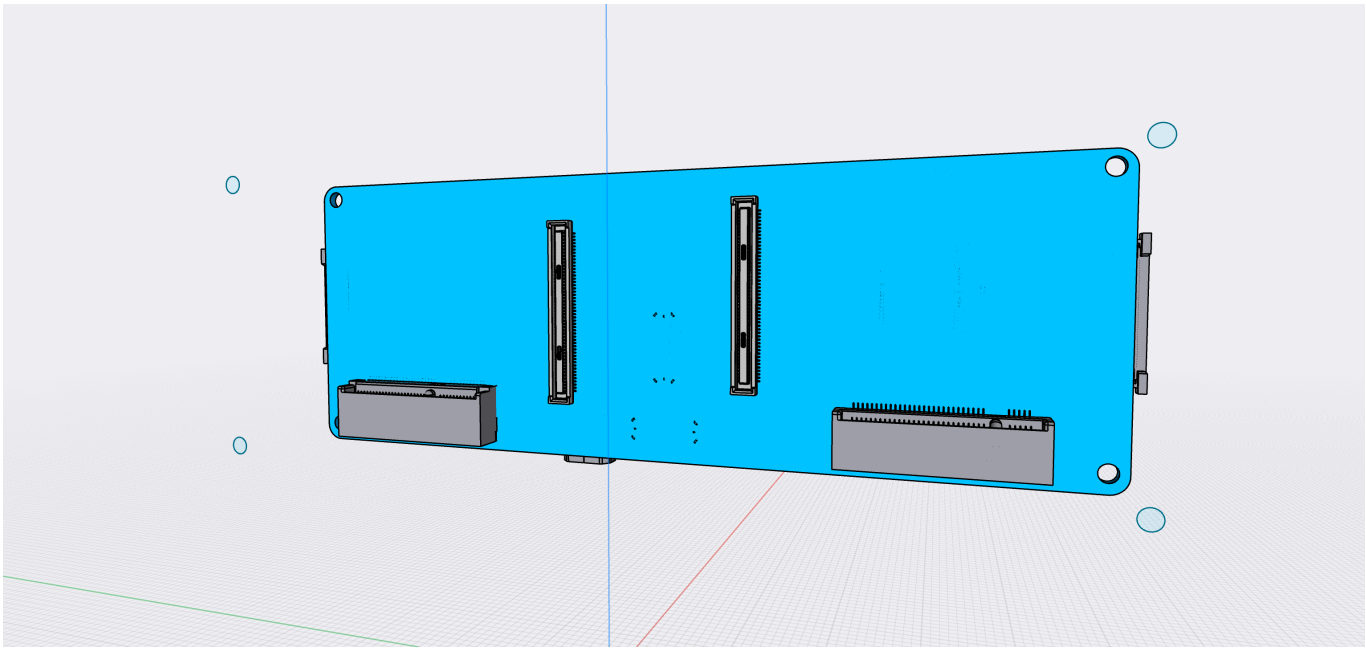
Dev. Connectors

- 1 * MicroHDMI (suggested Molex 46765-1301) Mouser Molex
- 2 * Molex 22PIN 0.5mm pitch 54548-2271
- 2 * I-PEX 30PIN 0.4mm pitch 20525-030E-02
- 3 * TE Connectivity 45PIN 0.3MM 571-4-2328724-5 FPC 3-2328724-5 $0.41 ProductDetail/Hirose-Connector/DF40C-34DS-04V51?qs=vcbW%252B4%252BSTIpg26DsEbj1iQ%3D%3D))
- 5 * 6 pin Molex 5044490607

Other Components

Connectors placed on the board are,

- 1 * 24C08 Carrier-board EEPROM. Mouser
- 1 * TSM-120-01-F-DV Samtec 2*20 pins surface mounted .100 (Smiley model) Mouser
- 2 * 6 pins header CSI breakout 200-TSM10601FSV Mouser
- 2 * 5 pins I2C SCCB 504449-0507 Mouser



Connectors for SB-UCM-iMX8PLUS, M.2 Key B, M.2 Key E. The SB-UCM-iMX8PLUS is the center of the board and receives all signals.

Power supply, CSI, I2S & I2C

The USB-C connectors can supply power, as can the 30 pins and 22 pins CSI connectors. The 34 pins connector outputs CSI, I2S, I2C, Power and control pins. The 6 pins connector ouputs I2S/I2C and Power. Voltages needed are 5V, 3V3, 2V8, 1V8. 2V8 is only needed for the camera module.

In the specific case of CSI connectors being used without an i.MX8 module attached, the CSI input connectors must supply power, if no USB connector does.

Pads on the board must be provided for attaching RTC battery.

If no connected USB plug connected provides power, the board would have to be a power source. Pads on the board must be provided for 5-20V PP_HV1/PP_HV2 directly connected to the PD Controller.

According to the UCM-IMX8PLUS Referene Guide the Supply Voltage is 3.45V to 4.4V. This fits with charging/discharging of a LiPO battery which will be supported in the future. While 5V is relevant for power supply via USB, the board has no need

Handling USB Connector (PD Controller)

The two USB ports may power the board. The powering is negotiated and handled by by TPS65988 (in future TPS65994AE). They also deliver data lanes which are multiplexed between the two USB busses on the i.MX8 module, m.2 connectors and T-USB alt connectors. This allows further development of alt mode connectivity.

Power regulators receive power from USB connectors and supply the board with power.

If one USB port delivers power to the board, the other one can consume power.

See I/O expanders for control pins connected to PD Controller.

Power supply TI chipset

Dual Port USB Type-C® and USB PD Controller with Integrated Source and Sink Power Path Supporting USB3 and Alternate Mode

The TPS65988 is a highly integrated stand-alone Dual Port USB Type-C and Power Delivery (PD) controller providing cable plug and orientation detection for a single USB Type-C connector. Upon cable detection, the TPS65988 communicates on the CC wire using the USB PD protocol. When cable detection and USB PD negotiation are complete, the TPS65988 enables the appropriate power path and configures alternate mode settings for external multiplexers. The TPS65988 integrates fully managed power paths with robust protection for a complete USB-C PD solution. The TPS65988 also enables the appropriate power path and configures alternate mode settings for external multiplexers.

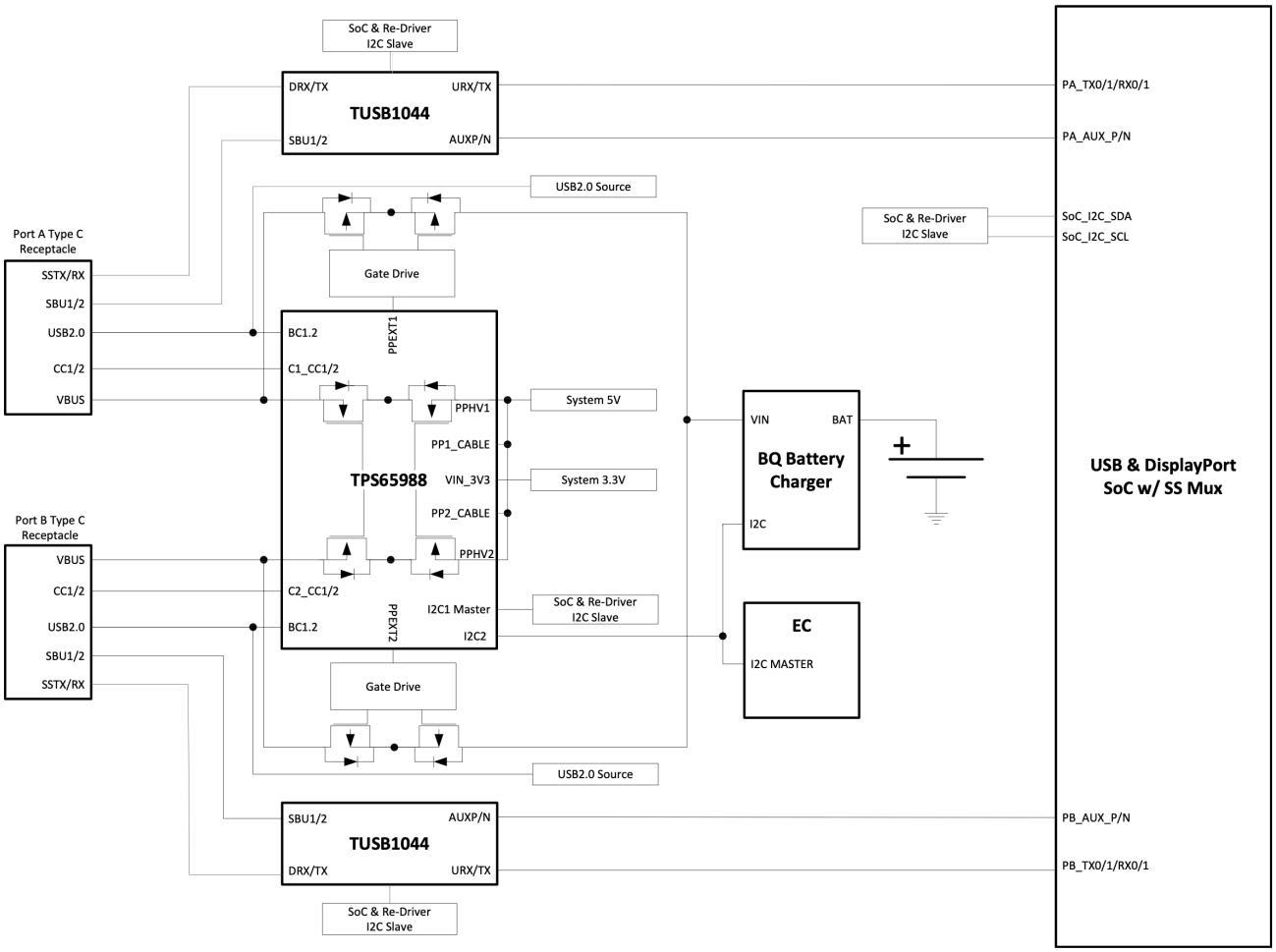


Figure 9-10. TPS65988 and SoC & Redriver I2C

Further information is found in the TPS65988 datasheet including reference implementation advice. The documents also include layout diagrams for the reference board. See 11.3 Stack-Up and Design Rules for advice on using 8-layer stacku-up PCB.

A minimal version of this setup should be placed on the 909 to handle power. I.E. No TUSB1044

The I2C Port 1 is connected to the SYS I2C. I2C Port 2 is for I2C3 stem. The I2C Port 3 is for Peripherals which so far are not identified. The I2C Port 1 & 2 interrupts are connected to I/O Expander Zero. (EX0.3 EX0.4)

The 45 pin debug connector and T-USB alt connectors can be used to test the chipset and USB devices attached.

CSI connectors

The CSI connectors data lanes are connected directly together for each side. It is only possible to connect a left and a right camera module at a time.

30 pin CSI connectors are intended to be used without a daughter board and instead a separate i.MX8 development board is used. The 22 pin connectors are connected directly to the equivalent lines on the 30 pins, and a likewise meant for an external development board or for testing alternate camera modules. The CSI lanes on 34 pins connector is connected directly to the equivalent lines on the 30 pins. This assumes that a camera is connected to either a 34 pins connector or a 22 pins connector, not both. If a i.MX8 daughter board is used rather than development board the CSI lines from the daughter board must be connected to the 34 pin camera module connectors. i.MX8 CSI1 is used for left module, CSI2 is used for right module.

The two 34 pin CSI connectors are wired to run in sync via the STROBE pin.

If power isn't connected over the USB-C plugs, the camera modules should be powered over the MIPI CSI connectors. In this case it should be possible to use either the 22 pin connectors or the 30 pin connectors for inputting the signal and power. This means that the 22 pin connectors can be used to input or output MIPI CSI lanes.

I2C / I2S connectors

The I2C/I2S connectors sends the power from USB-C connectors away from the board as regulated 5V and 3V3.

Breaking out individual I2C/I2S busses adds common power. This is used for SCCB, Sound out and Sound in.

No.	Pin	Description
1	1V8 / 3V3	Power at signal level 1.8V / 3.3V
2	GND	GND
3	INT / BCLK	I2C Interrupt / I2S BCLK
7	SCL / LRCLK	I2C SCL
8	SDA	I2C SDA
4	5V	Board power 5V

Max. Current per pin 1.0A

For the two camera modules the SCCB signals are broken out with a six pin connector, in the corner, next to the CSI connectors. INT is connected to ATT_INT. The signal level for SCCB is 1.8V.

The microphone signals from the 34 pins connectors are broken out in the 6 pins connector next to the left camera connector. The signal level for Microphone I2S connector is 1.8V.

Next to the right camera connector the SAI3 OUT SPEAKER is broken out as a 6 pins connector.

Microphone I2S (SAI5)

The microphone I2S mapping is done by using AL2 mode for the SAI3 pads to get SAI5 signals. Multiplexed Signal Pins. The microphones on the 6 pins and 34 pins connector use SAI5_RX_DATA0.

Misc pin	SoM pin	i.MX pad	Functionality	ALT
11	P1.26	SAI3_TXD	SAI5_RX_DATA3	ALT2
17	P1.28	SAI3_RXD	SAI5_RX_DATA0	ALT2
15	P1.30	SAI3_MCLK	SAI5_MCLK	ALT2
19	P1.32	SAI3_RXC	SAI5_RXC	ALT2
23	P1.34	SAI3_RXFS	SAI5_RX_SYNC	ALT2
13	P1.36	SAI3_TXC	SAI5_RX_DATA2	ALT2
21	P1.38	SAI3_TXFS	SAI5_RX_DATA1	ALT2

Speaker I2S (SAI2)

CAN1 and CAN2 are mapped as SAI2 and brought out as speaker 6 pins connector. Multiplexed Signal Pins.

Misc pin	SoM pin	i.MX pad	Functionality	ALT
8	P1.33	SAI2_TXD0 ~~CAN2_TX~~	SAI2_TXD0	
10	P1.49	SAI2_MCLK ~~CAN2_RX~~	SAI2_MCLK	
12	P1.51	SAI2_TXC ~~CAN1_RX~~	SAI2_TXC	
14	P1.53	SAI2_RXC ~~CAN1_TX~~	SAI2_RXC	

I/O Expanders

The development board uses a single Expander. The 909 and 801 uses 4x PCA9555 to control more states The system expander input triggers interrupt via EX0_nINT (GPIO4_IO19).

This first expander, which is also on the dev. board maps,

Expander	Connected to
EX0.0	mPCIe_PERST on M2 Key B
EX0.1	mPCIe_PERST on M2 Key E
EX0.2	
EX0.3	PD_CTL_INT_1
EX0.4	PD_CTL_INT_2

Expander	Connected to
EX0.5	PD_CTL_RESET
EX0.6	LVDS_TOUCH_INT
EX0.7	LVDS_TOUCH_RESET
EX0.8	CSI1_PWR_DWN_B
EX0.9	LEFT_CAM_RESET
EX0.10	LEFT_ATT_INT
EX0.11	LEFT_ATT_XSHUT
EX0.12	CSI2_PWR_DWN_B
EX0.13	RIGHT_CAM_RESET
EX0.14	RIGHT_ATT_INT
EX0.15	RIGHT_ATT_XSHUT

SYS I2C addresses

Address	Chipset	Description
0x20	PCA9555	16 bit expander EX0
0x21	PCA9555	16 bit expander EX1/USB1
0x22	PCA9555	16 bit expander EX2/USB2
0x54..0x57	EEPROM	
0x47 0x67	HD3SS220	USB1 MUX M.2 / T-USB
?	HD3SS220	USB2 MUX M.2 / T-USB
0x68 0x6A	PI6CG18200	PCIe clock generator
0x70 0x71	TPS65988	PD Controller Port 1 / SYS
0x7E 0x7F	TPS65988	PD Controller Port 2 / i.MX I2C3

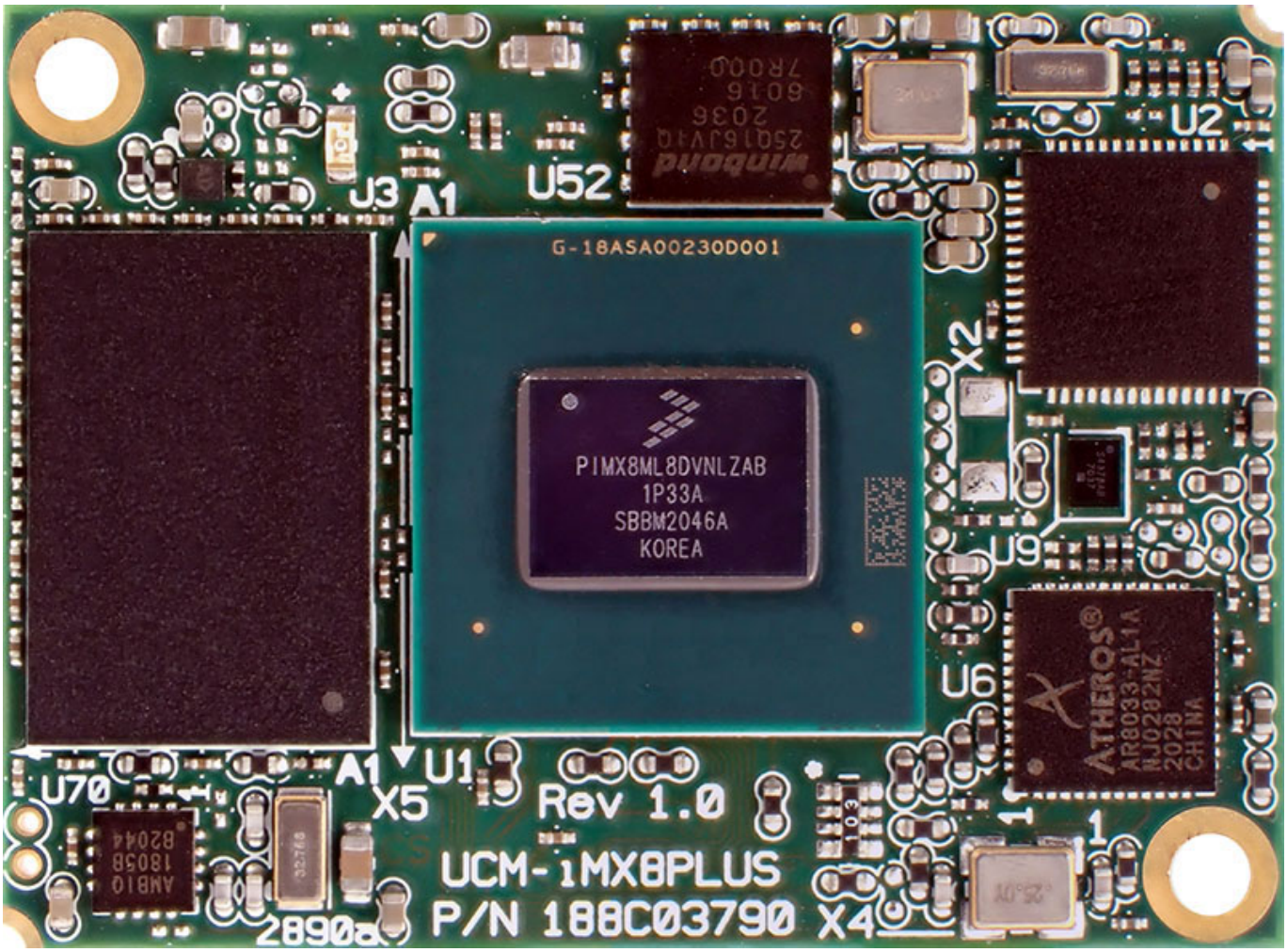
Connecting the SB-UCM-iMX8PLUS SoM

The daughter board clicks into the two Hirose 100pin board-to-board connectors.

For further details see Product Page.

The CSI1 & CSI2 are wired from the 100pin connectors to relevant CSI connectors. The CSI1 lanes are connected to Left CSI. The CSI2 lanes are connected to Right CSI. The USB1 and USB2 data will be connected to multiplexers The 45 pins Debug connector will break out many additional signal lines

- 2 * Hirose 100 pin connectors are used to connect the SoM daughter board



Booting

The board can boot from eMMC / SD or USB.

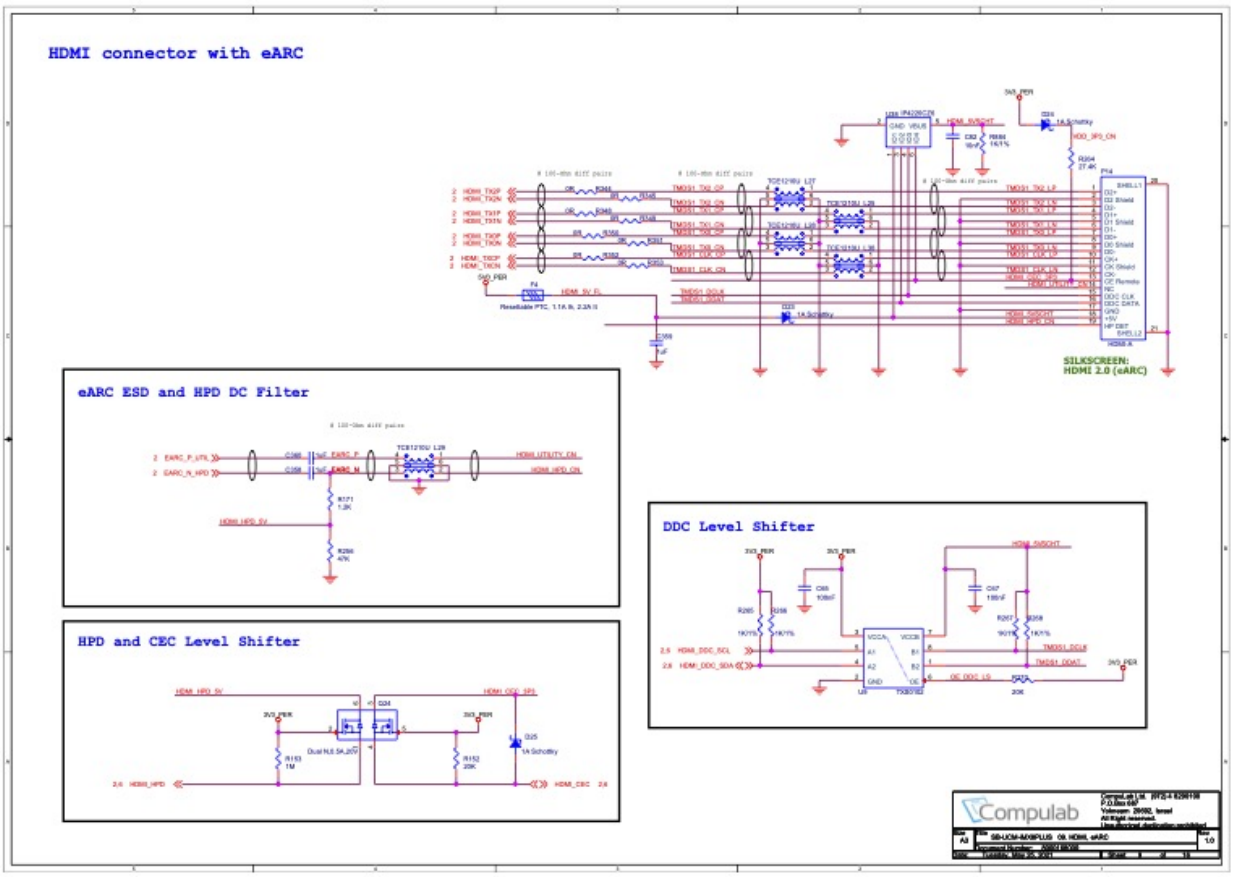
The board has push buttons for POWER and RESET.

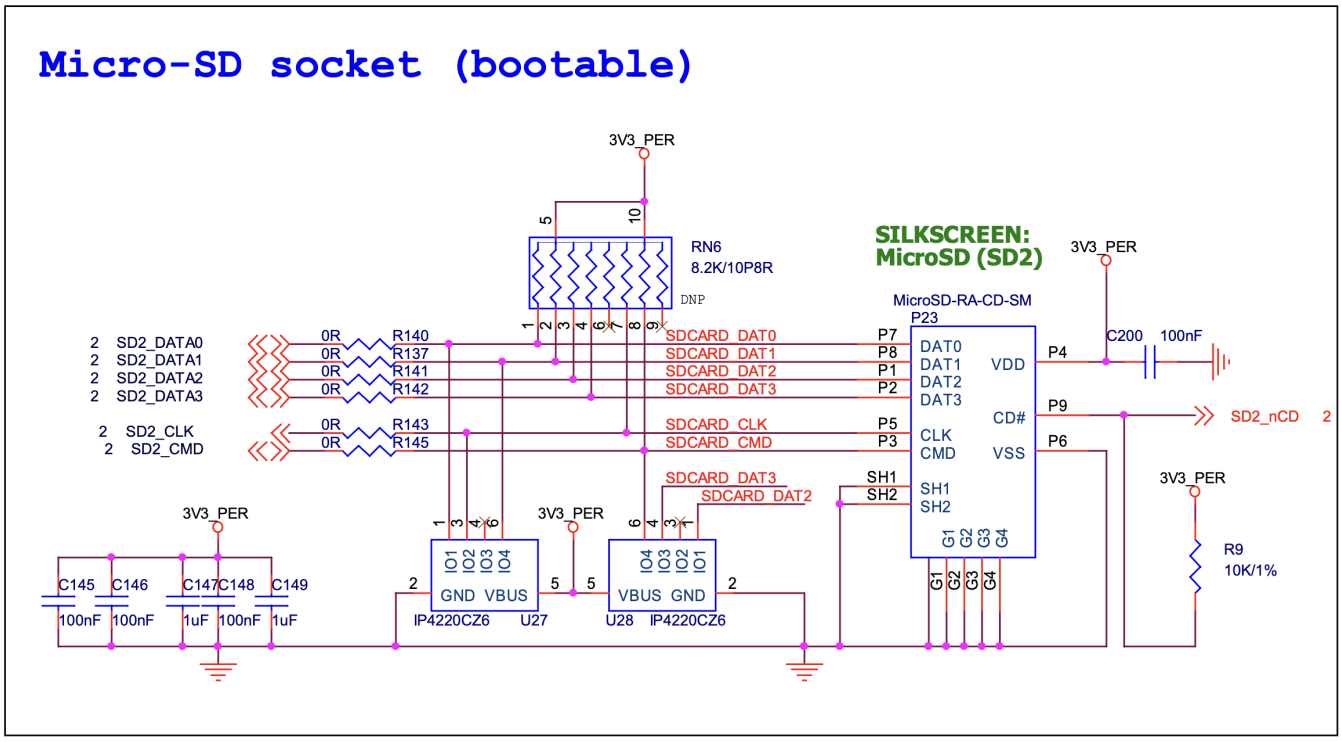
The board has a power LED

MicroSD, MicroHDMI, M.2 key B & Debug Breakout

The MicroHDMI connector is connected to the HTMI_TX*, HDMI_DDC_*, HDMI_CEC, HDMI_HPD pins from the i.MX8 module.

The MicroSD connector is connected to SD2_DATA*, SD2_CLK, SD2_CMD, SD2_nCD on the i.MX8 module.





M.2 Key B

See end of document for pinouts and EXPANSION document for more information.

Note that some pins are connected to I/O Expander 2 meant for USB2 and Key B.

Debugging Breakout connector

See end of this document for pinouts.

T-USB Data and M.2 Key E Expansion

Data is routed primarily over the two USB-C connectors, but it is also available over Breakout connectors as well as the two M.2 Expansion connectors.

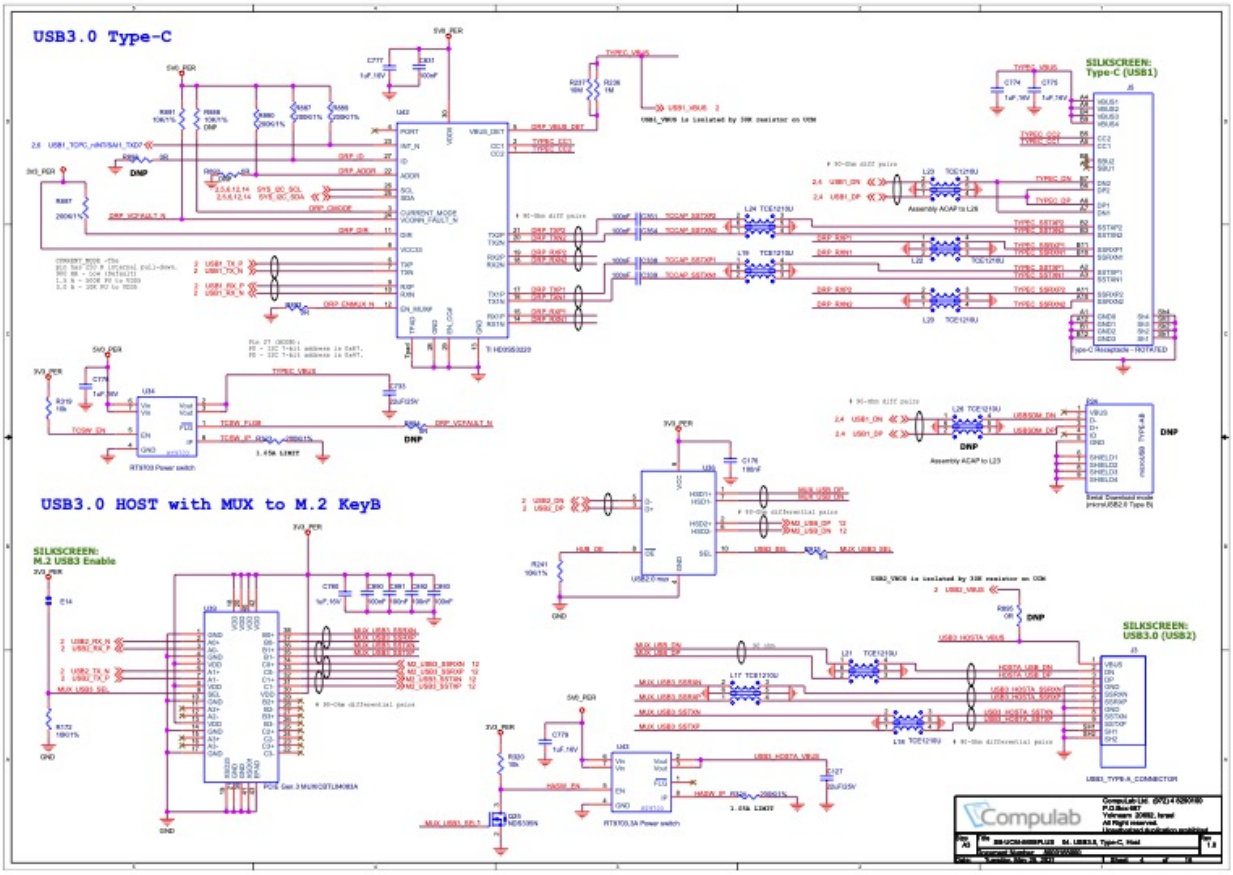
T-USB connector 3.0 data mapping

Two USB-C connectors are arranged in a T shape and the normal way to use it is with a combined connector attached. This means that the wires will normally be connected in a particular orientation. The system takes advantage of this by detecting when both USBs are connected in the normal arrangement.

The USB-C signal lines from the T-USB connector is managed by the Multiplexing chipsets around the PD Controller. The USB-C signal lines for the OTG connector in T-USB come from USB1(OTG cabable 2.0 & 3.0). The SBU1/SBU2 are connected to AUX-/AUX+ pins on the T-USB OTG alt connector. The USB 3.0 superspeed data pairs and SBU1/SBU2 are passed from USB-C connectors to HD3SS460.

The Host USB-C connector is similarily connected. The HD3SS460 chips are controlled over I2C by the MCU using SYS I2C. The HD3SS3220 chips are controlled over I2C by the MCU using SYS I2C.

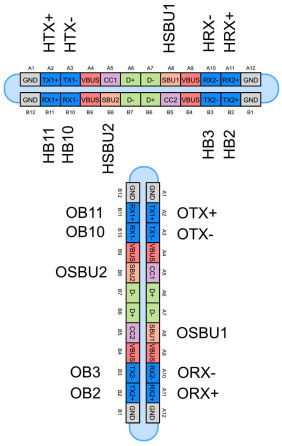
One side of the RX/TX pins are carried to the T-USB alt connector, and not connected to USB1 signals. (Should the side be muxed?)



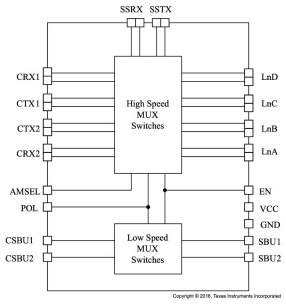
The USB connectors are named H (Host) and O (OTG). Host is the top of the T, OTG is the vertical base. To specify a specific pin H or O is prefixed I.E. OTX1+, HSBU2.

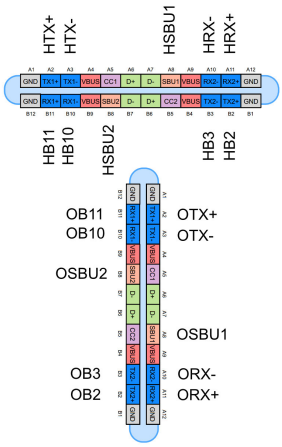
Where possible data pins are not combined but carried through individually.

The GND/VBUS pins are connected to the power charging circuit as normal. The system should accept charging power from either connector.



T-USB plugged in combo





DisplayPort over Host USB

No.	Pin	Usage	OTG connect to..	Host connect to..
1	A1	GND		
2	A2	TX1+		HD3SS460
3	A3	TX1-		HD3SS460
4	A4	VBUS		
5	A5	CC1	TPS65988	TPS65988
6	A6	D+	65988 & MCU	65988 & MCU
7	A7	D-	65988 & MCU	65988 & MCU
8	A8	SBU1		HD3SS460
9	A9	VBUS	65988 & Regs	65988 & Regs
10	A10	RX2-		HD3SS460
11	A11	RX2+		HD3SS460
12	A12	GND		
13	B1	GND		
14	B2	TX2+		HD3SS460
15	B3	TX2-		HD3SS460
16	B4	VBUS	65988 & Regs	65988 & Regs

No.	Pin	Usage	OTG connect to..	Host connect to..
17	B5	CC2	TPS65988	TPS65988
18	B6	X+	65988 & MCU	65988 & MCU
19	B7	X-	65988 & MCU	65988 & MCU
20	B8	SBU2		HD3SS460
21	B9	VBUS	65988 & Regs	65988 & Regs
22	B10	RX1-		HD3SS460
23	B11	RX1+		HD3SS460
24	B12	GND		

The USB Type-C connector has 24 pins. Figures 1 and 2, respectively, show the pins for the USB Type-C receptacle and plug.

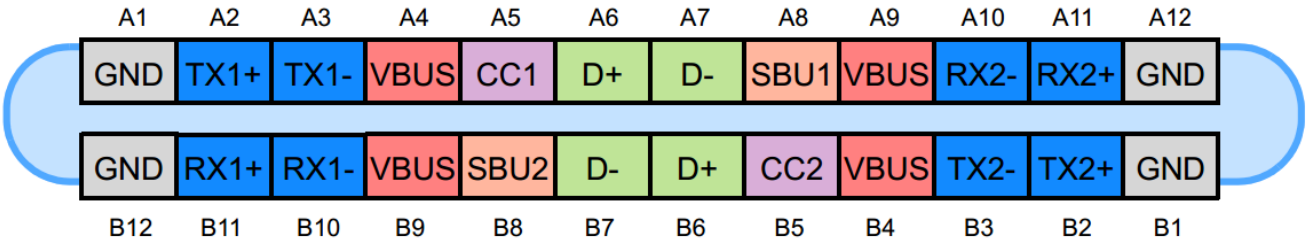


Figure 1. The USB Type-C receptacle. Image courtesy of Microchip.

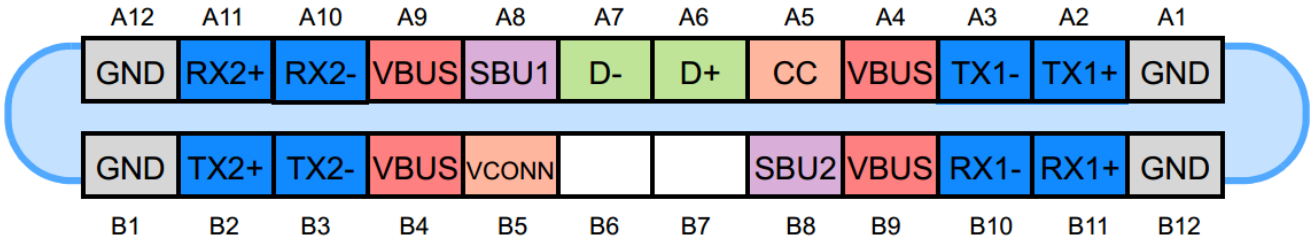


Figure 1. The USB Type-C plug. Image courtesy of Microchip.

For later revision

Only **one side** of the connectors are connected to the matching USB connector that leads to the Dev Board.

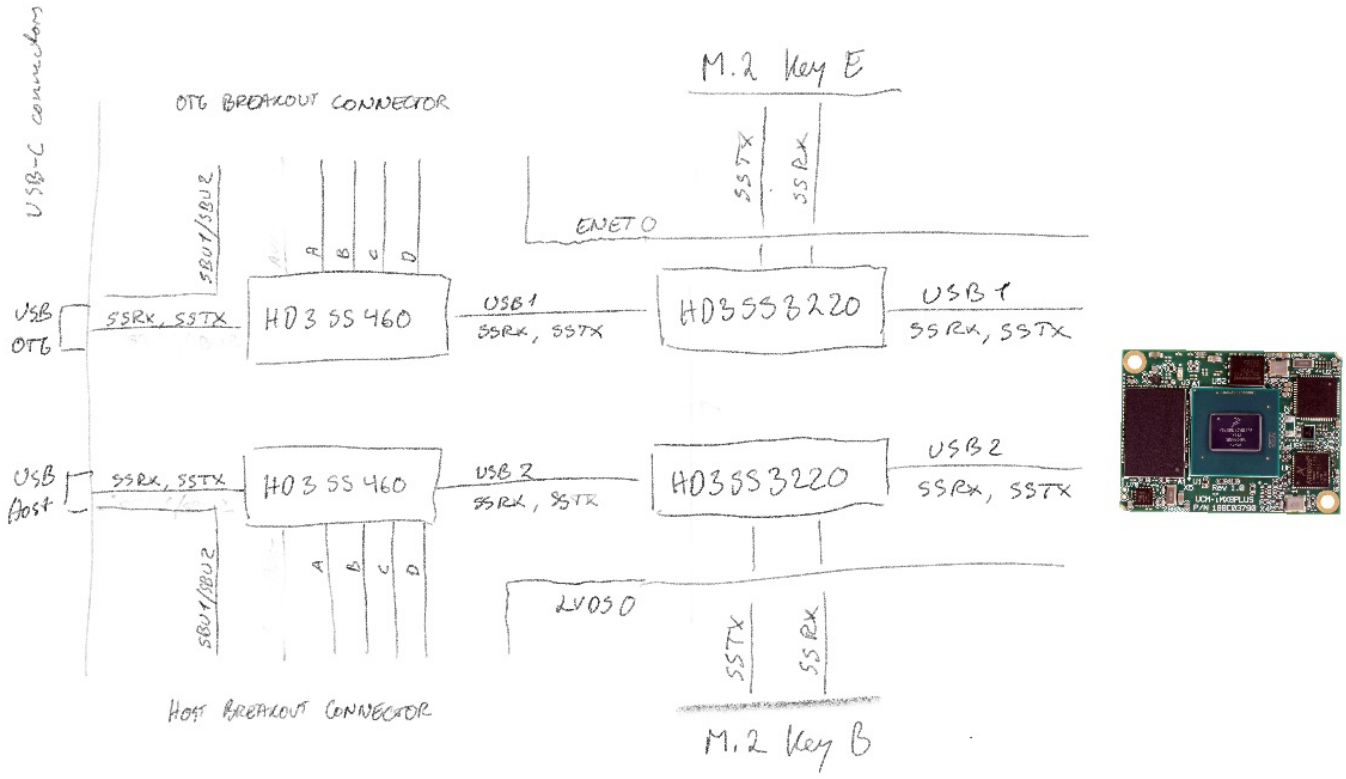
The following pins are connected to the extras connector: TX2+, TX2-, SBU1, SBU2, RX-, RX1+, DX+, DX-

The following pins are treated as normally USB-C connection pins: A1-A7, A9-A12, B5.

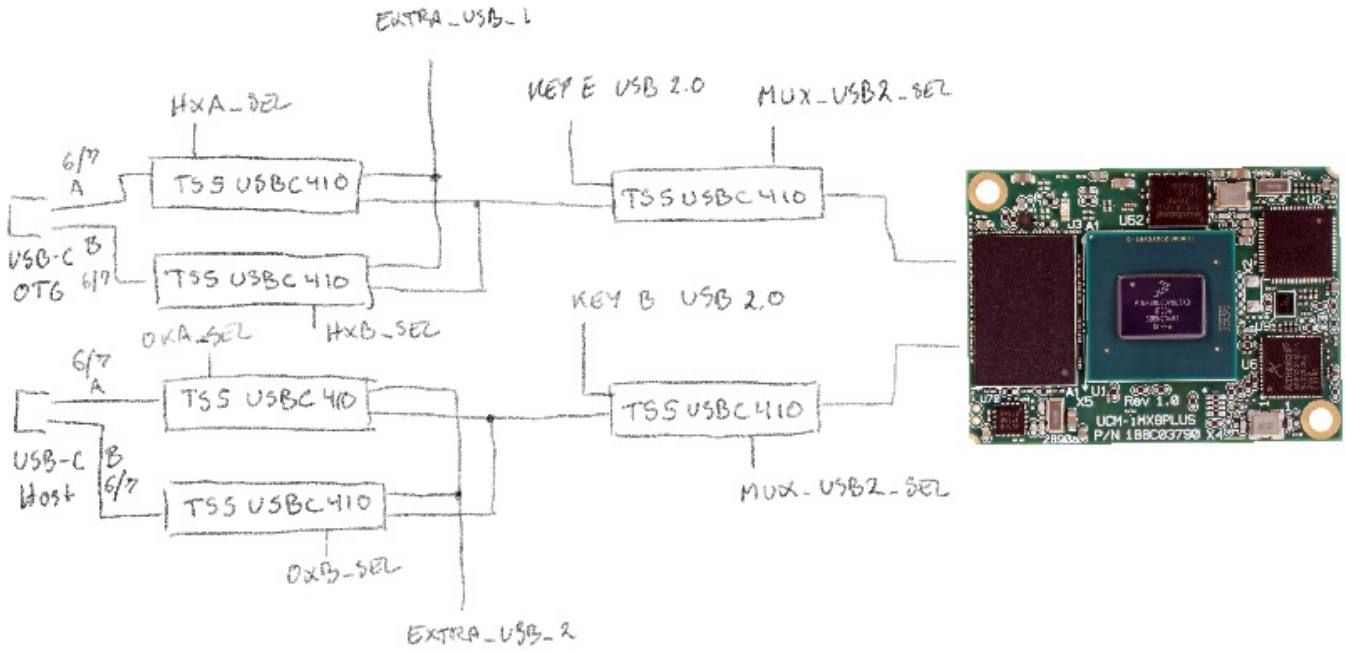
Multiplexing USB

The i.MX8 has two USB busses. USB1(supports OTG) and USB2(Host mode only).

The USB 3.0 superspeed USB1/USB2 from the SoM are multiplexed using HD3SS3220 and controlled by MUX_USB3_SEL pins. The USB-C connector Alt. mode is managed by HD3SS460.



The USB 2.0 USB1/USB2 from the SoM are multiplexed using TS5USBC41 and controlled by MUX_USB2_SEL pins. The USB-C connector USB 2.0 signals(A/B 6/7) are managed separately and multiplexed using TS5USBC41. This allows routing an Extra USB 2.0 signal selectively via the Debug Breakout connector.



Key E

See end of document for pinouts and EXPANSION document for more information.

I2C EEPROM

Add an EEPROM like 24C08 present on the UCM carrier board.

Wiring and Connecting

The board can be used in different ways

1. Adding a daughterboard, two OV2735 camera modules and connecting a USB cable with power.
2. Adding a daughterboard, two RPi camera modules and connecting a USB cable with power.
3. Use the board to connect two OV2735 camera modules to Compulab SB-UCM-iMX8PLUS
4. Use the board to connect two OV2735 camera modules to I-Pi SMARC IMX8M Plus

Signal voltage level

- 201 Camera Module uses 1.8V signals
- IMX477 sensor uses 1.8V for signals
- Does RPi cam module level shift the signals?
- UMC iMX8PLUS module uses 3.3V for signals by default
- UMC iMX8PLUS module RGMII ENET1 signals operate at 1.8V voltage level
- iMX8M plus is documented to use VDD_MIPI_1P8 power group for CSI1 & CSI2
- iMX8M plis is documented to use VDD_HDMI_1P8 power group for HDMI
- NVCC_SAI1_SAI5 power group?
- What will the I2C 5+6 power group be?
- USB 1 & 2 uses VDD_USB_3P3 power group
- Signal voltage PD Controller?

Required distances/location

- Camera module distance 70mm
- USB-C connectors cannot be moved
- Board size can only be increased to save cost
- Holes in the corners should be the regular sort for mounting.
-

I/O Expanders

The development board uses a single Expander. The 909 and 801 uses 4x PCA9555 to control more states The system expander input triggers interrupt via EX0_nINT (GPIO4_IO19).

This first expander, which is also on the dev. board maps,

Expander	Connected to
EX0.0	mPCIe_PERST on M2 Key B
EX0.1	mPCIe_PERST on M2 Key E
EX0.2	
EX0.3	PD_CTL_INT_1
EX0.4	PD_CTL_INT_2
EX0.5	PD_CTL_RESET
EX0.6	LVDS_TOUCH_INT

Expander	Connected to
EX0.7	LVDS_TOUCH_RESET
EX0.8	CSI1_PWR_DWN_B
EX0.9	LEFT_CAM_RESET
EX0.10	LEFT_ATT_INT
EX0.11	LEFT_ATT_XSHUT
EX0.12	CSI2_PWR_DWN_B
EX0.13	RIGHT_CAM_RESET
EX0.14	RIGHT_ATT_INT
EX0.15	RIGHT_ATT_XSHUT

I/O Expander like Compulab Carrier Board

The development board uses a single Expander. The 909 and 801 uses 3x PCA9555 to control more states.

The EX1 expander input triggers interrupt via EX_O_nINT (GPIO1_IO1). The pins relate to USB1 Host and M.2 Key E.

The EX1 expander allows controlling T-USB maps,

Expander	Connected to
EX1.0	USB_O_ALT_EN
EX1.1	USB_O_ALT_POL
EX1.2	USB_O_ALT_AMSEL
EX1.3	MUX_USB2_SEL
EX1.4	MUX_USB3_SEL
EX1.5	COEX4
EX1.6	DEV_WLAN_WAKE
EX1.7	ALERT / I2C_IRQ
EX1.8	GPIO3 on 65988 (HPD1)
EX1.9	LED / DAS / DSS
EX1.10	W_DISABLE2#
EX1.11	W_DISABLE1#
EX1.12	UART WAKE
EX1.13	SDIO WAKE

Expander	Connected to
EX1.14	LED2#
EX1.15	

The development board uses a single Expander. The 909 and 801 uses 3x PCA9555 to control more states.

The EX2 expander input triggers interrupt via EX_H_nINT (GPIO1_IO0). The pins relate to USB2 Host and M.2 Key B.

The EX2 expander allows controlling T-USB maps,

Expander	Connected to
EX2.0	USB_H_ALT_EN
EX2.1	USB_H_ALT_POL
EX2.2	USB_H_ALT_AMSEL
EX2.3	MUX_USB2_SEL
EX2.4	MUX_USB3_SEL
EX2.5	M2B_PWROFF
EX2.6	RESET#
EX2.7	ALERT / I2C_IRQ
EX2.8	GPIO4 on 65988 (HPD2)
EX2.9	LED / DAS / DSS
EX2.10	W_DISABLE_2#
EX2.11	W_DISABLE#
EX2.12	DEVSLP 3V3
EX2.13	
EX2.14	CONFIG_1
EX2.15	

SYS I2C addresses

Address	Chipset	Description
0x20	PCA9555	16 bit expander EX0
0x21	PCA9555	16 bit expander EX1/USB1
0x22	PCA9555	16 bit expander EX2/USB2
0x54..0x57	EEPROM	

Address	Chipset	Description
0x47 0x67	HD3SS220	USB1 MUX M.2 / T-USB
?	HD3SS220	USB2 MUX M.2 / T-USB
0x68 0x6A	PI6CG18200	PCIe clock generator
0x70 0x71	TPS65988	PD Controller Port 1 / SYS
0x7E 0x7F	TPS65988	PD Controller Port 2 / i.MX I2C3

I2S (SAI5) 4 channel microphone input mapping

One lane goes to the 34 pins camera connectors

The full 4 lanes are available on the debug connector and M.2 Key B.

8.1 Carrier Board Design Guidelines

APPLICATION NOTES from UCM-iMX8M-Plus Reference guide.

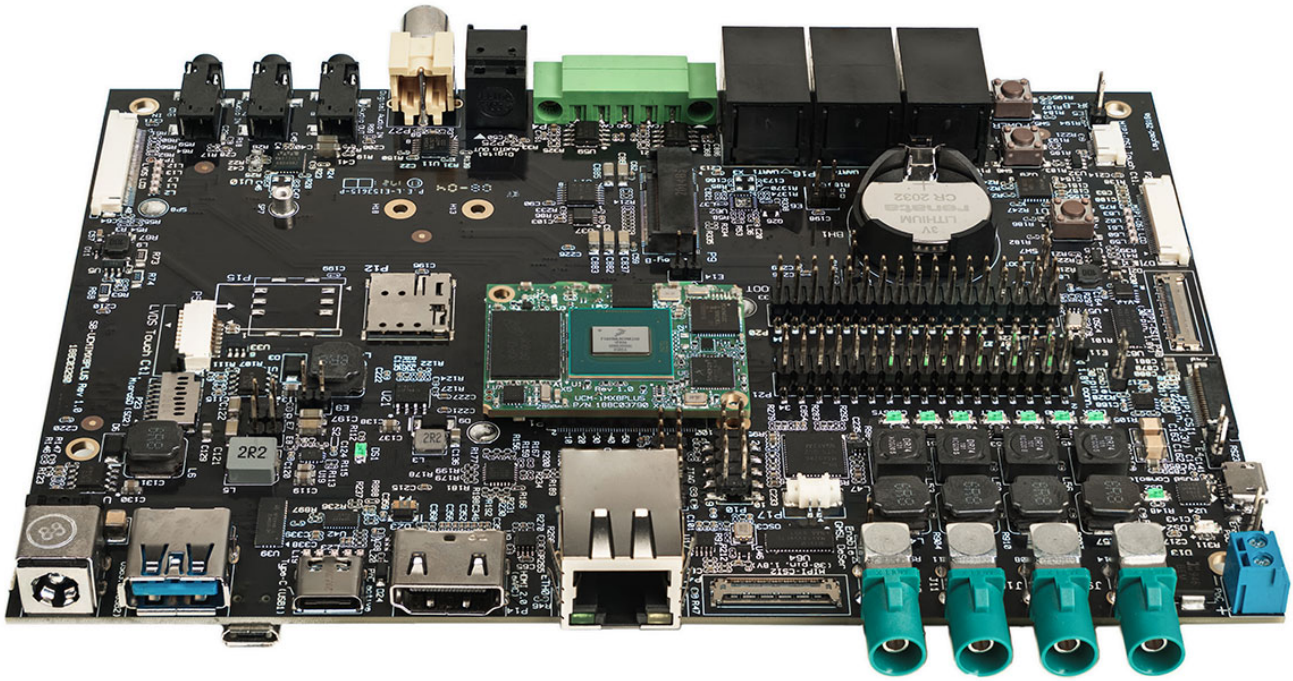
- Ensure that all V_SOM and GND power pins are connected.
- Major power rails - V_SOM and GND must be implemented by planes, rather than traces. Using at least two planes is essential to ensure the system signal quality because the planes provide a current return path for all interface signals.
- It is recommended to put several 10/100uF capacitors between V_SOM and GND near the mating connectors.
- Except for a power connection, no other connection is mandatory for UCM-iMX8M-Plus operation. All power-up circuitry and all required pullups/pulldowns are available onboard UCM-iMX8M-Plus.
- If for some reason you decide to place an external pullup or pulldown resistor on a certain signal (for example - on the GPIOs), first check the documentation of that signal provided in this manual. Certain signals have on-board pullup/pulldown resistors required for proper initialization. Overriding their values by external components will disable board operation. For details please refer to section Error! Reference source not found..
- You must be familiar with signal interconnection design rules. There are many sensitive groups of signals. For example:
- PCIe, Ethernet, USB and more signals must be routed in differential pairs and by a controlled impedance trace.
- Audio input must be decoupled from possible sources of carrier board noise.
- The following interfaces should meet the differential impedance requirements with manufacturer tolerance of 10%:
- USB2.0: DP/DM signals require 90 ohm differential impedance.
- All single-ended signals require 50 ohm impedance.
- PCIe TX/RX data pairs and PCIe clocks require 85 ohm differential impedance.
- Ethernet, MIPI-CSI and MIPI-DSI signals require 100 ohm differential impedance.
- The carrier board interface connectors provide 3mm mating height. Bear in mind that there are components on the bottom side of UCM-iMX8M-Plus. It is not recommended to place any components underneath the UCM-iMX8M-Plus module.
- Refer to the SB-UCMIMX8PLUS carrier board reference design schematics.

- It is recommended to send the schematics of the custom carrier board to Compulab support team for review.

V_SOM is recommended between 3.45 and 4.4 volt, typical 3.7

for more information see UCM i.MX8 PLUS Reference Guide

Connecting the SB-UCM-iMX8PLUS carrier board



For further details see Product Page.

- 2 * I-PEX connector directly between UCM carrier board and bridge board
- 45 pins connected to Inbetween breakout boards
- 10 pins power connector to Inbetween breakout boards
- USB-C connector to Inbetween breakout boards
- USB-A connector to Inbetween breakout boards
- HDMI female to Inbetween breakout boards

909b Connector Pinouts

Debugging Breakout connector

No.	Pin	Description	Voltage
1	5V	Board Power 5V	
2	3V3	Board Power 3.3V	
3	VIN	USB Power input direct	

No.	Pin	Description	Voltage
4	GND	GND	
5	UART2_RX	Debug UART2 RX	
6	UART2_TX	Debug UART2 TX	
7	GND	GND	
8	UART4_RX	Debug UART4 RX	
9	UART4_TX	Debug UART4 TX	
10	GND	GND	
11	TDI	JTAG	
12	TMS	JTAG	
13	TCK	JTAG	
14	RTCK	NC? JTAG	
15	TDO	JTAG	
16	RESET	SYS_RST_PMIC SoM	
17	PWRBTN	Power Button SoM	
18	QSPI_BOOT_EN_3P3	FLEXSPI BOOT	
19	ALT_BOOT	PB_ALT_BOOT	
20	MIC_BCLK / SCK	I2S Mic Bit clock line (RXC)	1.8V
21	MIC_WS / LRCLK	I2S Mic Word clock line (RXFS)	1.8V
22	MIC_SDATA1	I2S Mic data 1	1.8V
23	MIC_SDATA2	I2S Mic Input data 2	1.8V
24	MIC_SDATA3	I2S Mic Input data 3	1.8V
25	MIC_SDATA4	I2S Mic Input data 4	1.8V
26	ECSPI2_MISO	SPI2 MISO	
27	ECSPI2_SCLK	SPI2 Clock	
28	ECSPI2_SS0	SPI2 SS0	
29	ECSPI2_MOSI	SPI2 MOSI	
30	L_CAM_FSIN	Left Frame sync input	
31	L_CAM_STROBE	Left Frame sync output	
32	L_EXTCLK	Left External Clock Input (MCLK)	
33	L_ATT_XSHUT	Left Attached Shutdown	

No.	Pin	Description	Voltage
34	L_RESET	Left Camera Reset	
35	L_PWRDN	Left Camera Shutdown	
36	R_CAM_FSIN	Right Frame sync input	
37	R_CAM_STROBE	Right Frame sync output	
38	R_EXTCLK	Right External Clock Input (MCLK)	
39	R_ATT_XSHUT	Right Attached Shutdown	
40	R_RESET	Right Camera Reset	
41	R_PWRDN	Right Camera Shutdown	
42	SYS_SCL	System I2C SCL	
43	SYS_SDA	System I2C SDA	
44	I2C3_SCL	Stem/3 I2C SCL	
45	I2C3_SDA	Stem/3 I2C SDA	

T-USB alt mode connectors

These connectors(only on the 909 model) enables experimentation with alternate modes and directional pins.

Host ALT

Pin	Code	Description
1	3V3	
2	SBU2	Host AUX+ / SBU2
3	SBU1	Host AUX- / SBU1
4	3V3	
5	HA+	Host A+
6	HA-	Host A-
7	3V3	
8	HB+	Host B+
9	HB-	Host B-
10	3V3	
11	HC+	Host C+
12	HC-	Host C-

Pin	Code	Description
13	3V3	
14	HD+	Host D+
15	HD-	Host D-
16	GND	
17		
18		
19	GND	
20	HX+	Host Extra 2.0 D+
21	HX-	Host Extra 2.0 D-
22	HXA_SEL	Select Host Extra A6/A7
23	HXB_SEL	Select Host Extra B6/B7
24	GND	
25	LVCLK+	LVDS CLK+
26	LVCLK-	LVDS CLK-
27	GND	
28	LVD0+	LVDS D0+
29	LVD0-	LVDS D0-
30	GND	
31	LVD1+	LVDS D1+
32	LVD1-	LVDS D1-
33	GND	
34	LVD2+	LVDS D2+
35	LVD2-	LVDS D2-
36	GND	
37	LVD3+	LVDS D3+
38	LVD3-	LVDS D3-
39	GND	
40	TOUCH_INT	LVDS TOUCH INT EX0.6
41	TOUCH_RST	LVDS TOUCH Reset EX0.7
42	I2C SCL	SYS SCL

Pin	Code	Description
43	I2C SDA	SYS SDA
44	UART3_TXD	P1.61 UART3 Tx
45	UART3_RXD	P1.21 UART3 Rx

OTG ALT

Pin	Code	Description
1	3V3	
2	SBU2	OTG AUX+ / SBU2
3	SBU1	OTG AUX- / SBU1
4	3V3	
5	OA+	OTG A+
6	OA-	OTG A-
7	3V3	
8	OB+	OTG B+
9	HB-	OTG B-
10	3V3	
11	OC+	OTG C+
12	OC-	OTG C-
13	3V3	
14	OD+	OTG D+
15	OD-	OTG D-
16	GND	
17		
18		
19	GND	
20	OX+	OTG Extra 2.0 D+
21	OX-	OTG Extra 2.0 D-
22	OXA_SEL	Select OTG Extra A6/A7
23	OXB_SEL	Select OTG Extra B6/B7
24	GND	

Pin	Code	Description
25	TRCLK+	ETH0 TR CLK+
26	TRCLK-	ETH0 TR CLK-
27	GND	
28	TR1+	ETH0 TR 1+
29	TR1-	ETH0 TR 1-
30	GND	
31	TR2+	ETH0 TR 2+
32	TR2-	ETH0 TR 2-
33	GND	
34	TR3+	ETH0 TR 3+
35	TR3-	ETH0 TR 3-
36	GND	
37	TR4+	ETH0 TR 4+
38	TR4-	ETH0 TR 4-
39	GND	
40	ETH0_LED_ACT	LED_ACT
41	ETH0_LINK-LED_10_100	ETH0_LINK-LED_10_100
42	I2C SCL	P1.99 SYS SCL
43	I2C SDA	P1.97 SYS SDA
44	UART1_TXD	P1.72 UART1 Tx
45	UART1_RXD	P1.19 UART1 Rx

Compress GPIO with expander and stem I2C (wire I2C3 ?)

M.2 Expansion Slots

M.2 Key B Expansion Module

Ziloo has an M.2 type B expansion port for SSD utility cards.

Features:

- 1 Lane PCIe (PExx0)
- USB 3.0 data multiplexed (USB2/Host, PExx1)
- USB 2.0 data multiplexed (USB2/Host)
- GNSS I2C (I2C3)

- MFG I2C (SYS I2C)
- AUDIO I2S MIC SAI5 4 channels (GPIO5..8 and COEX*)
- SPI (ANTCTL*)
- DAS/DSS broken out with activity LED + expander bit
- Additional signals via 16 bit I/O Expander
- Some are broken out with pads near connector (CONFIG 0/2/3, DPR)
- SIM pins are not connected, reserved for now

The USB is connected to T-USB (not the M.2 expansions) on boot to support NVMe SSD expansions by default. The USB data signals from SoM are multiplexed between T-USB Host (USB2) and M.2 Key B based on MUX_USB2_SEL & MUX_USB3_SEL.

Be aware the current pin plan is not final. Input/Ouput such as DIN/DOUT RXD/TXD may be the wrong way around. It must be verified with reference hardware design/testing.

According to documentation: Type refers to the signal direction: • Type O means signal is an output from the MPU/MCU to the adapter. • Type I means signals is an input to the MPU/MCU from the adapter.

Control pins mapped by I/O Expander

The system I/O expander controls mPCIe_PERST which resets PCIe. PCIE_CLKREQ_B is a direct pin on the SoM. USB1_SS_SEL is a direct pin on the SoM.

TODO consider bootup default state of I/O Expanders. USB must not connect M.2 by default

TODO unallocated/GPIO pins from chipsets

A dedicated I/O Expander controls addition pins on Key B.

The development board uses a single Expander. The 909 and 801 uses 3x PCA9555 to control more states.

The EX2 expander input triggers interrupt via EX_H_nINT (GPIO1_IO0). The pins relate to USB2 Host and M.2 Key B.

The EX2 expander allows controlling T-USB maps,

Expander	Connected to
EX2.0	USB_H_ALT_EN
EX2.1	USB_H_ALT_POL
EX2.2	USB_H_ALT_AMSEL
EX2.3	MUX_USB2_SEL
EX2.4	MUX_USB3_SEL
EX2.5	M2B_PWROFF
EX2.6	RESET#
EX2.7	ALERT / I2C_IRQ

Expander	Connected to
EX2.8	GPIO4 on 65988 (HPD2)
EX2.9	LED / DAS / DSS
EX2.10	W_DISABLE_2#
EX2.11	W_DISABLE#
EX2.12	DEVSLP 3V3
EX2.13	
EX2.14	CONFIG_1
EX2.15	

M.2 Key B Pin allocations

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
1	CONFIG_3		Defines Module Type	pad	
2		+3.3V	3.3 V power supply from main board		3.3V
3	GND		Ground		GND
4		+3.3V	3.3 V power supply from main board		3.3V
5	GND		Ground (available?)		GND
6		M2B_PWROFF	Card PWR OFF	EX2.5	1.8/3.3
7	USB D+		USB data pair positive USB D+	USB D+	
8		W_DIS1	Wireless disable 1	EXB.3	
9	USB D-		USB data pair negative USB D-	USB D-	
10		DAS/DSS	Device Actvty Signal	LED / EX2.9	3.3V
11	GND		Ground (available?)		GND
12 - 19					
20		M2_I2S_CLK	GPIO5 M2_I2S_CLK	MIC I2S	1.8V
21	CONFIG_0			pad	
22		M2_I2S_DIN	GPIO6 M2_I2S_DIN	MIC I2S DATA0	1.8V

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
23	GPIO11		NC	MIC I2S MCLK	1.8V
24		M2_I2S_DOUT	GPIO7 M2_I2S_DOUT	PWM_OUT1	1.8V
25	DPR			pad	
26		GPIO10		EX2.10	1.8V
27	GND		Ground		GND
28		M2_I2S_WS	GPIO8 M2_I2S_WS	MIC I2S WS	1.8V
29	USB3 RX-		PER-1 / SSIC M2_USB3_SSRXN	M2_USB3_SSRX-	
30		SIM_RST	UIM RESET	-	
31	USB3 RX+		PER+1 / SSIC M2_USB3_SSRXP	M2_USB3_SSRX+	
32		SIM_CLK	UIM CLK	-	
33	GND		Ground		GND
34		SIM_DATA	UIM DATA	-	
35	USB3 TX-		PET-1 / SSIC M2_USB3_SSTX-	M2_USB3_SSTX-	
36		SIM_PWR	UIM PWR	-	
37	USB3 TX+		PET+2 / SSIC M2_USB3_SSTX+	M2_USB3_SSTX+	
38		DEVSLP	Device Sleep, input. high=sleep	EX2.12	3.3V
39	GND		Ground		GND
40		M2 SMB SCL	SMB_CLK M2 SMB SCL	I2C3 SCL	1.8V
41	PCIE RXN-		PCIE RXN- / PER-0 / SATA-B+	PCIE RXN-	
42		M2 SMB SDA	SMB_DATA M2 SMB SDA	I2C3 SDA	1.8V
43	PCIE RXN+		PCIE RXN+ / PER+0 / SATA-B-	PCIE RXN+	1.8V
44		GPIO2	GPIO2 / ALERT	EX2.7	1.8V
45	GND		Ground		GND
46		GPIO3		PWM2_OUT	1.8V
47	PCIE TXN-		PCIE TXN- / PET-0 / SATA-A-	PCIE TXN-	1.8V

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
48		GPIO4		PWM3_OUT	1.8V
49	PCIE TXN+		PCIE TXN+ / PET-0 / SATA-A+	PCIE TXN+	1.8V
50		PERST	PCI Reset	mPCIe_PERST	
51	GND		Ground		GND
52		CLKREQ	Reference clock request	PCIE_CLKREQ_B	3.3V
53	PCIE REFCLK-		PCIE REFCLK-	REFCLK-	
54		WAKE	PCIe WAKE# Active Low.	USB1_SS_SEL	
55	PCIE REFCLK+		PCIE REFCLK+	REFCLK+	
56		MFG_DAT	SDA	SYS I2C SDA	
57	GND		Ground		
58		MFG_CLK	SCL	SYS I2C SCL	
59	ANTCTL0			ECSPI2_MISO	
60		COEX3		MIC I2S DATA3	
61	ANTCTL1			ECSPI2_SS0	
62		COEX_TXD		MIC I2S DATA2	1.8V
63	ANTCTL2			ECSPI2_SCLK	
64		COEX_RXD		MIC I2S DATA1	1.8V
65	ANTCTL3			ECSPI2_MOSI	
66		SIM DETECT	SIM CD	-	
67	RESET#		RESET	EX2.6	1.8V
68		SUSCLK	32.768 kHz provided by Platform	-	
69	CONFIG_1		Defines module type +	EX2.14	
70		VRES	Power VRES		+3.3V
71	GND		Ground		GND
72		VRES	Power VRES		+3.3V
73	GND		Ground		GND
74		VRES	Power VRES		+3.3V

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
75	CONFIG_2		Defines Module Type NC		

Reference designs

 Congatec reference design SSD Key B Congatec reference design

 UCM Carrier Board reference design Key B Compulab reference design

M.2 Key E Expansion Module

Ziloo has an M.2 type E expansion port for Wireless/Bluetooth/GSM utility cards.

Features:

- UART2, UART4
- USB2/USB3 OTG data
- SD1 SDIO 4bit
- PCIe single lane reserved pins
- JTAG + debugging pins reserved (lay out pads)
- Speaker I2S
- Two LEDs next to connector
- SIM pins are not connected, reserved for now

Control pins mapped by I/O Expander

USB2_SS_SEL is a direct pin on the SoM.

TODO consider bootup default state of I/O Expanders. USB must not connect M.2 by default

TODO unallocated/GPIO pins from chipsets

A dedicated I/O Expander controls addition pins on Key E.

The development board uses a single Expander. The 909 and 801 uses 3x PCA9555 to control more states.

The EX1 expander input triggers interrupt via EX_O_nINT (GPIO1_IO1). The pins relate to USB1 Host and M.2 Key E.

The EX1 expander allows controlling T-USB maps,

Expander	Connected to
EX1.0	USB_O_ALT_EN
EX1.1	USB_O_ALT_POL
EX1.2	USB_O_ALT_AMSEL
EX1.3	MUX_USB2_SEL

Expander	Connected to
EX1.4	MUX_USB3_SEL
EX1.5	COEX4
EX1.6	DEV_WLAN_WAKE
EX1.7	ALERT / I2C_IRQ
EX1.8	GPIO3 on 65988 (HPD1)
EX1.9	LED / DAS / DSS
EX1.10	W_DISABLE2#
EX1.11	W_DISABLE1#
EX1.12	UART WAKE
EX1.13	SDIO WAKE
EX1.14	LED2#
EX1.15	

M.2 Key E Pin allocations

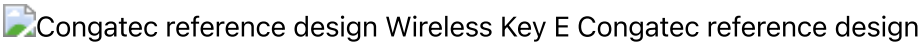
Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
1	GND		Ground		
2		+3.3V	3.3 V power supply from main board		3.3V
3	USB D+		USB data pair positive	USB D+	
4		+3.3V	3.3 V power supply from main board		3.3V
5	USB D-		USB data pair negative	USB D-	
6		M2B_PWROFF	Card PWR OFF	EX2.5	1.8/3.3
7	GND		Ground		GND
8		M2_I2S_CLK	GPIO5 M2_I2S_CLK	MIC I2S	1.8V
9	SDIO CLK		SDIO	SD1 CLK	1.8V
10		M2_I2S_WS	GPIO8 M2_I2S_WS	MIC I2S WS	1.8V
11	SDIO CMD		SDIO	SD1 CMD	1.8V
12		M2_I2S_DIN	GPIO6 M2_I2S_DIN	MIC I2S DATA0	1.8V

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
13	SDIO DATA0		SDIO	SD1 DATA0	1.8V
14		M2_I2S_DOUT	GPIO7 M2_I2S_DOUT	PWM_OUT1	1.8V
15	SDIO DATA1		SDIO	SD1 DATA1	1.8V
16		LED2#			
17	SDIO DATA2		SDIO	SD1 DATA2	1.8V
18		GND	Ground		GND
19	SDIO DATA3		SDIO	SD1 DATA3	1.8V
20		UART WAKE#	Bluetooth uses to wake up platform	EX1.12	3.3V
21	SDIO WAKE#		WiFi uses to wake up platform	EX1.13	1.8V
22		UART RxD		UART2_RXD	1.8V
23	SDIO RESET#		Signal to independently reset WiFi	SD1_RESET_B	1.8V
24 - 31					
32		UART TxD		UART2_TXD	1.8V
33	GND		Ground		GND
34		UART CTS		UART4_RXD	1.8V
35	PCIE TXN-		PCIE TXN- / PET-0 / SATA-A-	-	1.8V
36		UART RTS		UART4_TXD	1.8V
37	PCIE TXN+		PCIE TXN+ / PET-0 / SATA-A+	-	1.8V
38		JTAG_TDO	Debugging		1.8V
39	GND		Ground		GND
40		COEX4	Wake up the WiFi	EX1.5	1.8V
41	PCIE RXN-		PCIE RXN- / PER-0 / SATA-B+	-	
42		DEV_BT_WAKE	Wake up the Bluetooth	EX1.6	1.8V

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
43	PCIE RXN+		PCIE RXN+ / PER+0 / SATA-B-	-	1.8V
44		JTAG_TDI	Debugging		1.8V
45	GND		Ground		GND
46		JTAG_TCK	Debugging		1.8V
47	PCIE REFCLK+		PCIE REFCLK+	-	
48		JTAG_TMS	Debugging		1.8V
49	PCIE REFCLK-		PCIE REFCLK-	-	
50		SUSCLK	32.768 kHz provided by Platform	-	
51	GND		Ground		GND
52		PERST0#	PCI Reset	-	
53	CLKREQ0#		Reference clock request	-	3.3V
54		W_DISABLE2#	Independently reset the Bluetooth	EX1.10	1.8V
55	PE WAKE#		PCIe uses to wake up platform	-	1.8V
56		W_DISABLE1#	Full power down Bluetooth + WiFi	EX1.11	1.8V
57	GND		Ground		GND
58		I2C_DATA	I2C DATA	I2C3 SDA	1.8V
59	USB3 TX+		PET+1 / SSIC M2_USB3_SSTX+	M2_USB3_SSTX+	
60		I2C_CLK	I2C CLK	I2C3 SCL	1.8V
61	USB3 TX-		PET-1 / SSIC M2_USB3_SSTX-	M2_USB3_SSTX-	
62		ALERT#		EX1.7	1.8V
63	GND		Ground		GND
64		Reserved			

Pin id.	Upper	Lower	Description	Counterpoint	Voltage Level
65	USB3 RX+		PER+1 / SSIC M2_USB3_SSRXP	M2_USB3_SSRX+	
66		SIM_SWP	UIM SWP	-	
67	USB3 RX-		PER-1 / SSIC M2_USB3_SSRXN	M2_USB3_SSRX-	
68		SIM_PWR	UIM PWR	-	
69	GND		Ground		GND
70		SIM_PWR	UIM PWR / PEWAKE1#	-	
71	REFCLK+1			-	
72		VRES	Power VRES		+3.3V
73	Reserved				
74		VRES	Power VRES		+3.3V
75	GND		Ground		GND

Reference designs

 Congatec reference design Wireless Key E Congatec reference design

Future Expansion connection

UIM / SIM / eSIM

M.2 Connectors have pins reserved for SIM (UIM) cards. A connector or eSIM may be added in the future.

i.MX 8 only provides PCIe x1 so Key M is not relevant. This leaves A, B and E.

- B is good for USB3, Audio, SATA
- E is good for SDIO, UART and PCM
- ATP M.2 key info page
- Congatec AN43

RPI FPC 22 pins

Pin	Code	Type	Details	Voltage
1	GND	Power	Ground	
2	CAM_D0_N	Data	MIPI Data Lane 0 Negative	
3	CAM_D0_P	Data	MIPI Data Lane 0 Positive	

Pin	Code	Type	Details	Voltage
4	GND	Power	Ground	
5	CAM_D1_N	Data	MIPI Data Lane 1 Negative	
6	CAM_D1_P	Data	MIPI Data Lane 1 Positive	
7	GND	Power	Ground	
8	CAM_CK_N	Data	MIPI Clock Lane Negative	
9	CAM_CK_P	Data	MIPI Clock Lane Positive	
10	GND	Power	Ground	
11	CAM_D2_N	Data	MIPI Data Lane 2 Negative	
12	CAM_D2_P	Data	MIPI Data Lane 2 Positive	
13	GND	Power	Ground	
14	CAM_D3_N	Data	MIPI Data Lane 3 Negative	
15	CAM_D3_P	Data	MIPI Data Lane 3 Positive	
16	GND	Power	Ground	
17	CAM_IO0	Power	Power Enable	
18	CAM_IO1	LED	LED Indicator	
19	GND	Power	Ground	
20	SCL	I2C	I2C SCL	
21	SDA	I2C	SCCB serial Interface data IO	
22	VCC	Power	3.3V Power Supply	

NVIDIA FPC 30 pins

The connector is an I-PEX type 20525-030E-02 with 0.4mm pitch & 30 pins. Data pins are 1.8V level.

Pin	Code	Details
1	CAM_3V3	3.3V Power Input
2	CAM_3V3	
3	CAM_1V8	1.8V Power Input
4	GND	
5	GND	
6	PWR DWN	PWRDN on 34pin
7	I2C SCL	

Pin	Code	Details
8	I2C SDA	
9	GND	
10	CSI D2-	
11	CSI D2+	
12	TRIGGER	
13	MCLK	EXTCLK on 34pin
14	Reserved	
15	CSI D1-	
16	CSI D1+	
17	GND	
18	GND	
19	CSI D0-	
20	CSI D0+	
21	RESET	RESET on 34pin
22	GND	
23	Reserved	
24	CSI CLK-	
25	CSI CLK+	
26	GND	
27	CSI D3-	
28	CSI D3+	
29	Flash	
30	Reserved	

Refs

- https://www.leopardimaging.com/product/accessories/cables/faw-1233-03/
- https://www.mouser.com/datasheet/2/233/LI-TX1-CB-6CAM_datasheet-1395894.pdf
- https://connecttech.com/ftp/pdf/ASG006_Spacely.pdf
- https://www.i-pex.com/product/cabline-ca

Ziloo Camera Module 34 pin connector

Just to be clear: All CSI lanes are laid out on one side of the connector with GND between.

Pin 1 is indicated on the board by a dot.

Toward thin part with microphone and other sensors

Pin	Code	Type	Details	Voltage
1	AF_VDD	Power	Reserved for Autofocus	3.3V
2	AVDD_2V8	Power	Analog, Max 500mA	2.8V
3	DOVDD	Power	Power for I/O circuit, Max 500mA	1.8V
4	VCC_1V8	Power	1.8V ,MAX 200mA	1.8V
5	GND	Power	GND	
6	CAM_FSIN	I/O	Frame sync input	
7	CAM_STROBE	I/O	Frame sync output	
8	EXTCLK	Input	External Clock Input (MCLK)	
9	ATT_INT	Output	Interrupt Attached Sensor, Active L	1.8V?
10	ATT_XSHUT	Input	Attached Sensor XSHUTDOWN	1.8V
11	Reserved	AF/PWM	PWM Motor control (NC)	
12	I2C_SCL	I/O	I2C?_SCL(pullup resistor 2.2K)	1.8V
13	I2C_SDA	I/O	I2C?_SDA(pullup resistor 2.2K)	1.8V
14	BCLK / SCK	I2S	Bit clock line	1.8V
15	WS / LRCLK	I2S	Word clock line	1.8V
16	SDATA1	I2S	Input data 1	1.8V
17	SDATA2	I2S	Input data 2 (NC)	1.8V

Towards image sensors

Pin	Code	Type	Details	Voltage
34	AGND	Power	Analog ground	
33	RESET	Input	Camera Reset, Active Low (RSTB)	
32	PWRDN	Input	Camera Power Down	
31	Reserved			
30	Reserved			
29	-		GND	
28	CSI_RX_D0P	Camera	MIPI_CSI_RX_D0+	1.8V
27	CSI_RX_D0N	Camera	MIPI_CSI_RX_D0-	1.8V

Pin	Code	Type	Details	Voltage
26	-		GND	
25	CSI_RX_D1P	Camera	MIPI_CSI_RX_D1+	1.8V
24	CSI_RX_D1N	Camera	MIPI_CSI_RX_D1-	1.8V
23	-		GND	
22	CSI_RX_D2P	Camera	MIPI_CSI_RX_D2+	1.8V
21	CSI_RX_D2N	Camera	MIPI_CSI_RX_D2-	1.8V
20	-		GND	
19	CSI_RX_CLKP	Camera	MIPI_CSI_RX_CLK+	1.8V
18	CSI_RX_CLKN	Camera	MIPI_CSI_RX_CLK-	1.8V