## VE370 Project 2

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## 1 Objective

This project aims at writing a pipiline stage processor of Risc-V in Verilog.

## 2 Tested Code

```
00100093
                           addi
                                     x1
                                           x0
                                                1
00200113
                           addi
                                     x2
                                           x0
                                                2
                     //
                                                3
00300193
                           addi
                                     x3
                                           x0
                           addi
00400213
                                     x4
                                           x0
                                                4
002080 \, \mathrm{b3}
                           add x1
                                     x1
                                           x2
                           add x1
003080b3
                                     x1
                                           x3
004080b3
                           add x1
                                     x1
                                           x4
00500293
                           addi
                                     x5
                                           x0
                                                5
00600313
                           addi
                                     x6
                                           x0
                                                6
                                                7
00700393
                           addi
                                     x7
                                           x0
00800413
                           addi
                                     x8
                                           x0
                                                8
00900493
                           addi
                                     x9
                                           x0
                                                9
00a00513
                           addi
                                     x10 x0
                                                10
                           addi
00b00593
                                     x11 x0
                                                11
00c00613
                           addi
                                     x12 x0
                                                12
00d00693
                           addi
                                     x13 x0
                                                13
                           addi
00e00713
                                     x14 x0
                                                14
00 \, \text{f} \, 00 \, 793
                           addi
                                     x15 x0
                                                15
01000813
                           addi
                                     x16 x0
                                                16
01100893
                           addi
                                     x17
                                          x0
                                                17
01200913
                           addi
                                     x18 x0
                                                18
01300993
                           addi
                                     x19 x0
                                                19
                                                20
01400 \, \mathrm{a} 13
                           addi
                                     x20 x0
                           addi
                                     x21 x0
                                                21
01500 \, a93
                           addi
                                     x22 x0
                                                22
01600 \, \mathrm{b} 13
                     //
01700 \, \mathrm{b} 93
                           addi
                                     x23 x0
                                                23
407302b3
                                     x6
                                           x7
                           sub x5
0062 ffb3
                           and x31
                                     x5
                                           x6
0055 e 533
                                x10
                                     x11 x5
                           or
fff37f13
                           andi
                                      x30 x6
                                                4095
0045\,\mathrm{a}023
                                      0(x11)
                          sw
                                x4
```

```
0005\,a603
                     //
                                x12 \ 0(x11)
                          lw
00760833
                          add x16 x12 x7
                     //
                     //
00b82023
                          sw
                                x11 \ 0(x16)
00082903
                     //
                                x18 \ 0(x16)
                          lw
007908b3
                     //
                          add x17 x18 x7
0049a223
                     //
                                x4
                                     4(x19)
                          sw
00000013
                     //
                          nop
00000013
                     //
                          nop
                               x20 \ 4(x19)
0049aa03
                          lw
014 \,\mathrm{ba}223
                     //
                                x20 \ 4(x23)
                          sw
00000013
                          nop
00000013
                          nop
004 \, \mathrm{bad} \, 83
                          lw
                               x27 \ 4(x23)
004baa83
                     //
                          lw
                                x21 \ 4(x23)
013aa223
                               x19 \ 4(x21)
                          sw
                               x25 \ 4(x21)
004aac83
                          lw
017 \, \mathrm{b} \, 0 \, \mathrm{c} \, 33
                          add x24 x22 x23
00000013
                          nop
00000013
                          nop
```

## 3 Results

```
close_sim
INFO: [Simtcl 6-16] Simulation closed
launch_simulation
INFO: [Vivado 12-5682] Launching behavioral simulation in 'D:/
   Study/SJTU/Junior/2021SU/VE370/Project/p2/Group_impl/
   Group_impl.sim/sim_1/behav/xsim'
INFO: [SIM-utils -51] Simulation object is 'sim_1'
INFO: [SIM-utils-54] Inspecting design source files for '
   testbench' in fileset 'sim_1'...
INFO: [USF-XSim-97] Finding global include files...
INFO: [USF-XSim-100] Fetching design files from 'sources_1'...(
   this may take a while)...
INFO: [USF-XSim-101] Fetching design files from 'sim_1'...
INFO: [USF-XSim-2] XSim::Compile design
INFO: [USF-XSim-61] Executing 'COMPILE and ANALYZE' step in 'D:/
   Study/SJTU/Junior/2021SU/VE370/Project/p2/Group_impl/
   Group_impl.sim/sim_1/behav/xsim'
"xvlog —incr —relax -prj testbench_vlog.prj"
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/EX_MEM.v" into
   library xil_defaultlib
INFO:
     [VRFC 10-311] analyzing module EXMEM
     [VRFC 10-311] analyzing module EX_MEM_control
INFO:
      [VRFC 10-311] analyzing module EX_MEM_imme
INFO:
INFO:
      [VRFC 10-311] analyzing module EX_MEM_pc
      [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
INFO:
   Junior/2021SU/VE370/Project/p2/group/src/ForwardingUnit.v"
```

```
into library xil_defaultlib
INFO: [VRFC 10-311] analyzing module ForwardingUnit
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/ID_EX.v" into
   library xil_defaultlib
INFO: [VRFC \ 10-311] analyzing module ID_EX
INFO: [VRFC 10-311] analyzing module ID_EX_control
INFO:
      [VRFC 10-311] analyzing module ID_EX_imme
INFO: [VRFC 10-311] analyzing module ID_EX_pc
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/IF_ID.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module IF_ID
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/MEM_WB.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module MEMWB
INFO: [VRFC 10-311] analyzing module MEM_WB_control
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/alu.v" into library
  xil_defaultlib
INFO: [VRFC 10-311] analyzing module alu
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/alu_control.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module alu_control
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/control.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module control
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/data_memory.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module data_memory
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/
   HazardControlDetection.v" into library xil_defaultlib
INFO: [VRFC 10-311] analyzing module hazardControlDetection
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/
   immediate_generator.v" into library xil_defaultlib
INFO: [VRFC 10-311] analyzing module immediate_generator
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/instru_memory.v"
   into library xil_defaultlib
INFO: [VRFC 10-311] analyzing module instru_memory
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/register.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module register
```

```
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/next_pc.v" into
   library xil_defaultlib
INFO: [VRFC 10-311] analyzing module next_pc
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/program_counter.v"
   into library xil_defaultlib
INFO: [VRFC 10-311] analyzing module program_counter
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/mux.v" into library
   xil_defaultlib
INFO: [VRFC 10-311] analyzing module one_64bit_mux
INFO: [VRFC 10-311] analyzing module two_64bit_mux
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/main.v" into library
    xil_defaultlib
INFO: [VRFC 10-311] analyzing module alu_control
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'alu_control' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/alu_control.v:3]
INFO: [VRFC 10-311] analyzing module alu
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'alu' [D:/Study/SJTU/Junior/2021SU/VE370/Project/p2/
   group/src/alu.v:3]
INFO: [VRFC \ 10-311] analyzing module control
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'control' [D:/Study/SJTU/Junior/2021SU/VE370/Project/
   p2/group/src/control.v:3]
INFO: [VRFC 10-311] analyzing module data_memory
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'data_memory' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/data_memory.v:3]
INFO: [VRFC 10-311] analyzing module immediate_generator
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'immediate_generator' [D:/Study/SJTU/Junior/2021SU/
   VE370/Project/p2/group/src/immediate_generator.v:3]
INFO: [VRFC 10-311] analyzing module instru_memory
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'instru_memory' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/instru_memory.v:3]
INFO: [VRFC 10-311] analyzing module next_pc
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'next_pc' [D:/Study/SJTU/Junior/2021SU/VE370/Project/
   p2/group/src/next_pc.v:3]
INFO: [VRFC 10-311] analyzing module program_counter
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'program_counter' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/program_counter.v:3
INFO: [VRFC 10-311] analyzing module register
WARNING: [VRFC 10-3609] overwriting previous definition of
```

```
module 'register' [D:/Study/SJTU/Junior/2021SU/VE370/Project/
   p2/group/src/register.v:3]
INFO: [VRFC 10-311] analyzing module IF_ID
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'IF_ID' [D:/Study/SJTU/Junior/2021SU/VE370/Project/p2/
   group/src/IF_ID.v:3]
INFO: [VRFC 10-311] analyzing module ID_EX
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'ID_EX' [D:/Study/SJTU/Junior/2021SU/VE370/Project/p2/
   group/src/ID_EX.v:3]
INFO: [VRFC 10-311] analyzing module ID_EX_control
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'ID_EX_control' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/ID_EX.v:51]
INFO: [VRFC 10-311] analyzing module ID_EX_imme
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'ID_EX_imme' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/ID_EX.v:110]
INFO: [VRFC 10-311] analyzing module ID_EX_pc
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'ID_EX_pc' [D:/Study/SJTU/Junior/2021SU/VE370/Project/
   p2/group/src/ID_EX.v:138]
INFO: [VRFC 10-311] analyzing module EXMEM
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'EX.MEM' [D:/Study/SJTU/Junior/2021SU/VE370/Project/p2
   /group/src/EX_MEM.v:3]
INFO: [VRFC 10-311] analyzing module EX_MEM_control
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'EX_MEM_control' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/EX_MEM.v:44]
INFO: [VRFC 10-311] analyzing module EX_MEM_imme
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'EX_MEM_imme' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/EX_MEM.v:83]
INFO: [VRFC 10-311] analyzing module EX_MEM_pc
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'EX_MEM_pc' [D:/Study/SJTU/Junior/2021SU/VE370/Project
   /p2/group/src/EX_MEM.v:101
INFO: [VRFC 10-311] analyzing module MEMWB
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'MEMWB' [D:/Study/SJTU/Junior/2021SU/VE370/Project/p2
   /group/src/MEM_WB.v:3]
INFO: [VRFC 10-311] analyzing module MEM_WB_control
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'MEM_WB_control' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/MEM_WB.v:30]
INFO: [VRFC 10-311] analyzing module one_64bit_mux
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'one_64bit_mux' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/mux.v:3]
```

```
INFO: [VRFC 10-311] analyzing module two_64bit_mux
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'two_64bit_mux' [D:/Study/SJTU/Junior/2021SU/VE370/
   Project/p2/group/src/mux.v:25]
INFO: [VRFC 10-311] analyzing module main
INFO: [VRFC 10-2263] Analyzing Verilog file "D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/group/src/testbench.v" into
   library xil_defaultlib
      [VRFC 10-311] analyzing module alu_control
INFO:
INFO:
      [VRFC 10-311] analyzing module alu
INFO:
      [VRFC 10-311] analyzing module control
      [VRFC 10-311]
                    analyzing module data_memory
INFO:
INFO: [VRFC 10-311] analyzing module immediate_generator
      [VRFC 10-311]
                    analyzing module instru_memory
INFO:
      [VRFC 10-311] analyzing module next_pc
INFO:
      [VRFC \ 10-311]
                    analyzing module program_counter
INFO:
INFO:
      [VRFC 10-311]
                    analyzing module register
      [VRFC 10 - 311]
INFO:
                    analyzing module IF_ID
INFO:
      [VRFC 10-311]
                    analyzing module ID_EX
      [VRFC 10-311]
                    analyzing module ID_EX_control
INFO:
                    analyzing module ID_EX_imme
INFO:
      [VRFC 10-311]
      [VRFC 10-311]
INFO:
                    analyzing module ID_EX_pc
INFO:
      [VRFC 10-311]
                    analyzing module EXMEM
INFO:
      [VRFC 10-311]
                    analyzing module EX_MEM_control
INFO:
      [VRFC 10-311] analyzing module EX_MEM_imme
      [VRFC 10-311] analyzing module EX_MEM_pc
INFO:
      [VRFC 10-311] analyzing module MEMWB
INFO:
INFO: [VRFC 10-311] analyzing module MEM_WB_control
INFO: [VRFC 10-311] analyzing module one_64bit_mux
INFO: [VRFC 10-311] analyzing module two_64bit_mux
INFO: [VRFC \ 10-311] analyzing module main
WARNING: [VRFC 10-3609] overwriting previous definition of
   module 'main' [D:/Study/SJTU/Junior/2021SU/VE370/Project/p2/
   group/src/main.v:18]
INFO: [VRFC 10-311] analyzing module testbench
run_program: Time (s): cpu = 00:00:00; elapsed = 00:00:05.
   Memory (MB): peak = 1844.254; gain = 0.000
INFO: [USF-XSim-69] 'compile' step finished in '5' seconds
INFO: [USF-XSim-3] XSim:: Elaborate design
INFO: [USF-XSim-61] Executing 'ELABORATE' step in 'D:/Study/SJTU
   /Junior/2021SU/VE370/Project/p2/Group_impl/Group_impl.sim/
   sim_1/behav/xsim'
"xelab -wto 4debe8d2cc774e4f82b6b4a336ea92a6 —incr —debug
   typical — relax — mt 2 -L xil-defaultlib -L unisims_ver -L
   unimacro_ver -L secureip —snapshot testbench_behav x
   il_defaultlib.testbench xil_defaultlib.glbl -log
   elaborate.log"
Vivado Simulator 2019.1
Copyright 1986-1999, 2001-2019 Xilinx, Inc. All Rights Reserved.
Running: E:/Softwares/Xilinx/Vivado/2019.1/bin/unwrapped/win64.o
```

```
/xelab.exe -wto 4debe8d2cc774e4f82b6b4a336ea92a6 -incr -
   debug typical — relax — mt 2 -L xil_defaultlib -L unisims_ver
   -L unimacro_ver -L secureip —snapshot testbench_behav x
   il_defaultlib.testbench xil_defaultlib.glbl -log
   elaborate.log
Using 2 slave threads.
Starting static elaboration
Completed static elaboration
Starting simulation data flow analysis
Completed simulation data flow analysis
Time Resolution for simulation is 1ps
Compiling module xil_defaultlib.program_counter
Compiling module xil_defaultlib.instru_memory
Compiling module xil_defaultlib.IF_ID
Compiling module xil_defaultlib.control
Compiling module xil_defaultlib.alu_control
Compiling module xil_defaultlib.ID_EX_control
Compiling module xil_defaultlib.immediate_generator
Compiling module xil_defaultlib.ID_EX_imme
Compiling module xil_defaultlib.register
Compiling module xil_defaultlib.ID_EX
Compiling module xil_defaultlib.ID_EX_pc
Compiling module xil_defaultlib.two_64bit_mux
Compiling module xil_defaultlib.ForwardingUnit
Compiling module xil_defaultlib.alu
Compiling module xil_defaultlib.EX_MEM
Compiling module xil_defaultlib.EX_MEM_control
Compiling module xil_defaultlib.EX_MEM_imme
Compiling module xil_defaultlib.EX_MEM_pc
Compiling module xil_defaultlib.next_pc
Compiling module xil_defaultlib.one_64bit_mux
Compiling module xil_defaultlib.data_memory
Compiling module xil_defaultlib.MEM_WB
Compiling module xil_defaultlib.MEM_WB_control
Compiling module xil_defaultlib.hazardControlDetection
Compiling module xil_defaultlib.main
Compiling module xil_defaultlib.testbench
Compiling module xil_defaultlib.glbl
Built simulation snapshot testbench_behav
INFO: [USF-XSim-69] 'elaborate' step finished in '3' seconds
INFO: [USF-XSim-4] XSim::Simulate design
INFO: [USF-XSim-61] Executing 'SIMULATE' step in 'D:/Study/SJTU/
   Junior/2021SU/VE370/Project/p2/Group_impl/Group_impl.sim/
   sim_1/behav/xsim'
INFO: [USF-XSim-98] *** Running xsim
   with args "testbench_behav -key {Behavioral:sim_1:Functional:
     testbench } -tclbatch {testbench.tcl} -log {simulate.log}"
INFO: [USF-XSim-8] Loading simulator feature
Vivado Simulator 2019.1
Time resolution is 1 ps
```

```
source testbench.tcl
 set curr_wave [current_wave_config]
 if \{ | string | length | scurr_wave | == 0 \} 
#
   if \{ |llength| |get_objects| | > 0 \} 
#
     add_{-}wave /
#
     set\_property needs\_save false [current\_wave\_config]
#
   } else {
      send\_msg\_id Add\_Wave-1 W\!ARNING "No top level signals
  found. Simulator will start without a wave window. If you
  want to open a wave window go to 'File-New Waveform
  Configuration ' or type 'create_wave_config' in the TCL
  console."
#
# }
# run 1000 ns
instruction = 0x00000013
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00100093
Time:
              0, CLK = 1, PC = 0x00000000
[x0] = 0x00000000000000000
   \times 2
[x5] = 0x00000000000000000
[x7] = 0x00000000000000000
[x9] = 0x0000000000000000
[x10] = 0x00000000000000000
[x11] = 0x000000000000000000
[x12] = 0x00000000000000000
[x13] = 0x000000000000000000
[x14] = 0x00000000000000000
[x16]
    [x17]
    [x18] = 0x00000000000000000
[x19] = 0x00000000000000000
[x20] = 0x000000000000000000
[x21] = 0x00000000000000000
x22] = 0x00000000000000000
[x23] = 0x000000000000000000
[x25] = 0x000000000000000000
[x26] = 0x000000000000000000
```

```
[x29] = 0x00000000000000000
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000001
instruction = 0x00200113
Time:
      20, CLK = 1, PC = 0 \times 000000004
[x0]
  [x1]
x2
  [x3]
  x4]
  x5]
  x6]
  x7
  x81
  x9
  [x10] = 0x0000000000000000
x11]
  x12
  [x13]
  \times 14
  x15]
  x 16]
  [x17]
x 18]
  [x19]
  x20
  [x21]
  x22
  [x23]
  x24
  [x25]
  [x26]
  [x27]
  [x28]
  [x29]
  [x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000002
instruction = 0x00300193
Time:
      40, CLK = 1, PC = 0 \times 000000008
[x2]
```

```
|x4|
 x5]
 x6]
 x7
 [x8]
 [x9]
 x10]
 \times 11
x12]
 x13]
 [x14]
 x15]
 [x16]
 x17
 [x18]
 x 19]
 [x20]
 x21]
 x22
 x23
 x24
 x25
 x26
 x27
 x28
 x29]
 [x30]
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000003
instruction = 0x00400213
    60, CLK = 1, PC = 0 \times 00000000 c
Time:
 [x0]
[x1]
 |x2|
 [x3]
 |x4|
 [x5]
 x6]
 \times 7
 x8]
x9]
 [x10] = 0x00000000000000000
x11]
 [x12]
 x13
 [x14]
 [x15]
```

```
[x17]
[x18]
 [x19]
 [x20]
 [x21]
 [x22]
 x23
 x24
 x25
 x26]
 [x27]
 x28
 [x29]
 [x30]
 [x31]
 ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x002080b3
    80, CLK = 1, PC = 0 \times 000000010
Time:
[x0]
 \times 1
\times 2
 x3
 x4
 x5]
 x 6
 x7]
 x81
 x9]
 [x10] = 0x000000000000000000
 x11]
[x12]
 [x13]
[x14]
 [x15]
 x16]
 [x17]
 [x18]
 x19]
 x20]
 [x21]
x22
 [x23]
 x24
 [x25]
 x26
 [x27]
 [x28]
```

```
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x003080b3
      100, CLK = 1, PC = 0 \times 00000014
Time:
[x0] = 0x00000000000000000
  \times 1
x2
 [x3]
 x4]
 x5]
 x6]
 [x7]
  x8]
 x 9]
 x12]
  x13]
  x14
  x15
  x 16]
  x17
x18]
  x19]
  x20
  x21
  x22
  [x23]
x24
  [x25]
  x26
[x27]
  x28]
  [x29]
[x30] = 0x00000000000000000
[x31] = 0x000000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x004080b3
      120, CLK = 1, PC = 0 \times 00000018
Time:
[x1] = 0x000000000000000000
 [x3]
 [x4]
```

[x30] = 0x00000000000000000

```
x6]
 \times 7
[x8]
 x9]
 [x11]
 [x12]
 x13]
 \times 14
x15]
 [x16]
 x17
 [x18]
 x19]
 [x20]
 \times 21
 x22
 x23
 x24
 x25
 x 26]
 x27
 x28
 x29]
 [x30]
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00500293
    140, CLK = 1, PC = 0 \times 00000001c
Time:
 [x0]
 [x1]
 [x2]
[x3]
 [x4]
x5]
 [x6]
 x7]
 x8]
 x9]
 \times 10
x11
 [x12]
 x13]
 [x14]
 [x15]
 [x16]
 [x17]
```

```
[x19]
[x20]
 [x21]
 [x22]
 [x23]
 [x24]
 [x25]
 x 26]
 [x27]
 x28
 [x29]
 [x30]
 [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000005
instruction = 0x00600313
    160, CLK = 1, PC = 0 \times 000000020
Time:
[x0]
 \times 1
x2
 x3
 \times 4
x5]
 x6]
 \times 7
 x8]
 x9]
 [x10] = 0x00000000000000000
 \times 11
[x12]
 x13]
[x14]
 [x15]
[x16]
 [x17]
 [x18]
 [x19]
 [x20]
 [x21]
 [x22]
 [x23]
x24
 [x25]
x26
 [x27]
 x28
 [x29]
 [x30]
```

```
ALUcontrol: 0x2
immediate: 0x0000000000000006
instruction = 0x00700393
       180, CLK = 1, PC = 0 \times 000000024
Time:
[x0] = 0x00000000000000000
  [x1]
  x2
[x3] = 0x0000000000000000
x4]
  x5]
  x6]
  \times 7
  x8]
  x9]
  [x10] = 0x0000000000000000
x11] = 0x00000000000000000
x12
  x13]
  x 14]
  x15]
  x 16]
  x17]
  x 18]
  x19]
x20
  [x21]
  x22]
  [x23] = 0x00000000000000000
  x24
  [x25]
x26
  [x27]
  [x28]
[x29]
  [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000007
instruction = 0x00800413
Time:
       200, CLK = 1, PC = 0 \times 000000028
[x0] = 0x00000000000000000
  [x1]
  \times 2
[x3] = 0x00000000000000003
\times 4
  [x5]
  [x6]
```

```
[x9]
 [x10]
 x11]
 [x12]
 [x13]
 [x14]
 x15]
 [x16]
 x17
 [x18]
 x19]
 [x20]
 x21]
 [x22]
 x23
 x24
 x25]
 x26
 x27
 [x28]
 x29
 [x30]
 [x31] = 0x000000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000008
instruction = 0x00900493
    220, CLK = 1, PC = 0 \times 00000002 c
Time:
[x0]
 [x1]
 x2
 [x3]
 \times 4
x5]
 x6]
 [x7]
 [x8]
 [x9]
 [x10] = 0x00000000000000000
 [x11]
 [x12]
[x13]
 [x14]
x15]
 [x16]
 [x17]
 [x18]
 [x19]
```

```
[x21]
[x22]
 [x23]
 [x24]
 [x25]
 [x26]
 [x27]
 x28]
 [x29]
 [x30]
 ALUcontrol: 0x2
immediate: 0x00000000000000009
instruction = 0x00a00513
    240, CLK = 1, PC = 0 \times 000000030
Time:
[x0]
 \times 1
x2
 x3
x4]
 x5]
 x6]
 x7]
 x8]
 x 9]
 [x10] = 0x00000000000000000
x11]
 [x12]
 x13]
 [x14]
 x15]
[x16]
 [x17]
[x18]
 [x19]
 [x20]
 [x21]
 [x22]
 [x23]
 x24
 [x25]
x 26]
 [x27]
 x28
 [x29]
 [x30]
 ALUcontrol: 0x2
```

immediate: 0x0000000000000000a

```
instruction = 0x00b00593
Time:
     260, CLK = 1, PC = 0 \times 00000034
 [x0]
[x1]
 [x2]
 [x3]
 x4]
 x5
x6]
 x7
x8
 [x10] = 0x00000000000000000
[x11]
  x12
  x13]
  x14
  x15]
  x 16]
  [x17]
  x18]
  x19]
  x20]
  x21]
  x22
  x23
  x24
  [x25]
  x 26]
  [x27]
  x28
[x29]
  [x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x000000000000000b
instruction = 0x00c00613
     280, CLK = 1, PC = 0 \times 000000038
Time:
[x0] = 0x00000000000000000
 [x1]
[x2]
 [x3]
 x4]
[x5]
 x6]
 [x7]
 [x8]
 [x9]
```

```
[x10]
[x11]
  [x12]
  [x13]
  [x14]
  [x15]
  x16]
  x17
  [x18]
  x19]
  [x20]
  x21
  [x22]
  x23
  x24
  x25
  x26
  x27
  x29
  [x30]
  [x31] = 0x000000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00d00693
     300, CLK = 1, PC = 0 \times 00000003 c
Time:
[x0] = 0x0000000000000000
 [x1]
x2
 [x3]
 x4]
[x5]
 x6]
x7
 [8x
 x9]
 [x10] = 0x000000000000000000
[x11]
  [x12]
  x13]
  [x14]
[x15]
```

[x16]

 $\times 17$ 

[x18]

x19

[x20]

[x21]

```
[x23]
x24
  [x25]
  x 26]
  [x27]
  [x28]
  [x29]
[x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x000000000000000d
instruction = 0x00e00713
     320, CLK = 1, PC = 0x00000040
Time:
[x0]
 \times 1
 x2
 x3
 x4]
 x5]
x6]
 x7]
 x8]
 x9]
 x10] = 0x00000000000000000a
x11
  x12]
  x13]
  [x14]
  x15]
  [x16]
x17
  [x18]
  [x19]
  [x20]
  [x21]
  x22
  [x23]
  [x24]
  x25
  x 26]
  [x27]
x281
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000000e
instruction = 0x00f00793
```

```
[x0]
 [x1]
 [x2]
 [x3]
 [x4]
 x5]
 x6]
 \times 7
x8]
 [x9]
 [x11]
 x12]
 x13]
 \times 14
 x15]
 x16
 x17
 x18]
 [x19]
 x20
 \times 21
 x22
 x23
x24
 x25
 x26]
 x27
 x28
 [x29]
[x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x000000000000000 f
instruction = 0x01000813
    360, CLK = 1, PC = 0 \times 000000048
Time:
 = 0x0000000000000000
[x0]
[x1]
 x2
 x3
 \times 4
 x5]
 x6]
[x7]
 x81
 [x9] = 0x00000000000000009
[x11] = 0x0000000000000000
```

340, CLK = 1, PC =  $0 \times 000000044$ 

Time:

```
[x12]
[x13]
  [x14]
  x15]
  [x16]
  [x17]
  [x18]
  x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25
  [x26]
  \times 27
  [x28]
  x29
  [x31] = 0x000000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000010
instruction = 0x01100893
     380, CLK = 1, PC = 0 \times 00000004 c
Time:
[x0]
 [x2]
 [x3]
 \times 4
[x5]
 x6]
 [x7]
 x8]
[x9]
 [x10] = 0x00000000000000000a
[x11]
  = 0 \times 00000000000000000
[x12]
  [x13]
  [x14]
  x15]
  [x 1 6]
[x17]
  [x18]
  x19]
  [x20]
  [x21]
  [x22]
  [x23]
```

```
[x25] = 0x000000000000000000
[x26]
  [x27]
  x28
  [x29] = 0x00000000000000000
[x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000011
instruction = 0x01200913
     400, CLK = 1, PC = 0 \times 000000050
Time:
x2
 x3]
 x41
 x5
 x 6
 x7]
  x8]
 x9]
 \times 11
  x12
  x13
  \times 14
  x15]
  x16]
  x17]
  [x18]
  x19]
  [x20]
  \times 21
[x22]
  x23
  x24
  [x25]
  [x26]
  x27
  x28
  [x29]
[x30]
  ALUcontrol: 0x2
immediate: 0x0000000000000012
instruction = 0x01300993
Time:
     420, CLK = 1, PC = 0 \times 000000054
[x0] = 0x00000000000000000
```

```
x1]
 [x2]
 [x3]
 x4]
 [x5]
 x6]
 x7
 x8]
 x9]
 x10]
 [x11]
x12
 [x13]
 x14
 [x15]
 x 16]
 [x17]
 x18
 x19]
 x20]
 x21]
 x22
 x23
 x24
 x25
 x 26]
 x27
x28
 [x29]
 x30
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000013
instruction = 0x01400a13
    440, CLK = 1, PC = 0 \times 000000058
Time:
 [x0]
[x1]
 [x2]
 [x3]
 x4]
 x5]
 x6
 \times 7
x8]
 [x9]
 = 0 \times 00000000000000000
[x12]
```

```
[x14]
[x15]
 [x16]
 [x17]
 [x18]
 [x19]
 [x20]
 x21]
 x22
 x23]
 x24
 x25
 [x26]
 x27
 x28
 x29]
 [x30]
 [x31] = 0x000000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000014
instruction = 0x01500a93
    460, CLK = 1, PC = 0 \times 00000005 c
Time:
[x0]
 x2]
 x3
x4]
 x5]
 \times 6
\times 7
 x8]
 [x9]
 x10
[x11]
 [x12]
 x13]
 [x14]
 [x15]
 x16]
 = 0 \times 00000000000000011
\times 17
 [x18]
[x 1 9]
 [x20]
 x21
 [x22]
 x23
 [x24]
 [x25]
```

```
[x28] = 0x00000000000000000
[x29] = 0x00000000000000000
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000015
instruction = 0x01600b13
Time:
       480, CLK = 1, PC = 0 \times 000000060
[x0] = 0x0000000000000000
  \times 1
\times 2
  x3]
  x4]
  x5]
  x6]
  \times 71
  \times 81
  x9]
x11
   = 0 \times 00000000000000000
x12
   x13]
   x 14]
   x15]
   x 16]
x17]
   = 0 \times 0000000000000011
x18]
   x19]
   [x20]
   x21
   x22
   x23
x24
   x25
   x26
   [x27]
  [x28]
   x29
   [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000016
instruction = 0x01700b93
       500, CLK = 1, PC = 0 \times 000000064
Time:
```

```
[x3]
x4]
 [x5]
 x6]
 |x7|
 [x8]
 x9]
 = 0 \times 0000000000000000
\times 11
x12]
  [x13]
  x14
  [x15]
  x 16]
  [x17]
  = 0 \times 0000000000000011
x 18]
  [x19]
  x201
  x21
  x22
  [x23]
  x24
  x25
  x 26]
  x27
  x28]
  [x29]
[x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000017
instruction = 0x407302b3
     520, CLK = 1, PC = 0 \times 000000068
Time:
[x0]
 [x1]
 [x2]
 [x3] = 0x00000000000000003
| x 4 ]
 x5]
 x6]
 \times 7
 x81
 [x9]
[x11] = 0x0000000000000000
[x12]
  [x13]
  [x14]
```

```
[x16]
[x17]
  = 0 \times 0000000000000011
[x18]
  x19]
  [x20]
  = 0 \times 00000000000000014
[x21]
  x22
  x23
  [x24]
  x25]
  [x26]
  x27
  [x28]
  x29
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x6
immediate: 0x00000000000000000
instruction = 0x0062 ffb3
      540, CLK = 1, PC = 0 \times 00000006 c
Time:
[x0]
 [x1]
  x2
  [x3]
  x4]
  x5]
x6]
 x7
x8]
  [x9]
 x10] = 0x00000000000000000a
[x11]
  [x12]
  [x13]
  [x14]
  x15]
  [x16]
  [x17]
  = 0 \times 0000000000000011
[x18]
  x19]
  [x20]
  = 0 \times 00000000000000014
x21
  [x22]
  x23
  [x24]
  x25
  [x26]
  [x27]
```

```
[x30] = 0x00000000000000000
ALUcontrol: 0x0
immediate: 0x00000000000000000
instruction = 0x0055e533
       560, CLK = 1, PC = 0 \times 000000070
Time:
[x0] = 0x00000000000000000
x 1
  [x2]
  x3]
  x4
  x5]
  x6]
  x7]
  x8]
  \times 91
  x11]
  = 0 \times 00000000000000000
x12]
  x13]
  \times 14
  x15
  x 16]
  x17]
  = 0 \times 0000000000000011
x 18]
  x19]
  x20
  = 0 \times 00000000000000014
x21
  x22
  x23
  [x24]
  x25
x 26]
  x27
  x28
  [x29] = 0x000000000000000000
[x30] = 0x00000000000000000
ALUcontrol: 0x1
immediate: 0x00000000000000000
instruction = 0xfff37f13
       580, CLK = 1, PC = 0 \times 00000074
Time:
[x0] = 0x0000000000000000
  [x2]
  [x3]
  x4]
```

```
x5]
[x6]
 x7]
 x8]
 [x9]
 [x11]
  = 0 \times 0000000000000000
x12
  x13]
  x14
  [x15]
  x 16]
  [x17]
  = 0 \times 0000000000000011
x18]
  [x19]
  x20]
  = 0 \times 00000000000000014
[x21]
  x22
  x23
  x24
  x 25]
  x26
  x27
  x28
  x29]
  x30]
  ALUcontrol: 0x0
immediate: 0xffffffffffffffff
instruction = 0x0045a023
     600, CLK = 1, PC = 0x00000078
Time:
[x0] = 0x00000000000000000
 [x1]
[x2]
 [x3]
 x4]
 [x5]
 = 0x ffffffffffffffff
x6]
 [x7]
 x8]
 x 9]
[x11]
x12
  |x13|
  x14
  [x15]
  [x16]
  = 0 \times 0000000000000011
```

```
[x18]
[x19]
  [x20]
  = 0 \times 00000000000000014
[x21]
  [x22]
  [x23]
  [x24]
  x25]
  x 26]
  x27
  [x28]
  [x29]
  [x30] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x0005a603
     620, CLK = 1, PC = 0 \times 00000007 c
Time:
 [x0]
 x2
 x3
 \times 4
 x5
 = 0x ffffffffffffffff
x6]
 x 7
x8]
 [x9]
 [x11]
  = 0 \times 00000000000000000
x12]
  [x13]
  [x14]
[x15]
  [x16]
  [x17]
  = 0 \times 0000000000000011
[x18]
  [x19]
  [x20]
  = 0 \times 00000000000000014
[x21]
  [x22]
x23
  [x24]
  x25
  [x26]
  x27
  [x28]
  [x29]
```

```
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00760833
Time:
        640, CLK = 1, PC = 0 \times 000000080
[x0] = 0x00000000000000000
  [x1]
  \times 2
x3]
  [x4]
x5]
  = 0x ffffffffffffffff
[x6] = 0x00000000000000000
x7]
  x8
  [x9] = 0x00000000000000000
x11]
   = 0 \times 00000000000000000
x12
   x13]
   x14]
   x 15]
   x 16]
   \times 17
   = 0 \times 00000000000000011
x18]
   x 19]
   [x20]
   = 0 \times 00000000000000014
x21
   [x22] = 0x00000000000000016
x23
   x24
   x25
[x26]
   [x27]
[x28]
   [x29]
   [x30] = 0x000000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00b82023
        660, CLK = 1, PC = 0x00000084
Time:
[x0] = 0x0000000000000000
  \times 1
[x2] = 0x00000000000000000
x3
  |x4|
  x51
  = 0x ffffffffffffffff
```

```
|x7|
[x8]
 [x9]
 [x10] = 0xfffffffffffffff
[x11]
  = 0 \times 00000000000000000
[x12]
  [x13]
  [x14]
  [x15]
x16]
  [x17]
  = 0 \times 0000000000000011
x18]
  [x19]
  x20
  = 0 \times 00000000000000014
[x21]
  x22
  [x23]
  x24
  x25
  x 26]
  [x27]
  x28
  [x29]
  x30
  ALUcontrol: 0x2
```

immediate: 0x00000000000000000

```
680, CLK = 1, PC = 0 \times 000000084
Time:
[x0]
  [x1]
  x2
  [x3]
  [x4]
x5]
  = 0x ffffffffffffffff
x6]
  [x7]
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
  [x12]
[x13]
  [x14]
x15]
  [x16]
  x17
  = 0 \times 0000000000000011
[x18]
  [x19]
  = 0 \times 00000000000000014
```

```
[x21]
[x22]
  [x23]
  [x24]
  [x25]
  [x26]
  [x27]
  x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00082903
      700, CLK = 1, PC = 0x00000088
Time:
[x0]
 \times 1
  x3]
  x4]
 = 0x ffffffffffffffff
\times 5
x6]
  x7]
  x8]
  x 9]
 [x10] = 0xfffffffffffffff
x11]
  [x12]
  x13]
  [x14]
  x15]
[x16]
  = 0 \times 0000000000000011
[x17]
[x18]
  [x19]
  [x20]
  = 0 \times 00000000000000014
[x21]
  [x22]
  [x23]
  x24
  [x25]
x 26]
  [x27]
x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
```

immediate: 0x00000000000000000

```
instruction = 0x007908b3
Time:
       720, CLK = 1, PC = 0 \times 00000008 c
  [x0]
  [x1]
[x2]
  [x3]
  x4]
  = 0x ffffffffffffffff
x5
x6]
  x7
x8
  [x9] = 0x00000000000000000
[x11]
  = 0 \times 00000000000000000
x12
  [x13]
  x14
  x15]
  x 16]
  [x17]
  = 0 \times 0000000000000011
x18]
  x19]
  x20]
  = 0 \times 00000000000000014
x21
  x22
  [x23]
  x24
  [x25]
  x 26]
  [x27]
  x28
[x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x0049a223
       740, CLK = 1, PC = 0 \times 000000090
Time:
[x0] = 0x00000000000000000
  [x1]
[x2]
  [x3]
  x4]
[x5] = 0x ffffffffffffffff
  x6]
[x7]
  [x8]
  [x9]
```

```
= 0x ffffffffffffffff
[x10]
  [x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
  = 0 \times 00000000000000011
[x17]
[x18]
  x19]
  [x20]
  = 0 \times 00000000000000014
x21
  [x22]
  x23
  [x24]
  x25
  [x26]
  x27
  x29
  [x30]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
```

```
760, CLK = 1, PC = 0x00000090
Time:
[x0]
  |x1|
  [x2]
  x3]
  [x4]
  = 0x ffffffffffffffff
x5]
  [x6]
  x7]
x8]
  [x9]
  [x10] = 0xffffffffffffffff
[x11]
  [x12]
  [x13]
  [x14]
  [x15]
[x16]
  = 0 \times 00000000000000000
  = 0 \times 0000000000000011
[x17]
x18]
  |x19|
  [x20]
  = 0 \times 00000000000000014
[x21]
  [x22]
```

```
[x25]
   [x26] = 0x00000000000000000
[x27]
   [x28] = 0x000000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x00000013
        780, CLK = 1, PC = 0 \times 00000094
Time:
x1]
  x2
  x3]
  \times 4
  x5
  = 0x ffffffffffffffff
x6]
  x7]
  x8
x 9]
  x11]
   = 0x000000000000000
x12
   \times 13
   x 14]
   [x15]
   x16]
   = 0 \times 0000000000000000
[x17]
   = 0 \times 0000000000000011
x18]
   = 0 \times 0000000000000000
[x19]
   [x20]
   = 0 \times 00000000000000014
[x21]
   x22
   [x23]
   [x25]
   x 26]
   \times 27
   x28
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

Time: 800, CLK = 1, PC = 0x00000098

```
[x0]
  [x1]
[x2]
  [x3]
  |x4|
  [x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  x13]
  [x14]
  x 15]
  [x16]
  = 0 \times 00000000000000000000
x17
  = 0 \times 00000000000000011
x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  = 0 \times 00000000000000014
x21
  x22
  x23
  x24
  x25]
  \times 26
  x27
  [x28]
  x29
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x0049aa03
       820, CLK = 1, PC = 0 \times 00000009 c
Time:
[x1]
  [x2]
  [x3]
  \times 4
  = 0x ffffffffffffffff
x5]
x6]
  x 7
[x8]
  x9
  [x10] = 0x ffffffffffffffff
[x11] = 0x0000000000000000
```

```
[x13]
  [x14]
[x15]
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  = 0 \times 00000000000000014
[x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x014ba223
       840, CLK = 1, PC = 0 \times 0000000 = 0
Time:
[x0]
  x1]
  [x2]
  [x3]
  x4]
  = 0x fffffffffffffffff
x5]
[x6]
  x7
  x8]
  [x9]
[x10] = 0xfffffffffffffff
  [x11]
x12]
  [x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
x 16]
[x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  x20
  = 0 \times 00000000000000014
[x21]
  x22
  [x23]
  [x24]
```

```
[x26] = 0x000000000000000000
   [x27]
x28]
  [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x00000013
Time:
       860, CLK = 1, PC = 0x0000000a4
[x0]
  [x1]
  x2
  [x3]
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8
  x10] = 0x fffffffffffffffff
x11]
   = 0 \times 00000000000000000
x12]
   x13]
   x 14]
   x15
   x 16]
   [x17]
x18]
   = 0 \times 0000000000000000
[x19]
   x20
   = 0 \times 00000000000000014
[x21]
   x22
   [x23]
   x24
   x25
   [x26]
  [x27]
   [x28]
   [x29]
[x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       880, CLK = 1, PC = 0x0000000a8
```

```
[x2]
[x3]
  | x 4 ]
  = 0x ffffffffffffffff
x5]
[x6]
  x7]
  x8
  x9]
  [x10] = 0xffffffffffffff
x11]
   = 0 \times 00000000000000000
[x12]
   x13]
   [x14]
   x15]
   [x16]
   = 0 \times 00000000000000000000
\times 17
   [x18]
   = 0 \times 00000000000000000000
x19
   x20
   = 0 \times 00000000000000014
x21
   [x22]
   x23
   x24
   x25
   x 26]
   x27
   [x28]
x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x004bad83
       900, CLK = 1, PC = 0x0000000 ac
Time:
[x0] = 0x00000000000000000
[x1]
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
[x5]
x6]
  \times 7
  x8]
[x9] = 0x00000000000000009
[x10] = 0xfffffffffffffff
[x11]
   = 0 \times 0000000000000000
[x12]
   [x13]
```

```
[x15]
[x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x004baa83
Time:
     920, CLK = 1, PC = 0x0000000b0
[x0]
 x 1
 x2
 x3]
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 x7]
 [x8]
 x9]
[x10] = 0xfffffffffffffff
  [x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000000
[x17]
  = 0 \times 00000000000000000000
[x18]
  [x19]
[x20]
  [x21]
x22
  [x23]
  x24
  [x25]
  [x26]
```

```
[x29] = 0x00000000000000000
[x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x013aa223
Time:
      940, CLK = 1, PC = 0x0000000b4
[x0]
  [x1]
x2
  [x3]
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  x7
  x81
x9]
  x11] = 0x0000000000000000b
x12
  x13]
  \times 14
  x15]
  x 16]
  \times 17
x 18]
  [x19]
  x20
  [x21]
  x22
  [x23]
  x24
[x25]
  x26]
  x27
  [x28]
  [x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x004aac83
Time:
      960, CLK = 1, PC = 0x0000000b8
[x1]
  [x2]
```

```
|x4|
  x5]
  = 0x fffffffffffffffff
[x6]
  \times 7
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
[x12]
  x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000000
x 19]
  [x20]
  x21]
  x22
  x23
  [x24]
  x25
  [x26]
  x27
  x28
  x29]
  [x30]
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x0000000000000004
instruction = 0x017b0c33
       980, CLK = 1, PC = 0 \times 0000000 bc
Time:
  [x0]
[x1]
  |x2|
  x3]
  |x4|
[x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
x9]
  [x10] = 0xffffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  [x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
```

```
[x17] = 0x0000000000000012
[x18] = 0x0000000000000000
[x19] = 0x00000000000000013
[x20] = 0x00000000000000004
[x21] = 0x00000000000000015
[x22]
    x23] = 0x0000000000000017
[x25] = 0x00000000000000000
x26] = 0x00000000000000000
[x27] = 0x000000000000000004
x28] = 0x00000000000000000
[x29] = 0x000000000000000000
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
INFO: [USF-XSim-96] XSim completed. Design snapshot '
  testbench_behav' loaded.
INFO: [USF-XSim-97] XSim simulation ran for 1000ns
launch\_simulation: Time (s): cpu = 00:00:03; elapsed = 00:00:11
   . Memory (MB): peak = 1844.254; gain = 0.000
run all
           1000, CLK = 1, PC = 0 \times 0000000 c0
Time:
[x0] = 0x00000000000000000
[x1]
    [x3] = 0x00000000000000000
[x4] = 0x000000000000000004
[x5] = 0xfffffffffffffff
[x6] = 0x0000000000000000
[x7] = 0x0000000000000007
[x8] = 0x00000000000000008
[x9] = 0x00000000000000009
[x11] = 0x0000000000000000
[x12]
    x13]
    \times 14
x16]
    = 0 \times 00000000000000000
[x17]
    x18]
    [x19] = 0x00000000000000013
[x20] = 0x00000000000000004
[x21] = 0x000000000000000004
[x22] = 0x00000000000000016
[x23] = 0x00000000000000017
```

```
[x25]
   [x26] = 0x00000000000000000
[x27]
   [x28] = 0x000000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1020, CLK = 1, PC = 0 \times 0000000 \times 4
Time:
[x0]
  x1]
  x2]
  x3]
  \times 4
  x5
  = 0x ffffffffffffffff
x6]
  x7]
  x8
x 9]
  x11]
   = 0x000000000000000
x12
   \times 13
   x 14]
   [x15]
   x16]
   = 0 \times 0000000000000000
[x17]
   x18]
   = 0 \times 0000000000000000
[x19]
   [x20]
   [x21]
   x22
   [x23]
   [x24] = 0x000000000000000000
[x25]
   x 26]
   \times 27
   x28
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

Time: 1040, CLK = 1, PC = 0x0000000c8

```
[x1]
|x2|
  [x3]
  |x4|
  [x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  x13]
  [x14]
  x15
  [x16]
  = 0 \times 00000000000000000000
x17
  x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21
  x22
  x23
  x24
  x25]
  [x26]
  x27
  [x28]
  x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1060, CLK = 1, PC = 0 \times 0000000 cc
Time:
[x1]
  [x2]
  [x3]
x4]
  x5]
  = 0x ffffffffffffffff
x6]
  \times 7
[x8]
  x9
  [x10] = 0x ffffffffffffffff
[x11] = 0x0000000000000000
```

[x0]

```
[x13]
  [x14]
|x15|
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1080, CLK = 1, PC = 0 \times 0000000 d0
Time:
[x0]
 x1]
  [x2]
  [x3]
 x4]
 = 0x fffffffffffffffff
x5]
[x6]
 x7
  x8]
 [x9]
[x10] = 0xfffffffffffffff
  [x11]
x12]
  [x13]
  [x14]
  [x15]
  x 16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  [x24]
```

```
[x26] = 0x000000000000000000
  [x27]
x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1100, CLK = 1, PC = 0 \times 0000000 d4
Time:
[x0] = 0x00000000000000000
[x1]
  x2
  [x3]
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8
  x10] = 0x fffffffffffffffff
\times 11
  x12]
  x13]
   x 14]
  x15
  x 16]
  [x17]
x18]
  = 0 \times 0000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  x 25]
  [x26]
  [x27]
  [x28]
  [x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1120, CLK = 1, PC = 0 \times 0000000 d8
```

```
[x2]
x3]
  [x4]
  x5]
  = 0x ffffffffffffffff
[x6]
  x7]
  x8
  x9]
  [x10] = 0xffffffffffffff
x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  x15
   [x16]
   = 0 \times 00000000000000000000
\times 17
   [x18]
   = 0 \times 00000000000000000000
x19
   x20
   x21
   [x22]
  x23
  x24
   x25
  x 26]
   x27
   [x28]
x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1140, CLK = 1, PC = 0 \times 0000000 \, dc
Time:
[x0] = 0x00000000000000000
[x1]
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
[x5]
x61
  \times 7
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
```

[x13]

```
[x15]
[x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
     1160, CLK = 1, PC = 0 \times 0000000 = 0
[x0]
  x 1
  x2
  x3]
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  x7]
  [x8]
  x9]
[x10] = 0xfffffffffffffff
[x11]
  = 0 \times 00000000000000000
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000000
[x17]
  = 0 \times 00000000000000000000
[x18]
  [x19]
[x20]
  [x21]
x22
  [x23]
  x24
  [x25]
  [x26]
```

```
[x29] = 0x00000000000000000
[x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
      1180, CLK = 1, PC = 0 \times 0000000 = 4
[x0]
  [x1]
x2
  [x3]
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x81
  x9]
  [x11] = 0x0000000000000000
x12
  x13]
  \times 14
  x15]
  x 16]
  = 0 \times 00000000000000000
  \times 17
x18]
  [x19]
  x20
  [x21]
  x22
  [x23]
  [x24]
[x25]
  x26]
  [x27]
  [x28] = 0x000000000000000000
[x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
      1200, CLK = 1, PC = 0 \times 00000000 = 8
[x2]
```

```
|x4|
  x5]
  = 0x fffffffffffffffff
[x6]
  \times 7
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
[x12]
  x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000000
x 19]
  [x20]
  x21
  x22
  x23
  [x24]
  x25
  [x26]
  x27
  x28
  x29]
  [x30]
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1220, CLK = 1, PC = 0 \times 0000000 ec
Time:
  [x0]
[x1]
  |x2|
  x3]
  |x4|
[x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
x9]
  [x10] = 0xffffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
```

```
[x17]
[x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  [x25]
  x26]
  [x27]
  x28
  [x29]
  [x30]
  [x31]
  ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1240, CLK = 1, PC = 0 \times 0000000 f0
Time:
[x0]
  x 1]
  x2
  x3]
  x4]
  = 0x ffffffffffffffff
x5]
  = 0x00000000000000006
x 6
x7]
  x81
  x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
x 1 1 ]
[x12]
  [x13]
[x14]
  [x15]
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  [x28]
```

```
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1260, CLK = 1, PC = 0 \times 0000000 \, f4
Time:
[x0] = 0x00000000000000000
  \times 1
x2
  [x3]
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
  [x9] = 0x00000000000000009
x10] = 0x fffffffffffffffff
x11] = 0x0000000000000000b
x12
  x13]
  x14
  x15
  x 16]
  x17
  x18
x19]
  [x20] = 0x00000000000000004
x21
  x22
  x23
x24
  [x25]
  x26
[x27]
  [x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1280, CLK = 1, PC = 0x0000000f8
Time:
[x3]
  [x4]
  = 0x fffffffffffffffff
```

[x30] = 0x0000000000000000

```
[x6]
  [x7]
[x8]
  [x9]
  [x10] = 0x ffffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  \times 21
  [x22]
  x23
  x24
  x25
  [x26]
  x27
  [x28]
  x29]
  [x30]
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1300, CLK = 1, PC = 0 \times 0000000 fc
Time:
  [x0]
  [x2]
[x3]
  [x4]
x5]
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8
  x9]
[x10] = 0xfffffffffffffff
[x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  = 0 \times 00000000000000000000
```

```
[x19]
[x20]
  [x21]
  [x22]
  [x23]
  [x24]
  [x25]
  x 26]
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1320, CLK = 1, PC = 0 \times 00000100
Time:
[x0]
 \times 1
x2]
 x3
 \times 4
x5]
 = 0x ffffffffffffffff
x6]
 x7
 x8]
x9]
 [x10] = 0xffffffffffffffff
  = 0 \times 0000000000000000
\times 11
[x12]
  [x13]
[x14]
  [x15]
[x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
x24
  [x25]
[x26]
  [x27]
  x28
  [x29]
  [x30]
```

```
immediate: 0x00000000000000000
instruction = 0x00000013
        1340, CLK = 1, PC = 0 \times 00000104
Time:
[x0] = 0x00000000000000000
   [x1]
   x2
[x3] = 0x00000000000000003
x4]
  x5]
  = 0x ffffffffffffffff
x6]
   [x7] = 0x00000000000000000
x8]
   [x9] = 0x00000000000000000
x11] = 0x0000000000000000b
x12] = 0x000000000000000004
x13]
   x 14]
   x15]
   x 16]
   = 0x000000000000000
\times 17
   x18]
   x19]
[x20] = 0x00000000000000004
[x21]
   x22] = 0x000000000000000000
[x23] = 0x00000000000000017
   x24
   [x25]
x26] = 0x00000000000000000
[x27]
   [x28] = 0x00000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
        1360, CLK = 1, PC = 0 \times 00000108
[x1]
   \times 2
[x3] = 0x00000000000000003
x4
  [x5]
  = 0x ffffffffffffffff
[x6]
```

ALUcontrol: 0x2

```
[x8] = 0x00000000000000008
[x9]
  [x10] = 0xffffffffffffffff
[x11]
  = 0 \times 0000000000000000
[x12]
  [x13]
  [x14]
  x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21]
  [x22]
  x23
  [x24]
  x25]
  x26
  x27
  [x28]
  x29]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1380, CLK = 1, PC = 0 \times 0000010 c
Time:
[x0]
 [x1]
  x2
  [x3]
  [x4]
x5]
  = 0x ffffffffffffffff
x6]
  x7
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
  [x12]
[x13]
  [x14]
x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
```

```
[x21]
[x22]
  [x23]
  [x24]
  [x25]
  [x26]
  [x27]
  x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1400, CLK = 1, PC = 0 \times 00000110
Time:
[x0]
 \times 1
  x3
  x4]
  = 0x ffffffffffffffff
\times 5
x6]
  x7]
  x8]
  x 9]
  x11]
  [x12]
  x13]
  [x14]
  x15]
[x16]
  = 0 \times 00000000000000000000
  [x17]
[x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  [x25]
x 26]
  [x27]
x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
```

```
instruction = 0x00000013
Time:
      1420, CLK = 1, PC = 0 \times 00000114
[x0] = 0x00000000000000000
[x2]
  [x3]
  x4]
  = 0x ffffffffffffffff
x5
x6]
  x7
x8
  [x9] = 0x0000000000000000
[x11]
  = 0 \times 0000000000000000
x12
  [x13]
  x14
  x15]
  x 16]
  = 0 \times 00000000000000000
[x17]
  x18]
  = 0 \times 0000000000000000
x19]
  x20]
  x21
  x22
  x23
  x24
  [x25]
  x 26]
  [x27]
  x28
[x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1440, CLK = 1, PC = 0 \times 00000118
Time:
[x0] = 0x00000000000000000
  [x1]
[x2]
  [x3]
  x4]
[x5] = 0x ffffffffffffffff
  x6]
[x7]
  [x8]
  [x9]
```

```
= 0x ffffffffffffffff
[x10]
  = 0 \times 00000000000000000000
[x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 0000000000000000
  [x17]
  [x18]
x19]
  [x20]
  x21
  [x22]
  x23
  [x24]
  x25
  [x26]
  x27
  x29]
  [x30]
  [x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1460, CLK = 1, PC = 0 \times 0000011 c
Time:
[x0] = 0x0000000000000000
  [x1]
x2
  [x3]
  \times 4
[x5]
  = 0x ffffffffffffffff
  x6]
\times 7
  [8x
  x9]
  [x10] = 0xfffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
[x15]
  [x16]
  x17
  |x18|
  [x19]
  = 0 \times 00000000000000013
[x20]
  [x21]
```

```
[x23]
x24
  [x25]
  x 26]
  [x27]
  [x28]
  [x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1480, CLK = 1, PC = 0x00000120
Time:
[x0]
 \times 1
  x2
  x3
  x4]
  = 0x ffffffffffffffff
\times 5
x6]
  x7]
  x8]
  x9]
  x11]
  x12]
  x13]
  [x14]
  x15]
  [x16]
x17
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  [x24]
  x25
  x 26]
  [x27]
x281
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

```
[x1]
  [x2]
  [x3]
  [x4]
  x5
  = 0x ffffffffffffffff
  x6]
  x7]
x8]
  [x9]
  [x11] = 0x0000000000000000
x12]
  [x13]
  \times 14
  [x15]
  x16]
  = 0 \times 00000000000000000
\times 17
  x18]
  = 0 \times 00000000000000000
[x19]
  x20
  \times 21
  x22
  x23
  x24
  x25
x26]
  x27
  x28
  [x29]
  [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1520, CLK = 1, PC = 0 \times 00000128
Time:
[x0]
  [x1]
  [x2]
[x3] = 0x0000000000000000
  \times 4
  = 0x ffffffffffffffff
x5
  x6]
[x7]
  x81
  [x9] = 0x00000000000000000
[x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
```

1500, CLK = 1, PC =  $0 \times 00000124$ 

Time:

[x0]

```
[x12]
[x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25
  [x26]
  \times 27
  [x28]
  x29]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1540, CLK = 1, PC = 0 \times 0000012 c
Time:
[x0]
 [x2]
 [x3]
  \times 4
[x5]
 = 0x ffffffffffffffff
  x6]
 [x7]
 x8]
[x9]
 [x10] = 0xfffffffffffffff
x11]
  = 0 \times 00000000000000000
[x12]
  [x13]
  [x14]
  [x15]
  [x 1 6]
[x17]
  [x18]
  x19]
  [x20]
  [x21]
  [x22]
  [x23]
```

```
[x25] = 0x000000000000000000
x 26]
  [x27]
  [x28]
  [x29] = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1560, CLK = 1, PC = 0x00000130
Time:
x2
  x3
  x41
  x5
  = 0x ffffffffffffffff
x 6
  x7]
  x8]
  x9]
  [x10] = 0xffffffffffffffff
\times 11
  x12]
  x13]
  \times 14
  x 15]
  x 16]
  = 0 \times 00000000000000000
x17]
   [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  [x21]
[x22]
   x23
  x24
  [x25]
  [x26]
  x27
  x28
  [x29]
[x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
       1580, CLK = 1, PC = 0 \times 00000134
```

```
|x1|
 [x2]
 [x3]
 x4]
 [x5]
 = 0x ffffffffffffffff
[x6]
 x7
 x8]
 x9]
 = 0 \times 00000000000000000
[x11]
x12
  [x13]
  x14
  [x15]
  x 16]
  [x17]
  x18
  = 0 \times 00000000000000000
x19]
  x20]
  [x21]
  x22
  x23
  x24
  x25
  x 26]
  x27
x28]
  [x29]
  x30
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1600, CLK = 1, PC = 0 \times 00000138
Time:
 [x0]
[x1]
 [x2]
 [x3]
 [x4]
 = 0x ffffffffffffffff
x5]
 x6
 x7
 x8]
[x9] = 0x000000000000000009
[x10] = 0xfffffffffffffff
  [x12]
```

```
[x14]
[x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  x21]
  [x22]
  x23]
  [x24]
  x25
  [x26]
  x27
  x28
  x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1620, CLK = 1, PC = 0 \times 0000013 c
Time:
[x0]
 x2]
 [x3]
x4]
 = 0x ffffffffffffffff
x5
 x6]
\times 7
 x8]
 [x9]
 [x10] = 0xffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 0000000000000000
  [x17]
  = 0 \times 0000000000000000
[x18]
[x19]
  [x20]
  x21
  [x22]
  x23
  [x24]
  [x25]
```

```
[x28] = 0x00000000000000000
[x29] = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
       1640, CLK = 1, PC = 0 \times 00000140
[x0] = 0x0000000000000000
  \times 1
[x2]
  x3
  \times 4
x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
  x 9]
x11
  x12
   x13]
   x 14]
   x15]
   = 0 \times 00000000000000000
x 16]
x17]
   = 0 \times 00000000000000000
x18]
x19]
   [x20]
   x21
   [x22]
   x23
x24
   x25
   x26
   [x27]
  [x28]
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1660, CLK = 1, PC = 0 \times 00000144
Time:
```

[x27] = 0x00000000000000004

```
[x3]
  x4]
  [x5]
  = 0x ffffffffffffffff
x6]
  |x7|
  [x8]
  [x9]
  [x10] = 0xfffffffffffffff
  [x11]
x12]
  [x13]
  [x14]
  [x15]
  x 16]
  = 0 \times 00000000000000000000
[x17]
  x 18]
  [x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  x27
  x28]
  [x29]
[x30] = 0x00000000000000000
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1680, CLK = 1, PC = 0 \times 00000148
Time:
[x0]
  [x1]
  [x2]
  [x3] = 0x00000000000000003
| x 4 ]
  x5]
  = 0x ffffffffffffffff
  x6]
  \times 7
  x81
  [x9]
[x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
[x12]
  [x13]
  [x14]
```

```
[x16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25]
  [x26]
  x27
  [x28]
  [x29]
  [x30]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
    1700, CLK = 1, PC = 0 \times 0000014 c
Time:
[x0]
 [x1]
 x2
 [x3]
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 x7
x8]
 [x9]
 [x11]
  [x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 0000000000000000
x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  [x25]
  [x26]
  [x27]
```

```
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
        1720, CLK = 1, PC = 0 \times 00000150
Time:
[x0] = 0x00000000000000000
[x1]
  [x2]
  x3
  [x4] = 0x000000000000000004
x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x8]
  \times 91
  x10 = 0x ffffffffffffffff
x11]
   = 0 \times 00000000000000000
x12]
   x13]
   \times 14
   x15
   x 16]
   = 0 \times 0000000000000000
x17]
   x 18]
   = 0 \times 00000000000000000
x19]
   [x20]
   x21
   x22
   x23
   [x24]
   [x25]
[x26]
   [x27]
   x28
   [x29] = 0x000000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
        1740, CLK = 1, PC = 0 \times 00000154
Time:
[x0] = 0x0000000000000000
  [x2]
  [x3]
  [x4]
```

```
[x7]
  [8 x
  [x9] = 0x00000000000000000
[x10] = 0x fffffffffffffff
[x11]
  = 0 \times 00000000000000000
x12
  [x13]
  x14
  [x15]
  x 16]
  = 0 \times 00000000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
  x22
  x23
  x24
  [x25]
  x26
  [x27]
  x28
  x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
instruction = 0x00000013
      1760, CLK = 1, PC = 0 \times 00000158
Time:
[x0] = 0x00000000000000000
  [x1]
[x2]
  [x3]
  x4]
  |x5|
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8]
  x 9
[x10] = 0xfffffffffffffff
  = 0 \times 00000000000000000
[x11]
[x12]
  \lfloor x 1 3 \rfloor
  x14
  [x15]
  [x16]
  = 0 \times 00000000000000000
```

|x5|

[x6]

= 0x ffffffffffffffff

```
[x18]
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  [x24]
  x25]
  x 26]
  x27
  [x28]
  [x29]
  [x30] = 0x00000000000000000
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     1780, CLK = 1, PC = 0 \times 0000015 c
Time:
 [x0]
 x2
 x3
 \times 4
 x5]
 = 0x ffffffffffffffff
x6]
 x 7
 x8]
 [x9]
 [x10] = 0xffffffffffffff
[x11]
  = 0 \times 00000000000000000
x12]
  [x13]
  [x14]
[x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  [x21]
  [x22]
x23
  [x24]
x25
  [x26]
  x27
  [x28]
  [x29]
```

```
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
        1800, CLK = 1, PC = 0 \times 00000160
[x0] = 0x00000000000000000
  [x1]
  \times 2
x3]
  x4
x5]
  = 0x ffffffffffffffff
[x6] = 0x00000000000000000
x7]
  x8
  [x9] = 0x00000000000000000
x11]
   = 0 \times 00000000000000000
x12
   x13]
   x14]
   x 15]
   x 16]
   = 0 \times 00000000000000000
\times 17
   x18]
   = 0 \times 00000000000000000000
x 19]
   x20]
   x21
   [x22] = 0x00000000000000016
x23
   x24
   x25
[x26]
   [x27]
[x28]
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
        1820, CLK = 1, PC = 0 \times 00000164
Time:
[x0] = 0x0000000000000000
  \times 1
[x2] = 0x00000000000000000
x3
  |x4|
  x51
  = 0x ffffffffffffffff
```

```
[x8]
  [x9]
  [x10] = 0xfffffffffffffff
[x11]
  [x12]
   [x13]
  [x14]
  [x15]
x16]
  = 0 \times 00000000000000000
[x17]
   x18]
   = 0 \times 00000000000000000000
[x19]
  x20
   [x21]
   x22
   [x23]
   x24
   x25
   x 26]
   [x27]
  x28
  [x29]
  x30]
  [x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1840, CLK = 1, PC = 0 \times 00000168
Time:
  = 0x0000000000000000
[x0]
  [x1]
[x2]
  [x3]
x 4]
  = 0x ffffffffffffffff
[x5]
x6]
  |x7|
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 0000000000000000
[x11]
[x12]
  [x13]
x14
  [x15]
  [x16]
  = 0 \times 0000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000
   = 0 \times 00000000000000013
```

[x7]

```
[x20]
[x21]
  [x22]
  [x23]
  [x24]
  [x25]
  x 26]
  x27
  [x28]
  x29
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
     1860, CLK = 1, PC = 0 \times 0000016 c
[x0]
 x2
 x3]
 \times 4
x5
 = 0x ffffffffffffffff
x6]
 [x7]
 x8]
 x9]
 [x11] = 0x0000000000000000
x12]
  [x13]
  x14
[x15]
  [x16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  [x25]
  [x26]
  x27
  [x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
```

```
immediate: 0x00000000000000000
instruction = 0x00000013
      1880, CLK = 1, PC = 0 \times 00000170
Time:
[x0] = 0x0000000000000000
  [x2]
  x3
[x4] = 0x00000000000000004
x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x8]
  x9]
  [x10] = 0xffffffffffffffff
\times 11
  = 0 \times 00000000000000000
x12]
  x13
  x15]
  x 16]
  x17]
  x 18]
  = 0 \times 00000000000000000
x19]
  x20
x21
  x22
  x23
  x24
  x25
  [x26]
  x27
[x28]
  [x29] = 0x00000000000000000
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1900, CLK = 1, PC = 0 \times 00000174
Time:
[x1]
[x2]
  x3]
  [x4] = 0x00000000000000004
  x5
  [x6]
  [x7]
```

```
[x9] = 0x00000000000000009
[x10]
  = 0x ffffffffffffffff
[x11]
  [x12]
  [x13]
  [x14]
  [x15]
  x16]
  = 0 \times 00000000000000000
  [x17]
x18]
  = 0 \times 00000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  x27
  x28]
  [x29]
  x30]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1920, CLK = 1, PC = 0 \times 00000178
Time:
[x0]
  \times 1
[x2]
  x3
[x4]
  = 0x ffffffffffffffff
[x5]
[x6]
  x7]
  x8]
  [x9] = 0x00000000000000000
[x10] = 0xffffffffffffffff
[x11]
  = 0 \times 00000000000000000
x12
  [x13]
[x14]
  [x15]
  x16]
  = 0 \times 00000000000000000
[x17]
  x18]
  = 0 \times 0000000000000000
[x19]
  [x20]
```

```
[x22]
[x23]
  [x24]
  x25]
  [x26] = 0x00000000000000000
[x27]
  [x28]
  x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      1940, CLK = 1, PC = 0 \times 0000017 c
Time:
[x0] = 0x00000000000000000
x 1]
  x2
  x3
  \times 4
x5]
  = 0x ffffffffffffffff
  x6]
\times 7
  x8]
  x9
[x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
x12]
  [x13]
  x14
  [x15]
  = 0 \times 00000000000000000
x16]
[x17]
   [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  x25
  [x26]
x27
  [x28]
  x29]
  [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

```
1960, CLK = 1, PC = 0 \times 00000180
Time:
[x0] = 0x0000000000000000
  [x1]
|x2|
  [x3]
  [x4]
  = 0x fffffffffffffffff
x5
  x 6
x7
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
   = 0 \times 00000000000000000
[x12]
   x13]
   [x14]
   x15
   x 16]
   = 0 \times 00000000000000000
\times 17
   [x18]
   = 0 \times 00000000000000000
x19]
   [x20]
   x21
   x22
   x23
   x24
x25
   [x26]
   x27
   [x28]
   x29
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       1980, CLK = 1, PC = 0 \times 00000184
Time:
[x0]
  [x1]
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
x5]
[x6]
  \times 7
[x8]
  [x9] = 0x00000000000000000
[x10] = 0x fffffffffffffff
```

```
[x11]
[x12]
  |x13|
 x14
  [x15]
 [x16]
  = 0 \times 00000000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000
[x19]
 x20]
 [x21]
 x22
  [x23]
 x24
  [x25]
  x26
  [x27]
  x281
  x29
  [x30]
[x31] = 0x00000000000000000
```

ALUcontrol: 0x2

immediate: 0x00000000000000000

instruction = 0x00000013

```
2000, CLK = 1, PC = 0 \times 00000188
Time:
[x0] = 0x0000000000000000
 [x1]
x2
 [x3]
 \times 4
[x5]
 = 0x ffffffffffffffff
 x6]
[x7]
 x81
 x9]
 [x10] = 0xfffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
[x15]
  [x16]
  [x17]
  [x18]
  [x19]
  [x20]
  [x21]
```

```
[x23]
  x24
  [x25]
  x 26]
  [x27]
  [x28]
  [x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     2020, CLK = 1, PC = 0x0000018c
Time:
[x0]
 \times 1
  x2
  x3
  x4]
  = 0x ffffffffffffffff
\times 5
x6]
  x7]
  x8]
  x9]
  x11]
  x12]
  x13]
  [x14]
  x15]
  [x16]
x17
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  [x24]
  x25
  x 26]
  [x27]
x281
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

```
[x1]
  [x2]
  [x3] = 0x00000000000000003
[x4]
  x5
  = 0x ffffffffffffffff
  x6]
  x7]
x8]
  [x9]
  [x11] = 0x0000000000000000
x12
   [x13]
  \times 14
  x15]
   x16]
   = 0 \times 00000000000000000
\times 17
   x18]
   = 0 \times 00000000000000000000
[x19]
  x20
  \times 21
  x22
  x23
  x24
  x25
x26]
  [x27]
  x28
  [x29]
[x30] = 0x00000000000000000
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2060, CLK = 1, PC = 0 \times 00000194
Time:
[x0]
  [x1]
  [x2]
[x3] = 0x0000000000000000
  \times 4
  = 0x ffffffffffffffff
x5
  x6]
[x7]
  x81
  [x9] = 0x00000000000000000
[x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
```

2040, CLK = 1, PC =  $0 \times 00000190$ 

Time:

[x0]

```
[x12]
[x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25
  [x26]
  \times 27
  [x28]
  x29
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     2080, CLK = 1, PC = 0 \times 00000198
Time:
[x0]
 [x2]
 [x3]
 \times 4
[x5]
 = 0x ffffffffffffffff
 x6]
 [x7]
 x8]
[x9]
 [x10] = 0xfffffffffffffff
x11]
  = 0 \times 00000000000000000
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
[x17]
  [x18]
  x19]
  [x20]
  [x21]
  [x22]
  [x23]
```

```
[x25] = 0x000000000000000000
[x26]
   [x27]
   [x28]
   [x29] = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       2100, CLK = 1, PC = 0 \times 00000019 c
Time:
x2
  x3
  x41
  x5
  = 0x ffffffffffffffff
x 6
  x7]
  x8]
  x9]
  [x10] = 0xffffffffffffffff
x 11]
   x12
   x13]
   \times 14
   x 15]
   [x16]
   = 0 \times 00000000000000000
x17]
   [x18]
   = 0 \times 00000000000000000
x19]
   [x20]
   [x21]
[x22]
   x23
   x24
   [x25]
  [x26]
   x27
   x28
   [x29]
[x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
       2120, CLK = 1, PC = 0 \times 000001 a_0
```

```
[x2]
  [x3]
  x4]
  |x5|
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8]
  x9]
  [x10] = 0xfffffffffffffff
  = 0 \times 00000000000000000
[x11]
x12]
  [x13]
  x14
  [x15]
  x 16]
  [x17]
  x18
  = 0 \times 00000000000000000
x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  x25
  x 26]
  x27
x28]
  [x29]
  [x30]
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2140, CLK = 1, PC = 0x000001a4
Time:
  [x0]
[x1]
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
x 5
  x6
  x7
  x8]
[x9] = 0x000000000000000009
[x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
[x12] = 0x000000000000000004
```

|x1|

```
[x14]
[x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  x21]
  [x22]
  x23]
  [x24]
  x25
  [x26]
  x27
  x28
  x29]
  [x30]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     2160, CLK = 1, PC = 0x000001a8
Time:
[x0]
 x2]
 [x3]
x4]
 = 0x ffffffffffffffff
x5
 x6]
[x7]
 x8]
 [x9]
 [x10] = 0xffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 0000000000000000
  [x17]
  = 0 \times 0000000000000000
[x18]
[x19]
  [x20]
  x21
  [x22]
  x23
  [x24]
  [x25]
```

```
[x28] = 0x00000000000000000
[x29] = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
       2180, CLK = 1, PC = 0 \times 000001 ac
[x0] = 0x0000000000000000
  \times 1
[x2]
  x3
  \times 4
x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
  x 9
x11
   x12
   x13]
   x 14]
   x15]
   = 0 \times 0000000000000000
x 16]
x17]
   = 0 \times 00000000000000000
x18]
x19]
   [x20]
   x21
   [x22]
   x23
x24
   x25
   x26
   [x27]
   [x28]
   x29
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       2200, CLK = 1, PC = 0 \times 000001 \, b0
Time:
```

[x27] = 0x00000000000000004

```
[x3]
  x4]
  [x5]
  = 0x ffffffffffffffff
x6]
  |x7|
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  [x11]
x12]
  [x13]
  [x14]
  [x15]
  x 16]
  = 0 \times 00000000000000000000
[x17]
  x 18]
  [x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  x27
  x28]
  [x29]
[x30] = 0x00000000000000000
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2220, CLK = 1, PC = 0 \times 0000001 \, \text{b4}
Time:
[x0]
  [x1]
  [x2]
  [x3] = 0x00000000000000003
| x 4 ]
  x5]
  = 0x ffffffffffffffff
  x6]
  \times 7
  x81
  [x9]
[x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
[x12]
  [x13]
  [x14]
```

```
[x16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25]
  [x26]
  x27
  [x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
    2240, CLK = 1, PC = 0x000001b8
Time:
[x0]
 [x1]
 x2
 [x3]
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 x7
x8]
 [x9]
 [x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 0000000000000000
x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  [x25]
  [x26]
  [x27]
```

```
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2260, CLK = 1, PC = 0 \times 000001 bc
Time:
[x0] = 0x00000000000000000
x 1
  [x2]
  x3]
  x4
  x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x8]
  x9
  x11]
  x12]
  x13]
  \times 14
  x15
  x 16]
  = 0 \times 00000000000000000000
x17]
  x 18]
  x19]
  [x20]
  x21
  x22
  x23
  [x24]
  x25
x 26]
  x27
  x28
  [x29] = 0x000000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2280, CLK = 1, PC = 0 \times 000001 c0
Time:
[x0] = 0x0000000000000000
  [x2]
  [x3]
  [x4]
```

```
[x7]
  x8]
  [x9]
  [x10] = 0x fffffffffffffff
[x11]
  = 0 \times 0000000000000000
x12
  [x13]
x14
  [x15]
  x 16]
  = 0 \times 00000000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
  x22
  x23
  x24
  [x25]
  x26
  [x27]
  x28
  x29]
  x30
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
instruction = 0x00000013
      2300, CLK = 1, PC = 0 \times 000001 \, c4
Time:
[x0] = 0x00000000000000000
  [x1]
[x2]
  [x3]
  x4]
  |x5|
  = 0x ffffffffffffffff
x6]
  [x7]
  x8]
  x 9
[x10] = 0xfffffffffffffff
  = 0 \times 00000000000000000
[x11]
x12
  \lfloor x 1 3 \rfloor
  x14
  [x15]
  [x16]
  = 0 \times 00000000000000000
```

|x5|

[x6]

= 0x ffffffffffffffff

```
[x18]
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  [x24]
  x25]
  x 26]
  x27
  [x28]
  [x29]
  [x30] = 0x00000000000000000
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
     2320, CLK = 1, PC = 0 \times 000001 c8
Time:
 [x0]
 x2
 x3
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 x 7
 x8]
 [x9]
 [x10] = 0xffffffffffffff
[x11]
  = 0 \times 00000000000000000
x12]
  [x13]
  [x14]
[x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  x23]
  [x24]
x25
  [x26]
  x27
  [x28]
  [x29]
```

```
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
        2340, CLK = 1, PC = 0 \times 000001 cc
[x0] = 0x00000000000000000
  [x1]
  \times 2
x3]
  x4
x5]
  = 0x ffffffffffffffff
[x6] = 0x00000000000000000
x7]
  x8
  [x9] = 0x00000000000000000
x11]
   = 0 \times 00000000000000000
x12]
   x13]
   x14]
   x15
   x 16]
   = 0 \times 00000000000000000
\times 17
   x18]
   = 0 \times 00000000000000000000
x 19]
   x20]
   x21
   x22
   x23
   x24
   x25
[x26]
   [x27]
[x28]
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
        2360, CLK = 1, PC = 0 \times 000001 d0
Time:
[x0] = 0x0000000000000000
  \times 1
[x2] = 0x00000000000000000
x3
  |x4|
  x51
  = 0x ffffffffffffffff
```

```
[x8]
  [x9]
  [x10] = 0xfffffffffffffff
[x11]
  [x12]
  [x13]
  [x14]
  [x15]
x 16]
  = 0 \times 00000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  x25
  x 26]
  [x27]
  x28
  [x29]
  x30]
  [x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2380, CLK = 1, PC = 0 \times 000001 d4
Time:
  |x0|
  [x1]
[x2]
  [x3]
x 4]
  = 0x ffffffffffffffff
[x5]
x6]
  |x7|
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
[x11]
  [x12]
  [x13]
[x14]
  [x15]
  [x16]
  = 0 \times 0000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000
  = 0 \times 00000000000000013
```

|x7|

```
[x20]
[x21]
  [x22]
  [x23]
  [x24]
  [x25]
  x 26]
  x27
  [x28]
  x29
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
Time:
     2400, CLK = 1, PC = 0 \times 000001 d8
[x0]
 x2
 x3]
 \times 4
\times 5
 = 0x ffffffffffffffff
x6]
 [x7]
x8]
 x9]
 [x11]
  = 0x000000000000000
x12]
  [x13]
  x14
[x15]
  [x16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  [x25]
  [x26]
  x27
  [x28]
  [x29]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
```

```
immediate: 0x00000000000000000
instruction = 0x00000013
       2420, CLK = 1, PC = 0 \times 000001 dc
Time:
[x0] = 0x0000000000000000
  x2
  x3
[x4] = 0x00000000000000004
x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
  x9
  [x10] = 0xffffffffffffffff
\times 11
   = 0 \times 00000000000000000
x12]
   x13]
   x15]
   x 16]
   x17]
   x 18]
   = 0 \times 00000000000000000
x19]
   x20
x21
   x22
   x23
  x24
   x25
   [x26]
   x27
[x28]
   [x29] = 0x00000000000000000
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       2440, CLK = 1, PC = 0x000001e0
Time:
[x1]
[x2]
  x3
[x4] = 0x00000000000000004
x5
  = 0x fffffffffffffffff
  [x6]
[x7]
  x8]
```

```
[x9] = 0x00000000000000009
[x10]
  = 0x ffffffffffffffff
[x11]
  [x12]
  [x13]
  [x14]
  [x15]
  x16]
  = 0 \times 00000000000000000
  [x17]
x18]
  = 0 \times 00000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  \times 27
  x28
  [x29]
  x30
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2460, CLK = 1, PC = 0 \times 000001 \, e4
Time:
[x0]
  \times 1
[x2]
  x3
  [x4]
  = 0x ffffffffffffffff
[x5]
[x6]
  x7]
  x8]
  [x9]
  [x10] = 0xffffffffffffffff
[x11]
  = 0 \times 0000000000000000
x12]
  [x13]
[x14]
  [x15]
x16]
  = 0 \times 0000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
```

```
[x22]
[x23]
  [x24]
  x25
  [x26] = 0x00000000000000000
[x27]
  x28
  x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2480, CLK = 1, PC = 0 \times 000001 \, e8
Time:
x 1]
  x2
  x3
  \times 4
x5]
  = 0x ffffffffffffffff
  x6]
\times 7
  x8]
  x9
  [x10] = 0xfffffffffffffff
x11] = 0x0000000000000000b
x12]
  [x13]
  \times 14
  [x15]
  = 0 \times 00000000000000000
x 16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  x25
  x26
x27
  [x28]
  [x29]
  [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

```
2500, CLK = 1, PC = 0 \times 000001 ec
Time:
[x0] = 0x0000000000000000
  [x1]
[x2]
  [x3]
  x4]
  = 0x fffffffffffffffff
x5
  x 6
[x7]
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
   = 0 \times 0000000000000000
[x12]
   x13]
   [x14]
   x15
   x 16]
   = 0 \times 00000000000000000
\times 17
   [x18]
   = 0 \times 00000000000000000
x19]
   x20]
   x21
   x22
   x23]
   x24
x25
   x 26]
   x27
   [x28]
   x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       2520, CLK = 1, PC = 0 \times 000001 f0
Time:
[x0]
  [x1]
  \times 2
  [x3]
  x4
  = 0x ffffffffffffffff
x5]
[x6]
  \times 7
[x8]
  [x9] = 0x000000000000000000
[x10] = 0xfffffffffffffff
```

```
[x11]
[x12]
  |x13|
  x14
  [x15]
  [x16]
  = 0 \times 00000000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000
[x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  [x27]
  x281
  [x30]
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
      2540, CLK = 1, PC = 0 \times 0000001 \, \text{f4}
Time:
[x0]
  |x1|
  [x2]
  x3]
  [x4]
  = 0x ffffffffffffffff
x5
  [x6]
  x7]
x8]
  [x9]
  [x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
[x12]
  [x13]
  [x14]
  [x15]
[x16]
  = 0 \times 00000000000000000
  [x17]
[x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  [x22]
```

```
[x24] = 0x000000000000000002d
[x25]
   [x26] = 0x00000000000000000
[x27]
   [x28]
  [x29]
   [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
       2560, CLK = 1, PC = 0 \times 0000001 \, f8
Time:
[x0]
  x1]
  x2]
  x3]
  x4
  x5
  = 0x ffffffffffffffff
x6]
  x7]
  x8
x 9]
  x 1 1 ]
   = 0x000000000000000
x12
   x13
   \times 14
   [x15]
   = 0 \times 0000000000000000
x 16]
[x17]
   x18]
   = 0 \times 0000000000000000
[x19]
   x20
   [x21]
   x22
   [x23]
   [x24] = 0x00000000000000002d
[x25]
   x 26]
   \times 27
   x28
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0x00000013
```

Time: 2580, CLK = 1, PC = 0x000001 fc

```
[x0]
  [x1]
[x2]
  [x3]
  |x4|
  [x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  x 15]
  [x16]
  = 0 \times 00000000000000000000
x17
  x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21
  x22
  x23
  x24
  x25]
  \times 26
  x27
  [x28]
  x29
  | x 3 0 |
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      2600, CLK = 1, PC = 0 \times 00000200
Time:
[x1]
  [x2]
  [x3]
  \times 4
  = 0x ffffffffffffffff
x5]
x6]
  x 7
[x8]
  x9
  [x10] = 0x ffffffffffffffff
[x11] = 0x0000000000000000
```

```
[x13]
  [x14]
|x15|
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     2620, CLK = 1, PC = 0 \times 00000204
Time:
[x0]
  x1]
  [x2]
  [x3]
  x4]
  = 0x fffffffffffffffff
x5]
[x6]
  x7
  x8]
  [x9]
[x10] = 0xfffffffffffffff
  [x11]
x12]
  [x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
x 16]
[x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  [x24]
```

```
[x26] = 0x000000000000000000
   [x27]
x28]
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       2640, CLK = 1, PC = 0 \times 00000208
Time:
[x0] = 0x00000000000000000
[x1]
  x2
  x3
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8
  [x9] = 0x00000000000000000
x10] = 0x fffffffffffffffff
[x11] = 0x0000000000000000
x12]
   x13]
   \times 14
   x15
   x 16]
   [x17]
x18]
   = 0 \times 00000000000000000
[x19]
   x20
   [x21]
   x22
   [x23]
   x24
   x 25]
   [x26] = 0x00000000000000000
[x27]
   [x28]
   [x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       2660, CLK = 1, PC = 0 \times 0000020 c
```

```
[x4]
  = 0x ffffffffffffffff
x5]
[x6]
  x7]
  x8
  x9]
  [x10] = 0xfffffffffffffff
[x11]
   = 0 \times 00000000000000000
[x12]
   x13]
   [x14]
   x15]
   [x16]
   = 0 \times 00000000000000000000
\times 17
   [x18]
   = 0 \times 00000000000000000000
x19
   x20
   x21
   [x22]
   x23
   x24
   x25
   x 26]
   \times 27
   [x28]
x29]
   [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       2680, CLK = 1, PC = 0 \times 00000210
Time:
[x0] = 0x00000000000000000
[x1]
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
[x5]
x6]
  \times 7
  x8]
[x9] = 0x00000000000000009
[x10] = 0xfffffffffffffff
[x11]
   = 0 \times 00000000000000000
[x12]
   [x13]
```

 $\begin{bmatrix} x2 \end{bmatrix}$ 

```
[x15]
[x16]
  |x17|
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
     2700, CLK = 1, PC = 0 \times 00000214
[x0]
 x 1
  x2
  x3]
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 \times 7
[x8]
 x9]
[x10] = 0xfffffffffffffff
  [x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000000
[x17]
  = 0 \times 00000000000000000000
[x18]
  [x19]
[x20]
  [x21]
x22
  [x23]
  x24
  [x25]
  [x26]
```

```
[x28] = 0x000000000000000000
[x29] = 0x00000000000000000
[x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxxx
Time:
       2720, CLK = 1, PC = 0 \times 00000218
[x0]
  [x1]
x2
  [x3] = 0x00000000000000000
x4]
  x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x81
  x9]
  [x11] = 0x0000000000000000
x12]
   x13]
   \times 14
   x15]
   x 16]
   = 0 \times 00000000000000000
   \times 17
x18]
   [x19]
x20
   [x21]
   x22
   [x23]
   [x24]
[x25]
   x26]
   x27
   [x28] = 0x000000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
       2740, CLK = 1, PC = 0 \times 00000021 c
[x2]
```

```
|x4|
  x5]
  = 0x fffffffffffffffff
[x6]
  x7
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
[x12]
  x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000000
x 19]
  [x20]
  x21]
  x22
  x23
  [x24]
  x25
  [x26]
  \times 27
  x28
  x29]
  [x30]
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      2760, CLK = 1, PC = 0 \times 00000220
Time:
  [x0]
[x1]
  |x2|
  x3]
  |x4|
[x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
x9]
  [x10] = 0xffffffffffffffff
x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
```

```
[x17]
[x18]
  [x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  [x25]
  x26]
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     2780, CLK = 1, PC = 0 \times 00000224
Time:
[x0]
 \times 1
x2
  x3
  x4]
  x5]
 = 0x ffffffffffffffff
x 6
  x7]
  x8]
 x9
  [x10] = 0xffffffffffffffff
x 1 1 ]
  = 0 \times 00000000000000000
[x12]
  [x13]
[x14]
  [x15]
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26
  [x27]
  [x28]
```

```
[x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       2800, CLK = 1, PC = 0 \times 000000228
Time:
[x0] = 0x00000000000000000
  \times 1
x2
  [x3]
  \times 4
  [x5] = 0xfffffffffffffff
x6]
  x7
  x8]
  [x9] = 0x00000000000000009
x11] = 0x0000000000000000b
x12
   x13]
   x14
   x15]
   x 16]
   x17]
   x181
x19]
   [x20] = 0x00000000000000004
[x21] = 0x000000000000000004
x22
   x23
[x25]
   [x26] = 0x00000000000000000
[x27]
   [x28]
   [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       2820, CLK = 1, PC = 0 \times 00000022 c
Time:
[x3] = 0x00000000000000000
[x4] = 0x000000000000000004
  = 0x fffffffffffffffff
```

```
[x6]
  [x7]
[x8]
  [x9]
  [x10] = 0x ffffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  \times 21
  [x22]
  x23]
  x24
  x25
  [x26]
  x27
  [x28]
  x29
  [x30]
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      2840, CLK = 1, PC = 0 \times 00000230
Time:
  [x0]
  [x2]
[x3]
  [x4]
x5]
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8
  x9]
[x10] = 0xfffffffffffffff
[x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  = 0 \times 00000000000000000000
```

```
[x19]
[x20]
  [x21]
  [x22]
  [x23]
  [x24]
  [x25]
  x 26]
  [x27]
[x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     2860, CLK = 1, PC = 0 \times 00000234
Time:
[x0]
 \times 1
[x2]
  x3
  \times 4
x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
x9]
 [x10] = 0xffffffffffffffff
  = 0 \times 0000000000000000
x 11
[x12]
  [x13]
[x14]
  [x15]
[x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  [x24]
  [x25]
  [x26]
  [x27]
  x28
  [x29]
  [x30]
```

```
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
        2880, CLK = 1, PC = 0 \times 00000238
Time:
[x0] = 0x00000000000000000
  [x1]
  \times 2
[x3] = 0x0000000000000000
x4]
  [x5] = 0xfffffffffffffff
x6]
  [x7] = 0x00000000000000000
x8]
  [x9] = 0x00000000000000009
[x11] = 0x0000000000000000
x12] = 0x000000000000000004
x13]
   x14]
   [x15]
   x 16]
   [x17]
   x18]
   x19]
[x20] = 0x00000000000000004
[x21]
   [x23] = 0x00000000000000017
[x25]
x26] = 0x00000000000000000
[x27]
   [x28] = 0x00000000000000000
[x29]
   [x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
        2900, CLK = 1, PC = 0 \times 0000023 c
Time:
[x1]
  \times 2
[x3] = 0x00000000000000003
x4
  [x5]
  = 0x ffffffffffffffff
[x6]
```

```
[x8] = 0x00000000000000008
[x9]
  [x10] = 0xffffffffffffffff
[x11]
  = 0 \times 0000000000000000
[x12]
  [x13]
  [x14]
  x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21]
  [x22]
  x23
  x24
  x25
  x26]
  x27
  [x28]
  x29]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxxx
      2920, CLK = 1, PC = 0 \times 00000240
Time:
[x0] = 0x00000000000000000
  [x1]
  x2
  [x3]
  [x4]
x5]
  = 0x ffffffffffffffff
x6]
  x7
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
  [x12]
[x13]
  [x14]
x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
```

```
[x21]
[x22]
  [x23]
  x24
  [x25]
  [x26]
  [x27]
  x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     2940, CLK = 1, PC = 0 \times 00000244
Time:
[x0]
  \times 1
  x3
  x4]
  = 0x ffffffffffffffff
\times 5
x6]
  x7]
  x8]
  x 9]
  [x10] = 0xffffffffffffffff
x11]
  [x12]
  x13]
  [x14]
  x15]
[x16]
  = 0 \times 00000000000000000000
  [x17]
[x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  [x25]
x 26]
  [x27]
x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
```

```
instruction = 0xxxxxxxx
Time:
      2960, CLK = 1, PC = 0 \times 00000248
[x0] = 0x00000000000000000
[x2]
  [x3]
  \times 4
[x5] = 0xfffffffffffffff
x6]
  [x7]
x8
  [x9] = 0x00000000000000000
[x11]
  = 0 \times 0000000000000000
x12
  [x13]
  x14
  x15
  x 16]
  [x17]
  x18]
  = 0 \times 0000000000000000
[x19]
  x20]
  x21
  x22
  [x23]
  [x25]
  x26
  [x27]
  x28
[x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      2980, CLK = 1, PC = 0 \times 00000024 c
Time:
[x0] = 0x00000000000000000
  [x1]
[x2]
  [x3] = 0x00000000000000003
  \times 4
[x5] = 0x ffffffffffffffff
  x6]
[x7]
  [x8]
  [x9]
```

```
= 0x ffffffffffffffff
[x10]
  = 0 \times 00000000000000000000
[x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 0000000000000000
  [x17]
  [x18]
x19]
  [x20]
  x21
  [x22]
  x23
  [x24]
  x25
  [x26]
  x27
  x29]
  [x30]
  [x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3000, CLK = 1, PC = 0 \times 00000250
Time:
[x0] = 0x0000000000000000
 x2
  [x3]
 \times 4
 [x5]
 = 0x ffffffffffffffff
 x6]
\times 7
 [8x
 x9]
 [x10] = 0xfffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
[x15]
  [x16]
x17
  |x18|
  [x19]
  [x20]
  [x21]
```

```
[x23] = 0x00000000000000017
x24
  [x25]
  x 26]
  [x27]
  [x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3020, CLK = 1, PC = 0x00000254
Time:
[x0]
  \times 1
  x2
  x3
  = 0x ffffffffffffffff
x5
x6]
  x7]
  x8]
  x 9
x11] = 0x0000000000000000b
x12]
  [x13]
  [x14]
  x 15]
  [x16]
  = 0 \times 00000000000000000
  x17
[x18]
  [x19]
  [x20]
  [x21]
  [x22]
  [x23]
  [x24]
  x25
  x 26]
  [x27]
[x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
```

```
[x1]
  [x2]
  [x3] = 0x00000000000000003
[x4]
  x5
  = 0x ffffffffffffffff
  x6]
  x7]
x8]
  [x9] = 0x00000000000000009
[x11] = 0x0000000000000000
x12]
   [x13]
   \times 14
   x15]
   x16]
   = 0 \times 00000000000000000
\times 17
   x18]
   = 0 \times 00000000000000000
[x19]
   x20
   \times 21
   x22
   x23
   x24
   x25
x 26]
  x27
   x28
   [x29]
  [x30] = 0x00000000000000000
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       3060, CLK = 1, PC = 0 \times 00000025 c
Time:
[x0]
  [x1]
  [x2]
[x3] = 0x0000000000000000
  \times 4
  = 0x ffffffffffffffff
x5
  x6]
[x7]
  x81
  [x9] = 0x00000000000000000
[x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
```

3040, CLK = 1, PC =  $0 \times 00000258$ 

Time:

[x0]

```
[x12]
[x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 0000000000000000
x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25
  [x26]
  \times 27
  [x28]
  x29
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3080, CLK = 1, PC = 0 \times 00000260
Time:
[x0]
 [x2]
 [x3]
 \times 4
[x5]
 = 0x ffffffffffffffff
 x6]
 [x7]
 x8]
[x9]
 [x10] = 0xfffffffffffffff
[x11]
  = 0 \times 0000000000000000
[x12]
  [x13]
  [x14]
  [x15]
  [x 1 6]
[x17]
  [x18]
  x19]
  [x20]
  [x21]
  [x22]
  [x23]
```

```
[x25] = 0x000000000000000000
[x26]
  [x27]
  [x28]
  [x29] = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3100, CLK = 1, PC = 0x00000264
Time:
x2
  x3]
  x4]
  x5
  = 0x ffffffffffffffff
x 6]
  x7]
  x8]
  x9]
  [x10] = 0xffffffffffffffff
x 11]
  x12]
  x13]
  \times 14
  x 15]
  [x16]
x17]
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  [x21]
[x22]
  x23
  x24
  [x25]
  [x26]
  x27
  x28
  [x29]
[x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3120, CLK = 1, PC = 0 \times 00000268
Time:
[x0] = 0x00000000000000000
```

```
|x1|
  [x2]
  [x3]
  x4]
  [x5]
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8]
  x9]
  [x10] = 0xfffffffffffffff
  = 0 \times 00000000000000000
[x11]
x12]
  [x13]
  x14
  [x15]
  x 16]
  [x17]
  x18
  = 0 \times 00000000000000000
x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  x25
  x 26]
  x27
x28]
  [x29]
  x30
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3140, CLK = 1, PC = 0 \times 00000026 c
Time:
  [x0]
[x1]
  [x2]
  [x3]
  [x4]
  x 5
  = 0x ffffffffffffffff
  x6
  x7
  x8]
[x9] = 0x000000000000000009
[x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
[x12] = 0x000000000000000004
```

```
[x14]
  [x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  x21]
  [x22]
  x23]
  [x24]
  x25
  [x26]
  x27
  [x28]
  x29]
  [x30]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3160, CLK = 1, PC = 0 \times 00000270
Time:
[x0] = 0x00000000000000000
  x2]
 [x3]
x4]
 = 0x ffffffffffffffff
x5
  x6]
[x7]
 x8
[x9]
 [x10] = 0xffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
  [x17]
  = 0 \times 00000000000000000
[x18]
[x 1 9]
  [x20]
x21
  [x22]
  x23
  [x24]
  [x25]
```

```
[x28] = 0x00000000000000000
[x29] = 0x000000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
       3180, CLK = 1, PC = 0 \times 00000274
[x0] = 0x0000000000000000
  \times 1
x3
  \times 4
x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
  x 9]
[x10] = 0xffffffffffffff
x11] = 0x0000000000000000b
x12
   x13]
   x 14]
   x 15]
   = 0 \times 0000000000000000
x 16]
x17
   = 0 \times 00000000000000000
[x18]
x19]
   [x20]
   x21
   [x22]
   x23
x24
   x25
   x26
   [x27] = 0x000000000000000004
[x28]
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       3200, CLK = 1, PC = 0 \times 00000278
Time:
```

[x27] = 0x00000000000000004

```
[x3]
  [x4]
  [x5]
  = 0x ffffffffffffffff
x6]
  |x7|
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  [x11]
x12]
  [x13]
  [x14]
  [x15]
  x16]
  = 0 \times 00000000000000000000
[x17]
  x18
  = 0 \times 00000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  [x27]
  x28]
  [x29]
[x30] = 0x00000000000000000
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3220, CLK = 1, PC = 0 \times 00000027 c
Time:
[x0]
  [x1]
  [x2]
  [x3] = 0x00000000000000003
[x4]
  [x5]
  = 0x ffffffffffffffff
  x6]
  \times 7
  x81
  [x9]
[x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
x12]
  [x13]
  [x14]
```

```
[x16]
  [x17]
[x18]
  [x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x25]
  [x26]
  x27
  [x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3240, CLK = 1, PC = 0 \times 00000280
Time:
[x0]
 [x1]
 x2
 [x3]
 x4]
 x5]
 = 0x ffffffffffffffff
 = 0x00000000000000006
x6]
 x7
x8]
 [x9]
 [x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 0000000000000000
  [x19]
[x20]
  [x21]
  [x22]
  x23
  [x24]
  [x25]
  [x26]
  [x27]
```

```
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3260, CLK = 1, PC = 0 \times 00000284
Time:
[x0] = 0x00000000000000000
[x1]
  [x2]
  x3
  [x4] = 0x000000000000000004
x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x8]
  \times 9
  x11]
  = 0 \times 0000000000000000
x12]
  x13]
  \times 14
  x 15]
  x 16]
  = 0 \times 0000000000000000
x17]
  x18]
  x19]
  [x20]
  x21
  x22
  x23
  [x24]
  [x25]
[x26]
  [x27]
  x28
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
instruction = 0xxxxxxxx
      3280, CLK = 1, PC = 0 \times 00000288
Time:
[x0] = 0x0000000000000000
  [x2]
  [x3]
```

```
[x6]
  [x7]
  x8]
  [x9] = 0x00000000000000000
[x10] = 0x fffffffffffffff
[x11]
   = 0 \times 0000000000000000
x12
   [x13]
[x14]
   [x15]
   x 16]
   = 0 \times 00000000000000000000
[x17]
   x18]
   = 0 \times 00000000000000000000
[x19]
   x20]
   [x21]
   x22
   x23
   x24
   [x25]
   x26
   [x27]
   x28
   x29]
   [x30]
   [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       3300, CLK = 1, PC = 0 \times 00000028 c
Time:
[x0] = 0x00000000000000000
|x1|
  [x2]
  [x3]
  x4]
  |x5|
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8]
  x 9
[x10] = 0xfffffffffffffff
   = 0 \times 00000000000000000
[x11]
[x12]
   \lfloor x 1 3 \rfloor
   x14
   [x15]
   [x16]
   = 0 \times 00000000000000000
```

|x5|

= 0x ffffffffffffffff

```
[x18]
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  [x24]
  x25]
  x 26]
  x27
  [x28]
  [x29]
  [x30] = 0x00000000000000000
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3320, CLK = 1, PC = 0 \times 00000290
Time:
 [x0]
 x2
 x3
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 x 7
 x8]
 [x9]
 [x10] = 0xffffffffffffff
[x11]
  = 0 \times 00000000000000000
x12]
  [x13]
  [x14]
[x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
x25
  [x26]
  x27
  [x28]
  [x29]
```

```
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
        3340, CLK = 1, PC = 0 \times 00000294
[x0] = 0x0000000000000000
   [x1]
[x2] = 0x00000000000000000
x3]
  [x4]
x5
   = 0x ffffffffffffffff
[x6] = 0x00000000000000000
x7]
   x8
   [x9] = 0x00000000000000000
x11] = 0x0000000000000000b
   x13]
   [x14]
   x 15]
   x 16]
   = 0 \times 00000000000000000
\times 17
   x18]
   = 0 \times 0000000000000000
x19]
   [x20]
   x21
   [x22] = 0x00000000000000016
x23
   x24
   x25
[x26]
   [x27]
[x28]
   [x29] = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
        3360, CLK = 1, PC = 0 \times 00000298
Time:
[x0] = 0x0000000000000000
   \times 1
[x2] = 0x00000000000000000
x3
  [x4] = 0x000000000000000004
  = 0x ffffffffffffffff
[x5]
```

```
[8x
  [x9]
  [x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
[x12]
  [x13]
  [x14]
  [x15]
x16]
  = 0 \times 00000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  x25
  x 26]
  [x27]
  x28
  [x29]
  x30
  [x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxxx
      3380, CLK = 1, PC = 0 \times 00000029 c
Time:
  [x0]
  [x1]
[x2]
  [x3]
x 4]
  = 0x ffffffffffffffff
[x5]
x6]
  |x7|
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  [x11]
[x12]
  [x13]
x14
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000
  = 0 \times 00000000000000013
```

|x7|

```
[x20]
[x21]
  [x22]
  [x23]
  [x24]
  [x25]
  x 26]
  x27
  x28
x29
  [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
instruction = 0xxxxxxxx
Time:
    3400, CLK = 1, PC = 0 \times 0000002 a_0
[x0]
 x2
 x3]
 \times 4
\times 5
 = 0x ffffffffffffffff
x6]
 [x7]
 x8]
 x9]
 [x11]
  x12]
  [x13]
  x14
[x15]
  [x16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  x23
  [x24]
  x 25]
  [x26]
  x27
  [x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
```

```
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3420, CLK = 1, PC = 0 \times 0000002 \text{ a4}
Time:
[x0] = 0x00000000000000000
  [x2]
  x3
[x4] = 0x00000000000000004
x5]
  = 0x ffffffffffffffff
  x6]
x7]
  x8]
  x9]
  [x10] = 0xffffffffffffffff
\times 11
  [x12]
  x13
  x 15]
  [x16]
  x17]
  x18
  = 0 \times 00000000000000000
x19]
  x20
x21
  x22
  [x23] = 0x0000000000000017
x24
  x25
  [x26]
  x27
[x28]
  [x29] = 0x00000000000000000
[x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxxx
      3440, CLK = 1, PC = 0 \times 0000002 a8
Time:
[x1]
  [x2]
  x3
[x4] = 0x00000000000000004
  x5
  [x6]
  [x7]
```

```
[x9] = 0x00000000000000009
[x10]
  = 0x ffffffffffffffff
[x11]
  [x12]
  [x13]
  [x14]
  [x15]
  x16]
  = 0 \times 00000000000000000
  [x17]
x18]
  = 0 \times 00000000000000000
[x19]
  x20
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  \times 27
  x28
  [x29]
  x30
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3460, CLK = 1, PC = 0 \times 0000002 ac
Time:
  [x0]
  \times 1
[x2]
  x3
  [x4]
  = 0x ffffffffffffffff
x5]
[x6]
  x7]
  x8]
  [x9] = 0x00000000000000000
[x10] = 0xffffffffffffffff
[x11]
  = 0 \times 00000000000000000
x12
  [x13]
[x14]
  [x15]
x16]
  = 0 \times 0000000000000000
[x17]
  x18]
  = 0 \times 0000000000000000
[x19]
  [x20]
```

```
[x22]
[x23]
  [x24]
  x25
  [x26] = 0x000000000000000000
[x27]
  [x28]
  x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
instruction = 0xxxxxxxx
      3480, CLK = 1, PC = 0 \times 0000002 \, b0
Time:
[x0] = 0x00000000000000000
  x2
  \times 4
x5]
  = 0x ffffffffffffffff
  x6]
\times 7
  x8]
  x9]
  [x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
[x12] = 0x00000000000000004
[x13]
  x14
  [x15]
  = 0 \times 00000000000000000
x16]
[x17]
  [x18]
  [x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  x25
  [x26]
[x27]
  [x28]
  x29]
  [x30] = 0x00000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
```

```
3500, CLK = 1, PC = 0 \times 0000002 \, \text{b4}
Time:
[x0] = 0x0000000000000000
  [x1]
|x2|
  [x3]
  x4
  = 0x ffffffffffffffff
x5
  x 6
x7
  x8]
x9]
  [x10] = 0xffffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  x13]
  [x14]
  x15
  x 16]
  \times 17
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21
  x22
  x23
  x24
x25
  [x26]
  x27
  [x28]
  x29
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3520, CLK = 1, PC = 0 \times 0000002 \, b8
Time:
[x0]
  [x1]
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
x5]
[x6]
  \times 7
[x8]
  [x9] = 0x00000000000000000
[x10] = 0xfffffffffffffff
```

```
[x11]
[x12]
  |x13|
  x14
  [x15]
  [x16]
  = 0 \times 00000000000000000000
[x17]
  x18]
  = 0 \times 00000000000000000
[x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x 26]
  [x27]
  x281
  [x30]
[x31] = 0x00000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3540, CLK = 1, PC = 0 \times 0000002 bc
Time:
[x0]
 |x1|
  [x2]
  x3
  [x4]
  = 0x ffffffffffffffff
x5
  [x6]
  x7]
x8]
  [x9]
  [x10] = 0xffffffffffffffff
[x11] = 0x0000000000000000
[x12]
  [x13]
  [x14]
  [x15]
[x16]
  = 0 \times 00000000000000000
  [x17]
[x18]
  = 0 \times 00000000000000000000
|x19|
  [x20]
  [x21]
  [x22]
```

```
[x24] = 0x000000000000000002d
[x25]
  [x26] = 0x00000000000000000
[x27]
   [x28] = 0x000000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
       3560, CLK = 1, PC = 0 \times 0000002 c0
[x1]
  x2
  x3]
  \times 4
  x5
  = 0x ffffffffffffffff
x6]
  x7]
  x8]
x 9]
  x11]
  x12
  \times 13
   x 14]
  [x15]
   = 0 \times 0000000000000000
x 16]
[x17]
   x18]
   = 0 \times 0000000000000000
[x19]
   [x20]
   [x21]
   x22
   [x23]
   [x24] = 0x00000000000000002d
[x25]
   x 26]
  \times 27
   [x28]
  [x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
instruction = 0xxxxxxxx
```

Time: 3580, CLK = 1, PC =  $0 \times 000002 \, c4$ 

```
[x1]
|x2|
  [x3]
  |x4|
  [x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8]
x9]
  [x10] = 0xfffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  x13]
  [x14]
  x 15]
  [x16]
  = 0 \times 00000000000000000000
x17
  x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21
  x22
  x23
  x24
  x25]
  \times 26
  x27
  [x28]
  x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3600, CLK = 1, PC = 0 \times 0000002 c8
Time:
[x1]
  [x2]
  [x3]
  \times 4
  = 0x ffffffffffffffff
x5]
x6]
  \times 7
[x8]
  x9
  [x10] = 0x ffffffffffffffff
[x11] = 0x0000000000000000
```

[x0]

```
[x13]
  [x14]
|x15|
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3620, CLK = 1, PC = 0 \times 0000002 cc
Time:
[x0]
  \times 1
  [x2]
  [x3]
  [x4]
  = 0x ffffffffffffffff
x5]
[x6]
  x7
  x8]
  [x9]
[x10] = 0xfffffffffffffff
x11]
  [x12]
  [x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
x 16]
[x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  [x24]
```

```
[x27]
[x28] = 0x00000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
       3640, CLK = 1, PC = 0 \times 0000002 d0
[x0] = 0x0000000000000000
[x1]
  x2
  x3
  x4]
  x5]
  = 0x ffffffffffffffff
x6]
  \times 7
  x8
  [x9] = 0x00000000000000009
x10] = 0x fffffffffffffffff
x11] = 0x0000000000000000b
x12]
   x13]
   \times 14
   x15
   x 16]
   [x17]
x18]
   = 0 \times 00000000000000000
[x19]
   x20
   [x21]
   x22
   [x23]
   x24
   x 25]
   [x26] = 0x00000000000000000
[x27]
   [x28]
   = 0x00000000000000000
[x29]
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
instruction = 0xxxxxxxx
       3660, CLK = 1, PC = 0 \times 0000002 \, d4
```

```
|x2|
  [x3]
  [x4]
  x5]
  = 0x ffffffffffffffff
[x6]
  x7]
  x8
  x9]
  [x10] = 0xfffffffffffffff
[x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  x15]
   [x16]
   = 0 \times 00000000000000000000
\times 17
   [x18]
   = 0 \times 00000000000000000000
x19
   x20
   x21
   [x22]
  x23
  x24
  x25
  x 26]
   \times 27
  [x28]
  x29]
  [x30] = 0x00000000000000006
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3680, CLK = 1, PC = 0 \times 0000002 \, d8
Time:
[x0] = 0x00000000000000000
[x1]
  [x2]
  [x3]
  [x4]
```

= 0x ffffffffffffffff[x5] x6] x7x8] [x9] = 0x00000000000000009[x10] = 0xfffffffffffffff[x11] $= 0 \times 00000000000000000$ [x12][x13]

```
[x15]
[x16]
  [x17]
  x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
  [x29]
  [x30]
  [x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
     3700, CLK = 1, PC = 0 \times 0000002 dc
[x0]
 x 1
 x2
 x3
 x4]
 x5]
 = 0x ffffffffffffffff
x6]
 x7]
 [x8]
 x9]
[x10] = 0xfffffffffffffff
  [x11]
[x12]
  [x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000000
[x17]
  = 0 \times 00000000000000000000
[x18]
  [x19]
[x20]
  [x21]
x22
  [x23]
  x24
  [x25]
  [x26]
```

```
[x29] = 0x00000000000000000
[x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxxx
Time:
       3720, CLK = 1, PC = 0 \times 0000002 e0
[x0]
  [x1]
x2
  [x3] = 0x00000000000000000
x4]
  x5]
  = 0x ffffffffffffffff
x6]
  x7]
  x81
  x9]
  [x11] = 0x0000000000000000
x12]
  x13]
   \times 14
   x15]
   x 16]
   = 0 \times 00000000000000000
\times 17
   x18]
   [x19]
x20
   [x21]
   x22
   [x23]
   [x24]
[x25]
   [x26]
  [x27]
   [x28] = 0x000000000000000000
[x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
Time:
       3740, CLK = 1, PC = 0 \times 0000002 \, e4
[x2]
```

```
|x4|
  x5]
  = 0x fffffffffffffffff
[x6]
  [x7]
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
[x12]
  x13]
  [x14]
  x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000000
x 19]
  [x20]
  x21]
  x22
  x23
  [x24]
  x25
  [x26]
  x27
  x28
  x29]
  [x30]
[x31] = 0x0000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3760, CLK = 1, PC = 0 \times 0000002 \, \text{e}8
Time:
  [x0]
[x1]
  |x2|
  [x3]
  | x 4 |
[x5]
  = 0x ffffffffffffffff
x6]
  x7
  x8]
x9]
  [x10] = 0xffffffffffffffff
x11]
  = 0 \times 0000000000000000
[x12]
  [x13]
  [x14]
  [x15]
  = 0 \times 00000000000000000000
```

```
[x17]
[x18]
  [x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  [x25]
  x26]
  [x27]
  x28
  [x29]
  [x30]
  [x31]
  ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3780, CLK = 1, PC = 0 \times 0000002 ec
Time:
[x0]
 x 1]
 x2
 x3
 x4]
 = 0x ffffffffffffffff
x5]
 x 6
x7]
 x81
x9
 [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
x 1 1 ]
[x12]
  [x13]
[x14]
  [x15]
  x16]
  = 0 \times 00000000000000000000
[x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  x20]
  [x21]
x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  [x28]
```

```
[x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       3800, CLK = 1, PC = 0 \times 0000002 f0
Time:
[x0] = 0x00000000000000000
  \times 1
x2
  [x3]
  \times 4
  [x5] = 0xfffffffffffffff
x6]
  x7
  x8]
  [x9] = 0x00000000000000009
x11] = 0x0000000000000000b
x12
   x13]
   x14
   x15
   x 16]
   x17
   x18
x19]
   [x20] = 0x00000000000000004
[x21] = 0x000000000000000004
x22
   x23
   x24
[x25]
   [x26] = 0x00000000000000000
[x27]
   [x28]
   [x29]
   [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       3820, CLK = 1, PC = 0 \times 0000002 \, \text{f4}
Time:
[x3]
  [x4]
  = 0x fffffffffffffffff
```

```
[x6]
  [x7]
[x8]
  [x9]
  [x10] = 0x ffffffffffffffff
[x11]
  [x12]
  x13]
  [x14]
x15]
  [x16]
  = 0 \times 00000000000000000
x17
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  \times 21
  [x22]
  x23
  x24
  x25
  [x26]
  x27
  [x28]
  x29
  [x30]
[x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3840, CLK = 1, PC = 0 \times 0000002 \, f8
Time:
  [x0]
  [x2]
[x3]
  [x4]
x5]
  = 0x ffffffffffffffff
[x6]
  [x7]
  x8
  x9]
[x10] = 0xfffffffffffffff
[x11]
  = 0 \times 00000000000000000
[x12]
  x13]
  [x14]
  [x15]
  [x16]
  = 0 \times 00000000000000000
[x17]
  = 0 \times 00000000000000000000
```

```
[x19]
[x20]
  [x21]
  [x22]
  [x23]
  [x24]
  [x25]
  x 26]
  [x27]
[x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3860, CLK = 1, PC = 0 \times 0000002 fc
Time:
[x0]
 \times 1
x2]
 x3
 \times 4
x5]
 = 0x ffffffffffffffff
x6]
 x7
 x8]
x9]
 [x10] = 0xffffffffffffffff
  = 0 \times 0000000000000000
\times 11
[x12]
  [x13]
[x14]
  [x15]
[x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  x22
  [x23]
x24
  [x25]
x26
  [x27]
  x28
  [x29]
  [x30]
```

```
ALUcontrol: 0x2
instruction = 0xxxxxxxx
       3880, CLK = 1, PC = 0 \times 00000300
Time:
[x0] = 0x00000000000000000
  [x1]
  x2
[x3] = 0x0000000000000000
x4]
  x5]
  = 0x ffffffffffffffff
x6]
  [x7] = 0x00000000000000000
x8]
  [x9] = 0x00000000000000009
x11] = 0x0000000000000000b
x12] = 0x000000000000000004
x13]
   x14]
   [x15]
   x 16]
   [x17]
   x18]
   x19]
[x20] = 0x00000000000000004
[x21]
   [x23] = 0x00000000000000017
   x24
   [x25]
x26] = 0x00000000000000000
[x27]
   [x28] = 0x00000000000000000
[x29]
   [x30] = 0x0000000000000000
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
       3900, CLK = 1, PC = 0 \times 00000304
Time:
[x1]
  \times 2
[x3] = 0x00000000000000003
x4
  [x5]
  = 0x ffffffffffffffff
[x6]
```

```
[x8] = 0x00000000000000008
[x9]
  [x10] = 0xffffffffffffffff
[x11]
  = 0 \times 0000000000000000
[x12]
  [x13]
  [x14]
  x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
x19]
  [x20]
  x21]
  [x22]
  x23
  x24
  x25
  x26]
  x27
  [x28]
  x29]
  [x31] = 0x000000000000000006
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3920, CLK = 1, PC = 0 \times 00000308
Time:
[x0] = 0x00000000000000000
  [x1]
  x2
  [x3]
  [x4]
x5]
  = 0x ffffffffffffffff
x6]
  x7
  [x8]
  [x9]
  [x10] = 0xffffffffffffffff
  = 0 \times 00000000000000000
[x11]
  [x12]
[x13]
  [x14]
x15]
  [x16]
  [x17]
  [x18]
  = 0 \times 00000000000000000
[x19]
```

```
[x21]
[x22]
  [x23]
  [x24]
  [x25]
  [x26]
  [x27]
  x28]
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
     3940, CLK = 1, PC = 0 \times 0000030 c
Time:
[x0]
 \times 1
  x3
  x4]
  = 0x ffffffffffffffff
\times 5
x6]
  x7]
  x8]
  x 9]
  [x10] = 0xfffffffffffffff
x11]
  [x12]
  x13]
  [x14]
  x15]
[x16]
  = 0 \times 00000000000000000000
  [x17]
[x18]
  = 0 \times 00000000000000000000
[x19]
  [x20]
  [x21]
  [x22]
  [x23]
  x24
  x25
x26]
  [x27]
x28
  [x29]
  [x30]
  [x31] = 0x00000000000000000
ALUcontrol: 0x2
```

immediate: 0x00000000000000000

```
instruction = 0xxxxxxxx
Time:
      3960, CLK = 1, PC = 0 \times 00000310
[x0] = 0x00000000000000000
[x2]
  [x3]
  \times 4
[x5] = 0xfffffffffffffff
x6]
  x7
x8
  [x9] = 0x00000000000000000
[x11]
  = 0 \times 00000000000000000
x12
  [x13]
  x14
  x15]
  x 16]
  [x17]
  x18]
  = 0 \times 00000000000000000
x19]
  x20]
  x21
  x22
  [x23]
  x24
  [x25]
  x26
  [x27]
  x28
[x29]
  [x30] = 0x00000000000000006
[x31] = 0x00000000000000000
ALUcontrol: 0x2
immediate: 0x00000000000000000
instruction = 0xxxxxxxx
      3980, CLK = 1, PC = 0 \times 00000314
Time:
[x0] = 0x00000000000000000
  [x1]
[x2]
  [x3]
  \times 4
[x5] = 0x ffffffffffffffff
  x6]
[x7]
  [x8]
  [x9]
```

```
[x10] = 0xfffffffffffffff
[x11] = 0x0000000000000000
[x12] = 0x000000000000000004
[x13] = 0x000000000000000000
x16] = 0x0000000000000000b
x17] = 0x0000000000000012
[x18] = 0x0000000000000000
x19] = 0x00000000000000013
[x20] = 0x000000000000000004
[x21] = 0x00000000000000004
[x22] = 0x00000000000000016
x23] = 0x000000000000017
x24] = 0x00000000000000002d
[x25] = 0x00000000000000000
[x26] = 0x00000000000000000
[x27] = 0x00000000000000004
x29 = 0x00000000000000000
[x30] = 0x00000000000000006
[x31] = 0x00000000000000000
$stop called at time: 4 us: File "D:/Study/SJTU/Junior/2021SU/
  VE370/Project/p2/group/src/testbench.v" Line 21
```

# Appendix

#### Main

```
`timescale 1ns / 1ps
`include "alu_control.v"
`include "alu.v"
`include "control.v"
`include "data_memory.v"
`include "immediate_generator.v"
`include "instru_memory.v"
`include "next_pc.v"
`include "program_counter.v"
`include "register.v"
`include "IF_ID.v"
`include "ID_EX.v"
`include "EXMEM.v"
`include "MEMWB.v"
`include "mux.v"
module main(input clk);
    wire [31:0] pc_in, pc_out,
```

```
IF_ID_pc,
            ID_EX_pc,
            EX_MEM_pc;
wire [6:0] im_funct7;
wire
     [2:0] im_funct3;
wire
    [6:0] im_opcode;
wire [31:0] im_instru;
wire [31:0] IF_ID_instru;
wire c_Branch, c_MemRead, c_MemtoReg, c_MemWrite, c_ALUSrc,
   c_RegWrite, c_Jump,
     ID_EX_Branch, ID_EX_MemRead, ID_EX_MemtoReg,
        ID_EX_MemWrite, ID_EX_ALUSrc, ID_EX_RegWrite,
        ID_EX_Jump,
     EX_MEM_Branch, EX_MEM_MemRead, EX_MEM_MemtoReg,
        EX_MEM_MemWrite, EX_MEM_RegWrite, EX_MEM_Jump,
     MEM_WB_RegWrite, MEM_WB_MemtoReg, MEM_WB_MemRead;
wire [1:0] c_ALUOp;
wire [3:0] c_ALUcontrol;
wire [3:0] ID_EX_ALUcontrol;
wire [63:0] r_wbdata,
            mem_data,
            MEM_WB_mem_data,
            r_read1,
            r_read2,
            ID_EX_Read1,
            ID_EX_Read2,
            EX_MEM_Read2;
wire [4:0] ID_EX_Rs1,
           ID_EX_Rs2,
           ID_EX_Rd,
           EX_MEM_Rd,
           MEM_WB_Rd;
wire [63:0] ALUin1, ALUin2;
wire [63:0] MemWriteData;
wire c_zero,
     EX_MEM_zero;
wire [63:0] alu_result,
            EX_MEM_alu_result,
            MEM_WB_alu_result;
```

```
wire [63:0] imme,
                ID_EX_imme,
                EX_MEM_imme;
    wire [63:0] zeros;
    wire [1:0] ForwardA, ForwardB; // for ALU
    wire MemSrc; // for load-store
    wire IF_Flush, ID_EX_Flush;
    wire ID_branch, RegWrite, ID_jump, PCWrite, IF_ID_Write;
    // wire ID_equal;
    wire [4:0] IF_ID_Rs1, IF_ID_Rs2, EX_MEM_Rs2;
    assign zeros = 64'b0;
/* IF */
program_counter asset_pc(clk, pc_in, pc_out);
instru_memory asset_im(pc_out, im_instru);
IF_ID asset_if_id (clk, IF_Flush, IF_ID_Write, pc_out, im_instru,
   IF_ID_pc , im_funct7 , im_funct3 , im_opcode , IF_ID_instru);
/* ID */
control asset_control(im_opcode, c_Branch, c_MemRead, c_MemtoReg
   , c_ALUOp, c_MemWrite, c_ALUSrc, c_RegWrite, c_Jump);
alu_control asset_aluct(clk, c_ALUOp, im_funct7, im_funct3,
  c_ALUcontrol);
ID_EX_control asset_id_ex_ctrl(clk, ID_EX_Flush, c_Branch,
  c_MemRead, c_MemtoReg, c_MemWrite, c_ALUSrc, c_RegWrite,
  c_Jump, c_ALUcontrol,
ID_EX_Branch, ID_EX_MemRead, ID_EX_MemtoReg, ID_EX_MemWrite,
  ID_EX_ALUSrc, ID_EX_RegWrite, ID_EX_Jump, ID_EX_ALUcontrol);
immediate_generator asset_ig(clk, IF_ID_instru, im_opcode, imme)
ID_EX_imme asset_id_ex_imme(clk, ID_EX_Flush, imme, ID_EX_imme);
register asset_reg(clk, MEM_WB_RegWrite, c_Branch, c_Jump,
  IF_ID_pc , IF_ID_instru , MEM_WB_Rd , r_wbdata , r_read1 , r_read2
  );
ID_EX asset_id_ex(clk, ID_EX_Flush, r_read1, r_read2,
   IF_ID_instru, ID_EX_Read1, ID_EX_Read2, ID_EX_Rs1, ID_EX_Rs2,
   ID_EX_Rd);
```

```
ID_EX_pc asset_ID_EX_pc(clk, ID_EX_Flush, IF_ID_pc, ID_EX_pc);
/* EX */
two_64bit_mux asset_alu_mux1 (ForwardA, ID_EX_Read1, r_wbdata,
   EX_MEM_alu_result, zeros, ALUin1);
two_64bit_mux asset_alu_mux2 (ForwardB, ID_EX_Read2, r_wbdata,
   EX_MEM_alu_result, zeros, ALUin2);
ForwardingUnit assert_forwarding_unit(ID_EX_Rs1, ID_EX_Rs2,
  EX_MEM_Rs2, EX_MEM_Rd, MEM_WB_Rd, EX_MEM_RegWrite,
   MEM_WB_RegWrite, EX_MEM_MemWrite, MEM_WB_MemRead, ForwardA,
   ForwardB, MemSrc);
alu asset_alu(ID_EX_ALUSrc, ID_EX_ALUcontrol, ALUin1, ALUin2,
   ID_EX_imme, c_zero, alu_result);
EXMEM asset_ex_mem(clk, c_zero, alu_result, ID_EX_Read2,
   ID_EX_Rd, ID_EX_Rs2, EX_MEM_zero, EX_MEM_alu_result,
   EX_MEM_Read2, EX_MEM_Rd, EX_MEM_Rs2);
EX_MEM_control asset_ex_mem_ctrl(clk, ID_EX_Branch,
   ID_EX_MemRead, ID_EX_MemtoReg, ID_EX_MemWrite, ID_EX_RegWrite
ID_EX_Jump, EX_MEM_Branch, EX_MEM_MemRead, EX_MEM_MemtoReg,
   EX_MEM_MemWrite, EX_MEM_RegWrite, EX_MEM_Jump);
EX_MEM_imme asset_ex_mem_imme(clk, ID_EX_imme, EX_MEM_imme);
EX_MEM_pc asset_EX_MEM_pc(clk, ID_EX_pc, EX_MEM_pc);
/* MEM */
next_pc asset_next_pc(clk, EX_MEM_Jump, EX_MEM_Branch,
   EX_MEM_zero, PCWrite, pc_out, EX_MEM_alu_result, EX_MEM_imme,
    pc_in);
one_64bit_mux asset_mem_wb_memsrc_mux(MemSrc, EX_MEM_Read2,
   mem_data, MemWriteData);
data_memory asset_dm(clk, EX_MEM_MemWrite, EX_MEM_MemRead,
   EX_MEM_alu_result, MemWriteData, mem_data);
MEMWB asset_mem_wb(clk, mem_data, EX_MEM_alu_result, EX_MEM_Rd,
    MEM_WB_mem_data, MEM_WB_alu_result, MEM_WB_Rd);
MEM_WB_control asset_mem_wb_ctrl(clk, EX_MEM_MemtoReg,
   EX_MEM_RegWrite, EX_MEM_MemRead, MEM_WB_MemtoReg,
   MEM_WB_RegWrite, MEM_WB_MemRead);
```

```
one_64bit_mux asset_mem_wb_mux(MEM_WB_MemtoReg,
    MEM_WB_alu_result, MEM_WB_mem_data, r_wbdata);
assign IF_ID_Rs1 = IF_ID_instru[19:15];
assign IF_ID_Rs2 = IF_ID_instru[24:20];

/* Hazard Detection Unit */
hazardControlDetection asset_hazardControlDetection(IF_ID_Rs1,
    IF_ID_Rs2, ID_EX_Rs2, EX_MEM_Rs2, ID_EX_Rd, EX_MEM_Rd,
    c_MemWrite,
ID_EX_MemRead, EX_MEM_MemRead, ID_EX_Branch, ID_EX_Jump, c_zero,
    ID_EX_RegWrite, PCWrite, IF_ID_Write, ID_EX_Flush, IF_Flush)
;
endmodule
```

#### **Hazard Detection Unit**

```
`timescale 1ns / 1ps
module hazardControlDetection(
    input [4:0] IF_ID_Rs1,
          [4:0] IF_ID_Rs2,
    input
          [4:0] ID<sub>EX_Rs2</sub>,
    input
    input
          [4:0] EX_MEM_Rs2,
    input [4:0] ID_EX_Rd,
    input [4:0] EX_MEM_Rd,
    input IF_ID_MemWrite,
    input ID_EX_MemRead,
    input EX_MEM_MemRead,
    input ID_branch,
    input ID_jump,
    input c_zero,
    input ID_EX_RegWrite,
    output PCWrite,
    output IF_ID_Write,
    output ID_EX_Flush,
    output IF_Flush
);
wire PCHold; // if PCHold==1, hold PC and IF/ID
assign PCHold = ( (ID_EX_MemRead && ~IF_ID_MemWrite) && (
  ID_EX_Rd = IF_ID_Rs1 \mid ID_EX_Rd = IF_ID_Rs2 \rangle / lw
   hazard
             | ( (ID_branch) && (ID_EX_MemRead) && (ID_EX_Rd =
               IF_ID_Rs1 \mid ID_EX_Rd = IF_ID_Rs2 \rangle / lw
               followed by branch
             || ( (ID_branch) && (EX_MEM_MemRead) && (EX_MEM_Rd
```

```
= IF_ID_Rs1 || EX_MEM_Rd = IF_ID_Rs2) | // lw
               followed by nop and then branch
            | ( (ID_branch) && (ID_EX_RegWrite) && (ID_EX_Rd !=
                5'b0) && (ID_EX_Rd == IF_ID_Rs1 || ID_EX_Rd ==
               IF_ID_Rs2) ) // R-format followed by branch
            | ( (ID_branch) && (ID_EX_RegWrite) && (ID_EX_Rd =
                5'b0) && (ID_EX_Rd == IF_ID_Rs1 || ID_EX_Rd ==
               IF_ID_Rs2) ); // addi followed by branch
// note we leave out the case that R-format followed by a nop
   then a branch, because that is solved by forwarding path
assign PCWrite=~PCHold; // if PCWrite==0, don't write in new
   instruction, IM decode the current instruction again
assign IF_ID_Write=~PCHold; // if IF_ID_Write==0, IF/ID register
    keeps the current instruction
assign \ \ ID\_EX\_Flush=PCHold; \ // \ if \ ID\_EX\_Flush=1, \ all \ control
   signals in ID/EX are 0 (implemented in ID/EX register later)
assign IF_Flush = (PCHold==0) && ( (ID_jump) || (ID_branch &&
   c_zero));
endmodule
```

#### $IF_{ID}$

```
`timescale 1ns / 1ps
module IF_ID (
    input clock,
    input IF_flush,
    input IF_ID_Write,
    input [31:0] pc,
    input [31:0] in,
    output reg [31:0] pc_next,
                       funct7, // [31-25]
    output reg [6:0]
                       funct3, // [14-12]
    output reg [2:0]
                       opcode, //[6-0]
    output reg [6:0]
    output reg [31:0] instru // /31-0/
);
initial
begin
    pc_next \ll 0:
    instru \ll 0;
    funct7 \ll 0;
    funct3 \ll 0;
    opcode \le 0;
end
```

```
always @(posedge clock)
begin
     if (IF_flush = 1) begin
         pc_next \ll 0;
         instru \ll 0;
         funct7 \ll 0;
         funct3 \ll 0;
         opcode \ll 0;
     end else if (IF_ID_Write == 1) begin
         pc_next <= pc;</pre>
         instru <= in;
         funct7 <= in[31:25];
         funct3 \le in [14:12];
         opcode \leq in [6:0];
     end else begin
         pc_next <= pc_next;</pre>
         instru <= instru;</pre>
         funct7 \ll funct7;
         funct3 \le funct3;
         opcode <= opcode;
     end
     // $display("funct7 = 0x%H", funct7);
     // $display("funct3 = 0x\%H", funct3);
// $display("opcode = 0x\%H", opcode);
end
endmodule
```

#### $ID_EX$

```
`timescale 1ns / 1ps
module ID_EX(
    input clock,
    input ID_flush,
    input [63:0] ReadDataIn1,
    input [63:0] ReadDataIn2,
    input [31:0] instru,
    output reg [63:0] ReadDataOut1,
    output reg [63:0] ReadDataOut2,
    output reg [4:0] Rs1,
    output reg [4:0] Rs2,
    output reg [4:0] Rd
);
initial
begin
    ReadDataOut1 = 0;
```

```
ReadDataOut2 = 0;
    Rs1 = 0;
    Rs2 = 0;
    Rd = 0;
end
always @(posedge clock)
begin
    if (ID_flush = 1) begin
         ReadDataOut1 = 0;
         ReadDataOut2 = 0;
         Rs1 = 0:
         Rs2 = 0;
         Rd = 0:
    end else begin
         ReadDataOut1 = ReadDataIn1;
         ReadDataOut2 = ReadDataIn2;
         Rs1 = instru[19:15];
         Rs2 = instru[24:20];
         Rd = instru[11:7];
    end
    // $display("ID_flush = 0x%H", ID_flush);
    // $display("ReadDataOut1 = 0x%H", ReadDataOut1);
// $display("ReadDataOut2 = 0x%H", ReadDataOut2);
    // $display("Rs1 = 0x\%H", Rs1);
// $display("Rs2 = 0x\%H", Rs2);
    // \$ display("Rd = 0x\%H", Rd);
end
endmodule
module ID_EX_control(
    input clock,
    input ID_flush,
    input i_Branch,
    input i_MemRead,
    input i_MemtoReg,
    input i_MemWrite,
    input i_ALUSrc,
    input i_RegWrite,
    input i_Jump,
    input [3:0] i_ALUcontrol,
    output reg o_Branch,
    output reg o_MemRead,
    output reg o_MemtoReg,
    output reg o_MemWrite,
    output reg o_ALUSrc,
    output reg o_RegWrite,
    output reg o_Jump,
```

```
output reg [3:0] o_ALUcontrol
);
    initial
    begin
         o_Branch = 0;
         o_{\text{MemRead}} = 0;
         o_{\text{MemtoReg}} = 0;
         o_{\text{MemWrite}} = 0;
         o_ALUSrc = 0;
         o_RegWrite = 0;
         o_Jump = 0;
         o_ALUcontrol = 0;
    end
    always @(posedge clock)
    begin
         if (ID_flush = 0) begin
             o_Branch = i_Branch;
             o_{-}MemRead = i_{-}MemRead;
             o_MemtoReg = i_MemtoReg;
             o_MemWrite = i_MemWrite;
             o_ALUSrc = i_ALUSrc;
             o_RegWrite = i_RegWrite;
             o_Jump = i_Jump;
             o_ALUcontrol = i_ALUcontrol;
         end else begin
             o_Branch = 0;
             o_{\text{-}}MemRead = 0;
             o_{\text{-}}MemtoReg = 0;
             o_{\text{-}}MemWrite = 0;
             o_ALUSrc = 0;
             o_RegWrite = 0;
             o_Jump = 0;
             o_ALUcontrol = 0;
         end
    end
endmodule
module ID_EX_imme(
    input clock,
    input ID_flush,
    input [63:0] immediate,
    output reg [63:0] immediate_out
);
    initial
    begin
```

```
immediate_out = 0;
    end
    always @(posedge clock)
    begin
        if (ID_flush = 0) begin
             immediate_out = immediate;
        end else begin
             immediate_out = 0;
        end
        // $display("ID_flush: 0x%H", ID_flush);
        // $display("immediate: 0x%H", immediate);
        // $display("immediate_out: 0x%H", immediate_out);
    end
endmodule
module ID_EX_pc(
    input clock,
    input ID_flush,
    input [31:0] pc,
    output reg [31:0] pc_next
);
initial
begin
    pc_next = 0;
end
always @(posedge clock)
begin
    if (ID_{-}flush = 0) begin
        pc_next = pc;
    end else begin
         pc_next = 0;
    end
end
endmodule
\mathbf{EX}_{-}\mathbf{MEM}
`timescale 1ns / 1ps
module EXMEM(
    input clock,
    input Zero,
    input [63:0] ALUresultIn,
```

```
input [63:0] ReadData2In,
    input [4:0] RegisterRd,
    input [4:0] RegisterRs2,
    output reg ZeroOut,
    output reg [63:0] ALUresultOut,
    output reg [63:0] ReadData2Out,
    output reg [4:0] Rd,
    output reg [4:0] Rs2
);
initial
begin
    ZeroOut \le 0;
    ALUresultOut \ll 0;
    ReadData2Out \leq 0;
    Rd \ll 0;
    Rs2 <= 0;
end
always @(posedge clock)
begin
    ZeroOut <= Zero;
    ALUresultOut <= ALUresultIn;
    ReadData2Out <= ReadData2In;
    Rd <= RegisterRd:
    Rs2 <= RegisterRs2;
    // $\$display("Zero = \(0x\%H\)", \(ZeroOut\);
    // display("ALUresult = 0x\%H", ALUresultOut);
// display("ReadData2 = 0x\%H", ReadData2Out);
    // $display("Rd = 0x%H", Rd);
    // $display("RegisterRs2 = 0x%H", Rs2);
end
endmodule
module EX_MEM_control(
    input clock,
    input i_Branch,
    input i_MemRead,
    input i_MemtoReg,
    input i_MemWrite,
    input i_RegWrite,
    input i_Jump,
    output reg o_Branch,
    output reg o_MemRead,
    output reg o_MemtoReg,
    output reg o_MemWrite,
    output reg o_RegWrite,
```

```
output reg o_Jump
);
initial
begin
    o_Branch \ll 0;
    o_{\text{-}}MemRead \le 0;
    o_{\text{-}}MemtoReg \ll 0;
    o_{\text{MemWrite}} \ll 0;
    o_RegWrite \ll 0;
    o_Jump \ll 0;
end
always @(posedge clock)
begin
    o_Branch <= i_Branch;
    o_MemRead <= i_MemRead;
    o_MemtoReg <= i_MemtoReg;
    o_MemWrite <= i_MemWrite;
    o_RegWrite <= i_RegWrite;
    o_Jump <= i_Jump;
end
endmodule
module EX_MEM_imme(
    input clock,
    input [63:0] immediate,
    output reg [63:0] immediate_out
);
initial
begin
    immediate_out <= 0;
end
always @(posedge clock)
begin
    immediate_out <= immediate;</pre>
end
endmodule
module EX_MEM_pc(
    input clock,
    input [31:0] pc,
    output reg [31:0] pc_next
);
```

```
initial
begin
    pc_next <= 0;
end

always @(posedge clock)
begin
    pc_next <= pc;
end

endmodule</pre>
```

#### $MEM_WB$

```
`timescale 1ns / 1ps
module MEMWB(
    input clock,
    input [63:0] MemoryDataIn,
    input [63:0] AluResultIn,
    input [4:0] RegisterRd,
    output reg [63:0] MemoryDataOut,
    output reg [63:0] AluResultOut,
    output reg [4:0] Rd
);
initial
begin
    MemoryDataOut \le 0;
    AluResultOut \le 0;
    Rd \ll 0;
end
always @(posedge clock)
begin
    MemoryDataOut = MemoryDataIn;
    AluResultOut = AluResultIn;
    Rd = RegisterRd;
end
endmodule
module MEM_WB_control(
    input clock,
    input i_MemtoReg,
    input i_RegWrite,
    input i_MemRead,
    output reg o_MemtoReg,
    output reg o_RegWrite,
    output reg o_MemRead
```

```
);
initial
begin
    o_{\text{MemtoReg}} \ll 0;
    o_RegWrite \ll 0;
    o_{\text{-}}MemRead \le 0;
end
always @(posedge clock)
begin
    o_MemtoReg <= i_MemtoReg;
    o_RegWrite <= i_RegWrite;
    o_MemRead <= i_MemRead;
    // $display("MEM\_WB\_RegWrite: 0x\%H", o\_RegWrite);
    // $\$display("MEM_WB_MemRead: 0x%H", o_MemRead);
end
endmodule
```

### Common Register

```
timescale 1ns / 1ps

module common_reg(
    input clock,
    input in,
    output reg out
);

initial
begin
    out <= 0;
end

always @(posedge clock)
begin
    out <= in;
end
endmodule</pre>
```

# Forwarding Unit

```
timescale 1ns / 1ps
module ForwardingUnit(
  input [4:0] ID_EX_Rs1,
  input [4:0] ID_EX_Rs2,
  input [4:0] EX_MEM_Rs2,
```

```
input [4:0] EX_MEM_Rd,
    input [4:0] MEM_WB_Rd,
    input EX_MEM_RegWrite,
    input MEM_WB_RegWrite,
    input EX_MEM_MemWrite,
    input MEM_WB_MemRead,
    output reg [1:0] ForwardA, // for ALU
    output reg [1:0] ForwardB, // for ALU
    output reg MemSrc // for load-store
);
initial begin
    ForwardA \leq 2'b00;
    ForwardB \leq 2'b00;
    MemSrc \le 1'b0;
end
// ForwardA
always @(*) begin
    if (EX_MEM_RegWrite == 1 && EX_MEM_Rd != 0 && EX_MEM_Rd ==
       ID_EX_Rs1) ForwardA <= 2'b10; //EX
    else if (MEM_WB_RegWrite == 1 && MEM_WB_Rd != 0 && MEM_WB_Rd
       = ID_EX_Rs1) ForwardA <= 2'b01; //MEM
    else ForwardA <= 2'b00;
end
// ForwardB
always @(*) begin
    if (EX_MEM_RegWrite == 1 && EX_MEM_Rd != 0 && EX_MEM_Rd ==
       ID_EX_Rs2) ForwardB <= 2'b10; //EX
    else if (MEM_WB_RegWrite == 1 && MEM_WB_Rd != 0 && MEM_WB_Rd
        = ID_EX_Rs2) ForwardB <= 2'b01; //MEM
    else ForwardB \leq 2'b00;
end
// MemSrc
always @(*) begin
    if (EX\_MEM\_MemWrite = 1 \&\& MEM\_WB\_MemRead = 1 \&\& (
      MEM_WB_Rd == EX_MEM_Rs2)) MemSrc <= 1'b1;
    else MemSrc <= 1'b0;
end
endmodule
```

### Control Unit

```
`timescale 1ns / 1ps
module control (
  input [6:0] opcode,
```

```
output reg Branch,
  output reg MemRead,
  output reg MemtoReg,
  output reg [1:0] ALUOp,
  output reg MemWrite,
  output reg ALUSrc,
  output reg RegWrite,
  output reg Jump
);
  initial begin
    Branch = 0;
    MemRead = 0;
    MemtoReg = 0;
    ALUOp = 2'b00;
    MemWrite = 0;
    ALUSrc = 0;
    RegWrite = 0;
    Jump = 0;
  end
  always @(*) begin
    case (opcode)
      7'b0110011: begin // R-type
        Branch = 0;
        MemRead = 0;
        MemtoReg = 0;
        ALUOp = 2'b10;
        MemWrite = 0;
        ALUSrc = 0;
        RegWrite = 1;
        Jump = 0;
      end
      7'b0000011: begin // Load
        Branch = 0;
        MemRead = 1;
        MemtoReg = 1;
        ALUOp = 2'b00;
        MemWrite = 0;
        ALUSrc = 1;
        RegWrite = 1;
        Jump = 0;
      7'b0010011: begin // immediate
        Branch = 0;
        MemRead = 0;
        MemtoReg = 0;
        ALUOp = 2'b10;
        MemWrite = 0;
        ALUSrc = 1;
```

```
RegWrite = 1;
    Jump = 0;
  end
  7'b0100011: begin // S-type
    Branch = 0;
    MemRead = 0;
    MemtoReg = 0;
    ALUOp = 2'b00;
    MemWrite = 1;
    ALUSrc = 1;
    RegWrite = 0;
    Jump = 0;
  end
  7'b1100111: begin // jalr
    Branch = 0;
    MemRead = 0;
    MemtoReg = 0;
    ALUOp = 2'b11;
    MemWrite = 0;
    ALUSrc = 1;
    RegWrite = 1;
    Jump = 1;
  end
  7'b1100011: begin // SB-type
    Branch = 1;
    MemRead = 0;
    MemtoReg = 0;
    ALUOp = 2'b01;
    MemWrite = 0;
    ALUSrc = 0;
    RegWrite = 0;
    Jump = 0;
  end
  7'b1101111: begin // UJ-type
    Branch = 1;
    MemRead = 0;
    MemtoReg = 0;
    ALUOp = 2'b11;
    MemWrite = 0;
    ALUSrc = 0;
    RegWrite = 1;
    Jump = 1;
  end
  default:
endcase
// $\shit ay("Branch = \%H; MemRead = \%H; MemtoReg = \%H; ALUOp =
  %H; Mem Write = %H; ALUSrc = %H; Reg Write = %H; Jump = %H; ",
    // Branch, MemRead, MemtoReg, ALUOp, MemWrite, ALUSrc,
```

```
RegWrite\;,\;\; Jump)\;; end endmodule
```

### Mux

```
`timescale 1ns / 1ps
module one_64bit_mux(
  input source,
  input [63:0] input0,
  input [63:0] input1,
  output reg [63:0] out
);
initial begin
    out <= 64'b0;
end
always @(*) begin
    case (source)
        1'b0:
            out = input0;
        1'b1:
            out = input1;
    endcase
end
endmodule
module two_64bit_mux(
  input [1:0] source,
  input [63:0] input0,
  input [63:0] input1,
  input [63:0] input2,
  input [63:0] input3,
  output reg [63:0] out
);
initial begin
    out <= 64'b0;
end
always @(*) begin
    case (source)
        2 b00:
            out = input0;
        2'b01:
            out = input1;
```

#### ImmGen

```
`timescale 1ns / 1ps
module immediate_generator(
 input clock,
 input [31:0] instru,
 input [6:0] opcode,
 output reg [63:0] immediate
);
  parameter NEG = 64'
    initial begin
   immediate = 64'b0;
  end
  always @(negedge clock) begin
   case (opcode)
     7'b0000011: begin // Load
       if (instru[31] == 1) begin
         immediate = \{NEG[51:0], instru[31:20]\};
       end else begin
         immediate = \{52'b0, instru[31:20]\};
       end
     end
     7'b0010011: begin // immediate
       if (instru[31] == 1) begin
         immediate = {NEG[51:0], instru[31:20]};
       end else begin
         immediate = \{52'b0, instru[31:20]\};
       end
     end
     7'b0100011: begin // S-type
       if (instru[31] = 1) begin
         immediate = \{NEG[51:0], instru[31:25], instru[11:7]\};
       end else begin
         immediate = \{52'b0, instru[31:25], instru[11:7]\};
       end
```

```
end
      7'b1100111: begin // jalr
         if (instru[31] == 1) begin
          immediate = \{NEG[51:0], instru[31:20]\};
        end else begin
          immediate = \{52'b0, instru[31:20]\};
        end
      end
      7'b1100011: begin // SB-type
         if (instru[31] == 1) begin
          immediate = {NEG[50:0], instru[31], instru[7], instru}
              [30:25], instru[11:8], 1'b0};
        end else begin
           immediate = {51'b0, instru[31], instru[7], instru
              [30:25], instru[11:8], 1'b0};
        end
      end
      7'b1101111: begin // UJ-type
         if (instru[31] == 1) begin
           immediate = \{NEG[42:0], instru[31], instru[19:12],
              instru[20], instru[30:21], 1'b0};
        end else begin
           immediate = \{43'b0, instru[31], instru[19:12], instru
              [20], instru[30:21], 1'b0};
        end
      end
      default:
        immediate = 64'b0;
    endcase
    $\frac{1}{3}\text{display} (\text{"immediate: } 0\text{M"}, \text{immediate} [63:0]);
  end
endmodule
```

### Registers

```
module register (
input clk,
input RegWrite,
input Branch,
input Jump,
input [31:0] pc,
input [31:0] instru,
input [4:0] rd,
input [63:0] WriteData,
output [63:0] ReadData1,
output [63:0] ReadData2
```

```
);
  reg [63:0] RegData [31:0]; // register data
  // initialize the regester data
  integer i;
  initial begin
    for (i = 0; i < 32; i = i + 1) begin
      RegData[i] = 64'b0;
    end
  end
  assign ReadData1 = RegData[instru[19:15]];
  assign ReadData2 = RegData[instru[24:20]];
  always @(negedge clk) begin
    if (RegWrite == 1) begin
      if (Branch == 1 & Jump == 1 & instru[11:7] != 5'b0) begin
        RegData[instru[11:7]] = \{32'b0, pc + 4\};
      end else if (rd != 5'b0) begin
        RegData[rd] = WriteData;
      end
    end
  end
endmodule
```

# **ALU Control**

```
`timescale 1ns / 1ps
module alu_control(
  input clock,
  input [1:0] ALUOp,
  input [6:0] funct 7,
  input [2:0] funct3,
  output reg [3:0] ALUcontrol
);
  always @(negedge clock) begin
    case (ALUOp)
      2'b00: // add
        ALUcontrol = 4'b0010;
      2'b01: begin
        case (funct3)
          3'b000: // beq sub
            ALUcontrol = 4'b0110;
          3'b001: // bne
            ALUcontrol = 4'b0011;
          3'b100: // blt
```

```
ALUcontrol = 4'b1000;
          3'b101: // bge
            ALUcontrol = 4'b0111;
          default: // ADD
            ALUcontrol = 4'b0010;
        endcase
      end
      2'b10: begin
        case (funct3)
          3'b000: begin // add
            case (funct7)
               7'b0000000: // add
                 ALUcontrol = 4'b0010;
               7'b0100000: // sub
                 ALUcontrol = 4'b0110;
               default: // add
                 ALUcontrol = 4'b0010;
            endcase
          end
          3'b001: // sll
            ALUcontrol = 4'b0100;
          3'b010: // slt
            ALUcontrol = 4'b0111;
          3'b100: // or
            ALUcontrol = 4'b1001;
          3'b110: // or
            ALUcontrol = 4'b0001;
          3'b111: // and
            ALUcontrol = 4'b0000;
          default: begin
            case (funct7)
               7'b1101111: // add
                 ALUcontrol = 4'b0010;
               default:
                 ALUcontrol = ALUcontrol;
            endcase
          end
        endcase
      end
      2'b11: // ADD
        ALUcontrol = 4'b0010;
    endcase
    $\frac{1}{3}\display("ALUcontrol: 0x\hat{H}", ALUcontrol);
  end
endmodule
```

ALU

```
`timescale 1ns / 1ps
module alu (
  input ALUSrc,
  input [3:0] ALUcontrol,
  input [63:0] data1,
  input [63:0] read2,
  input [63:0] imme,
  output reg zero,
  output reg [63:0] ALUresult
);
  reg [63:0] data2;
  always @(*) begin
    if (ALUSrc = 0) begin
      data2 = read2;
    end else begin
      data2 = imme;
    end
  end
  always @(*) begin
    case (ALUcontrol)
      4'b0000: // AND
        ALUresult = data1 \& data2;
      4'b0001: // OR
        ALUresult = data1 \mid data2;
      4'b0010: // ADD
        ALUresult = data1 + data2;
      4'b0011: // NEQ
        ALUresult = (data1 = data2) ? 1 : 0;
      4'b0100: // SLL
        ALUresult = data1 << data2;
      4'b0110: // SUB
        ALUresult = data1 - data2;
      4'b0111: // SLT
        ALUresult = (data1 < data2) ? 1 : 0;
      4'b1000: // SGE
        ALUresult = (data1 < data2) ? 0 : 1;
      4'b1001: // XOR
        ALUresult = data1 \hat{data2};
      4'b1100: // NOR
        ALUresult = data1 \mid ^{\sim} data2;
      default:
        ALUresult = 64'b0;
    endcase
    if (ALUresult = 0) begin
      zero = 1;
```

```
end else begin
    zero = 0;
end

// $display("ALUSrc: 0x%H", ALUSrc);
    // $display("ALUcontrol: 0x%H", ALUcontrol);
    // $display("data1: 0x%H", data1);
    // $display("data2: 0x%H", data2);
    // $display("ALUresult: 0x%H", ALUresult);
    // $display("Zero: 0x%H", zero);
end

endmodule
```

# InstructionMemory

```
`timescale 1ns / 1ps
module instru_memory(
  input [31:0] addr,
  output reg [31:0] instru // [31-0]
);
  parameter NOP = 32'b000000000000000000000000000011;
  parameter SIZE = 128;
  reg [31:0] mem [SIZE - 1:0];
  // initially set instruction to nop
  integer n;
  initial begin
    for (n = 0; n < SIZE; n = n + 1) begin
      mem[n] = NOP;
    end
    $readmemh("D:/Study/SJTU/Junior/2021SU/VE370/Project/p2/
       group/test/case.txt", mem);
       \$readmemh("E:/JI/3" JUNIOR/2021" summer/ve370/7-14
       hazardtest4.txt", mem);
    instru = NOP;
  end
  always @(addr) begin
    if (addr != -4) begin
      instru = mem[addr >> 2];
    end
    $\frac{1}{3}\display(\text{"instruction} = 0\text{x/H", instru});
  end
endmodule
```

### DataMemory

```
`timescale 1ns / 1ps
module data_memory(
  input clk,
  input MemWrite,
  input MemRead,
  input [63:0] ALUresult,
  input [63:0] writeData,
  output reg [63:0] readData
);
  parameter NONE = 64'b0;
  parameter SIZE = 128;
  reg [63:0] mem [SIZE - 1:0];
  // initially set default data to 0
  integer i;
  initial begin
    for (i = 0; i < SIZE; i = i + 1) begin
      mem[i] = NONE;
    end
  end
  // Write back Data Memory
  always @(*) begin
    if (MemRead == 1) begin
      readData = mem[ALUresult];
    end
  end
  // Write memory
  always @(negedge clk) begin
    if (MemWrite == 1) begin
      mem[ALUresult] = writeData;
    end
  end
endmodule
Next PC
```

```
"timescale 1ns / 1ps
module next_pc(
  input clock,
  input Jump,
  input Branch,
  input Zero,
```

```
input PCWrite,
  input [31:0] old,
  input [63:0] alu_result,
  input [63:0] immediate,
  output reg [31:0] next
);
  reg [31:0] new;
  reg [63:0] origin;
  reg [63:0] jump;
  initial begin
    next = 32'b0;
    origin = 64'b0;
  end
  always @(old) begin
    new = old + 4;
    origin = \{32'b0, old[31:0]\};
  end
  always @(immediate, origin) begin
    jump = origin + (immediate >> 1);
  end
  always @(posedge clock) begin
    // assign next program counter value
    if (PCWrite == 1) begin
      if (Branch = 1 \& (Zero = 1 | Jump = 1)) begin
        next = jump[31:0];
      end else begin
        next = new;
      end
      if (Jump = 1 \& Branch = 0) begin
        next = alu\_result[31:0];
      end
    end else begin
      next = next;
    end
    // $ display ("Jump = 0x%H", Jump);
    // $display("Branch = 0x%H", Branch);
// $display("Zero = 0x%H", Zero);
    // $\$display("PCWrite = 0x\%H", PCWrite);
    // $display("old = 0x%H", old);
    // $display("origin = 0x%H", origin);
    // $display("jump = 0x%H", jump);
    // $display("new = 0x%H", new);
    // $\$display("next = 0x\%H", next);
  end
```

# endmodule

# **Program Counter**

```
`timescale 1ns / 1ps

module program_counter(
  input clk,
  input [31:0] in,
  output reg [31:0] out
);

initial begin
  out = -4;
  end

always @(negedge clk) begin
  out = in;
  end

endmodule
```

#### **TestBench**

```
`timescale 1ns / 1ps
`include "main.v"
module testbench;
  integer currTime;
  reg clk;
  main uut (
    .clk (clk)
  initial begin
    #0
    clk = 0;
    \operatorname{currTime} = -10;
    uut.asset_pc.out = -4;
    $display("
       ");
    #2000 $display("
       ");
    #2000 $stop;
```

```
end
  always @(posedge clk) begin
    // indicating a posedge clk triggered
    $display("
        ");
    #1; // wait for writing back
    display("Time: %d, CLK = %d, PC = 0x%H", currTime, clk, uut
        .asset_pc.out);
     \begin{array}{lll} \mbox{$\$display("[x0] = 0x\%H", uut.asset\_reg.RegData[0]);} \\ \mbox{$\$display("[x1] = 0x\%H", uut.asset\_reg.RegData[1]);} \\ \end{array} 
    $display("[x2] = 0x%H", uut.asset_reg.RegData[2]);
$display("[x3] = 0x%H", uut.asset_reg.RegData[3]);
                     = 0x%H", uut.asset_reg.RegData[4]);
= 0x%H", uut.asset_reg.RegData[5]);
    $display("
                 x4
    $display("
                 x5
    $display("
                 [x6] = 0x%H", uut.asset_reg.RegData[6]);
                      = 0x%H", uut.asset_reg.RegData[7]);
= 0x%H", uut.asset_reg.RegData[8]);
    $display("
                 x7]
    $display("
                 x8]
    $display("
                      = 0x%H", uut.asset_reg.RegData[9]);
                 x9]
    $display("
                 [x10] = 0x\%H", uut.asset_reg.RegData[10]);
                       = 0x\%H", uut.asset_reg.RegData[11]);
    $display("
                  x11
    $display("
                 x12
                       = 0x\%H", uut.asset_reg.RegData[12]);
    $display("
                       = 0x\%H", uut.asset_reg.RegData[13]);
                  x13
    $display("
                 x14
                       = 0x\%H", uut.asset_reg.RegData[14]);
                       = 0x\%H", uut.asset_reg.RegData[15]);
    $display("
                  x15
    $display("
                       = 0x\%H", uut.asset_reg.RegData[16]);
                 x16
    $display("
                       = 0x%H", uut.asset_reg.RegData[17]);
                 x17
    $display("
                       = 0x\%H", uut.asset_reg.RegData[18]);
                 x18
                       = 0x%H", uut.asset_reg.RegData[19]);
    $display("
                 x19
    $display("
                 x20
                       = 0x\%H", uut.asset_reg.RegData[20]);
    $display("
                 x21
                       = 0x\%H", uut.asset_reg.RegData[21]);
    $display("
                 x22
                       = 0x\%H", uut.asset_reg.RegData[22]);
    $display("
                       = 0x\%H", uut.asset_reg.RegData[23]);
                 x23
    $display("
                 x24
                       = 0x\%H", uut.asset_reg.RegData[24]);
    $display("
                 x25
                       = 0x\%H", uut.asset_reg.RegData[25]);
    $display("
                       = 0x\%H",
                 x26
                                 uut.asset_reg.RegData[26]);
    $display("
                 x27
                       = 0x\%H", uut.asset_reg.RegData[27]);
    $display("
                 x28
                       = 0x\%H", uut.asset_reg.RegData[28]);
    $display("
                       = 0x\%H", uut.asset_reg.RegData[29]);
                 x29
    $display("
                       = 0x\%H", uut.asset_reg.RegData[30]);
                 x30
    display("[x31] = 0x\%H", uut.asset_reg.RegData[31]);
  end
  always #10 begin
    clk = clk;
    currTime = currTime + 10;
  end
endmodule
```