

VCC_IO

GND

DIR

STEP

SDO

SDI

SCK

CSN

GND

DRV_ENN

SRBL

CLK16

SG

TST

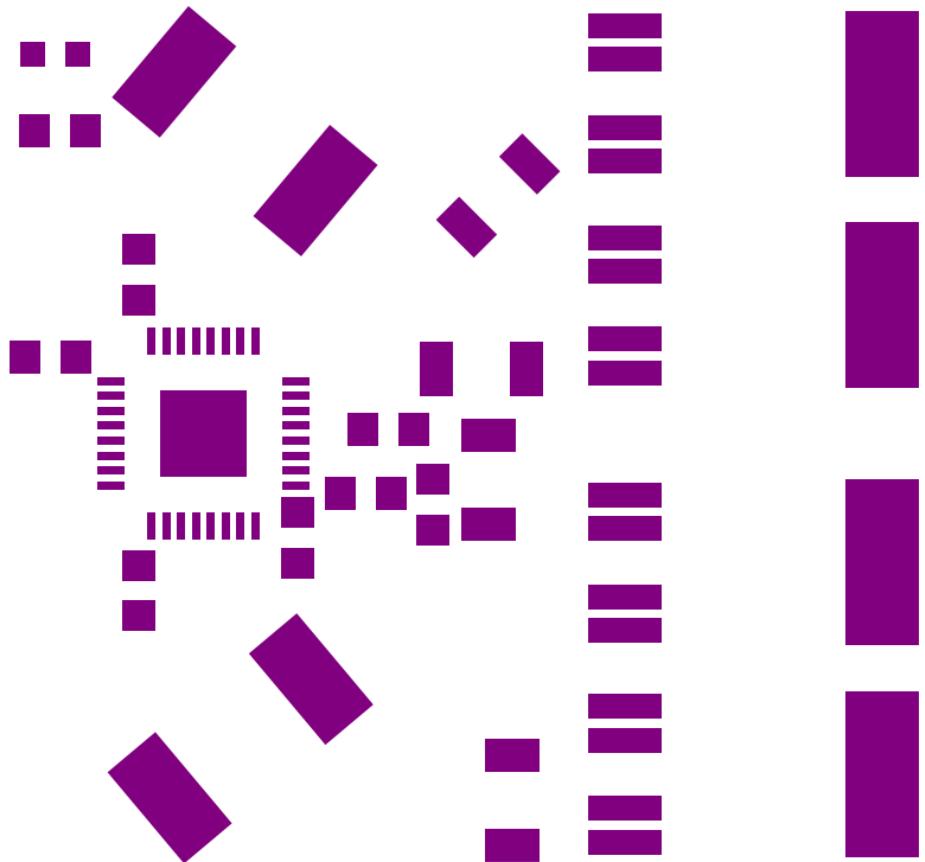
SRAL

TMC2590-B0B40
V1.0

5V GND S
12V GND D
12V GND B

6X GND

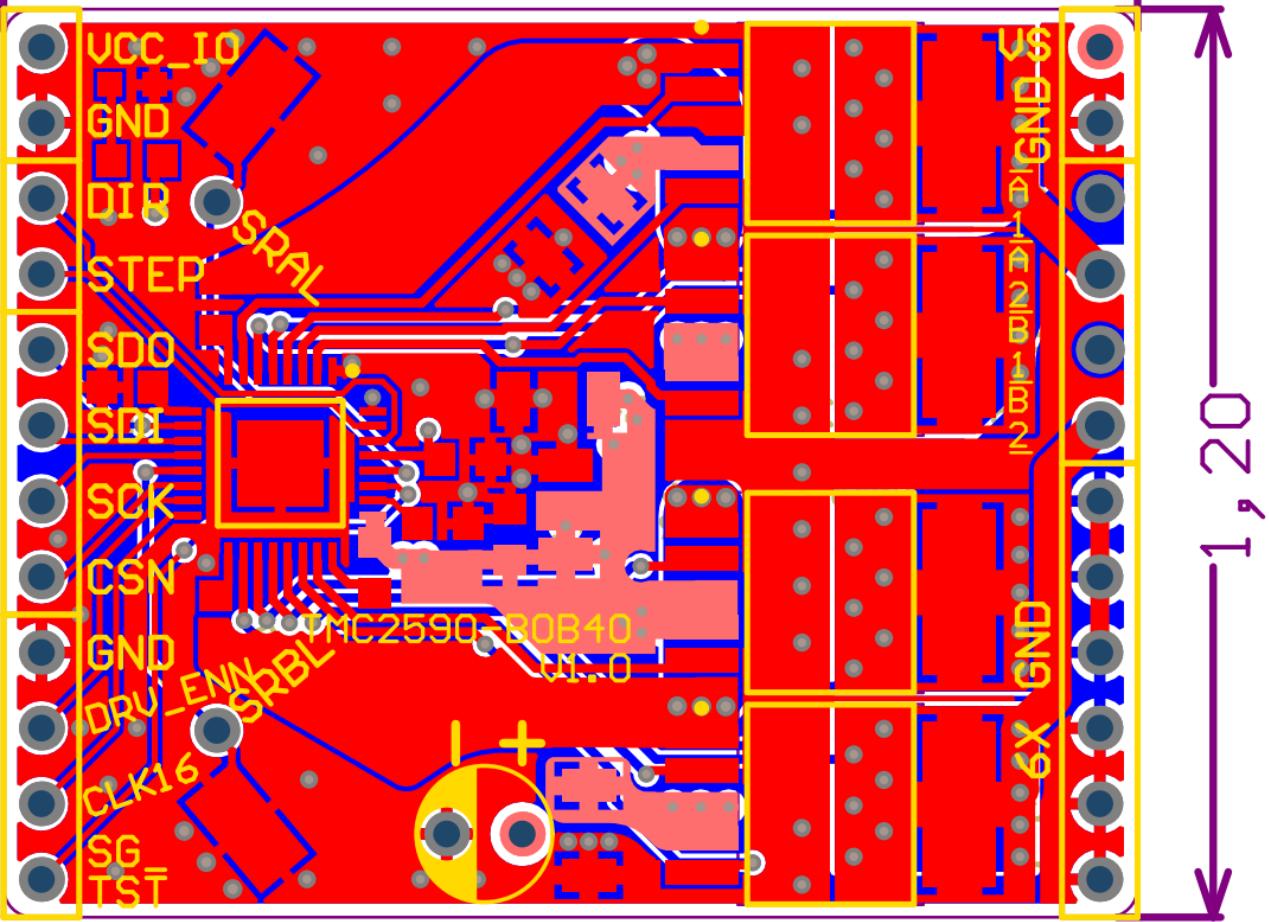
VS	VCC_IO
GND	GND
A1	SRAL DIR
A2	STEP
B1	SDO
B2	TMC2590-BOB40_V1.0
	<i>IC name: TMC2590</i>
GND	<i>Supply range: VS = 9...40V</i>
	<i>Max. current: IRMS = 2.8A</i>
GND	CSN
GND	DRV_GND
GND	SRBL
GND	CLK16
GND	SG_TST

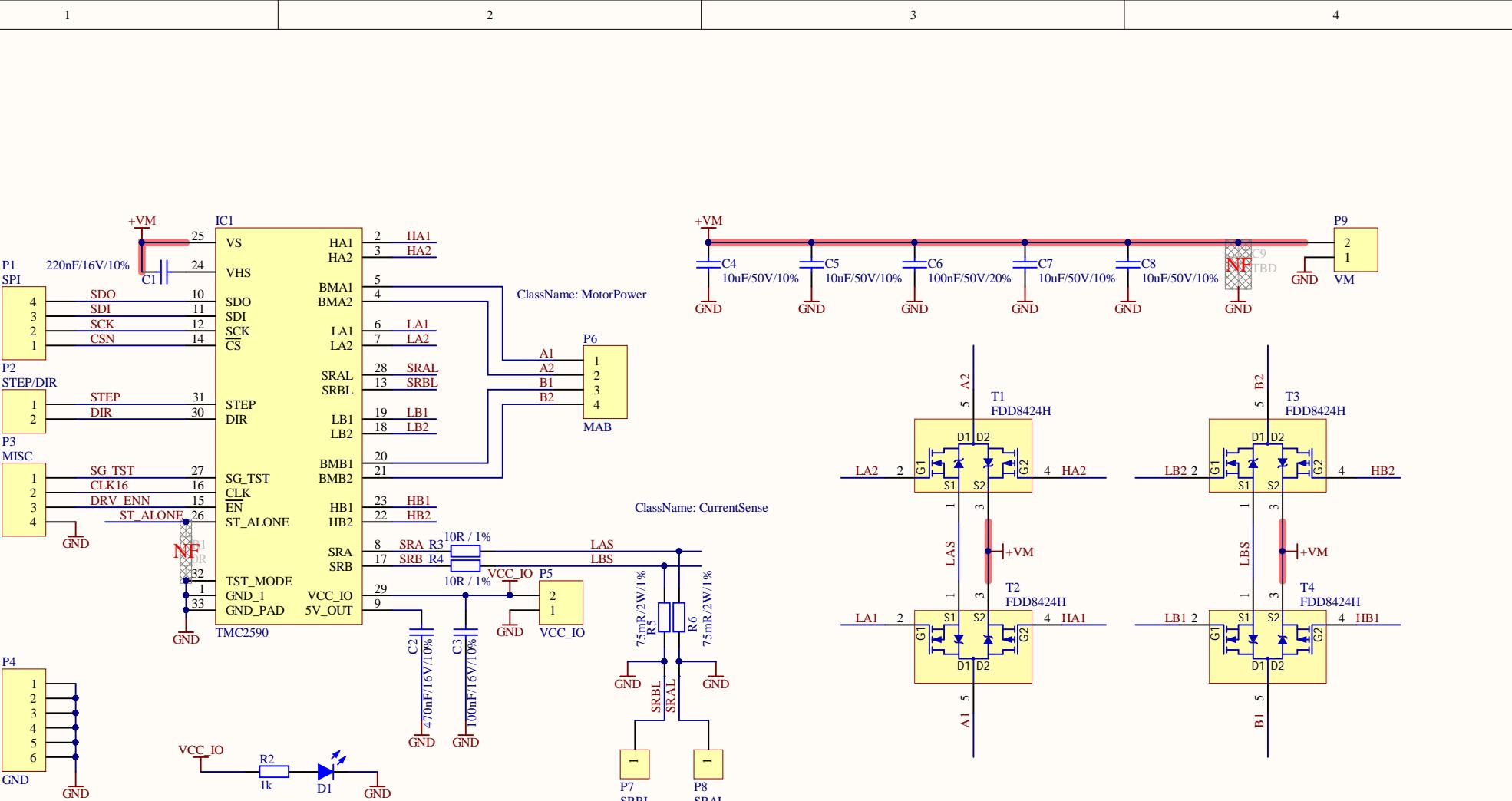


1,50

1,20

-1,50-





Title <i>TMC2590-BOB40</i>		Revision 1.0
Size A4	Number	
Date: 7.09.2018	Sheet	of
File: C:\Users\...\TMC2590_BOB40_V1_0.SchDoc	Drawn By:	

ChangeLog

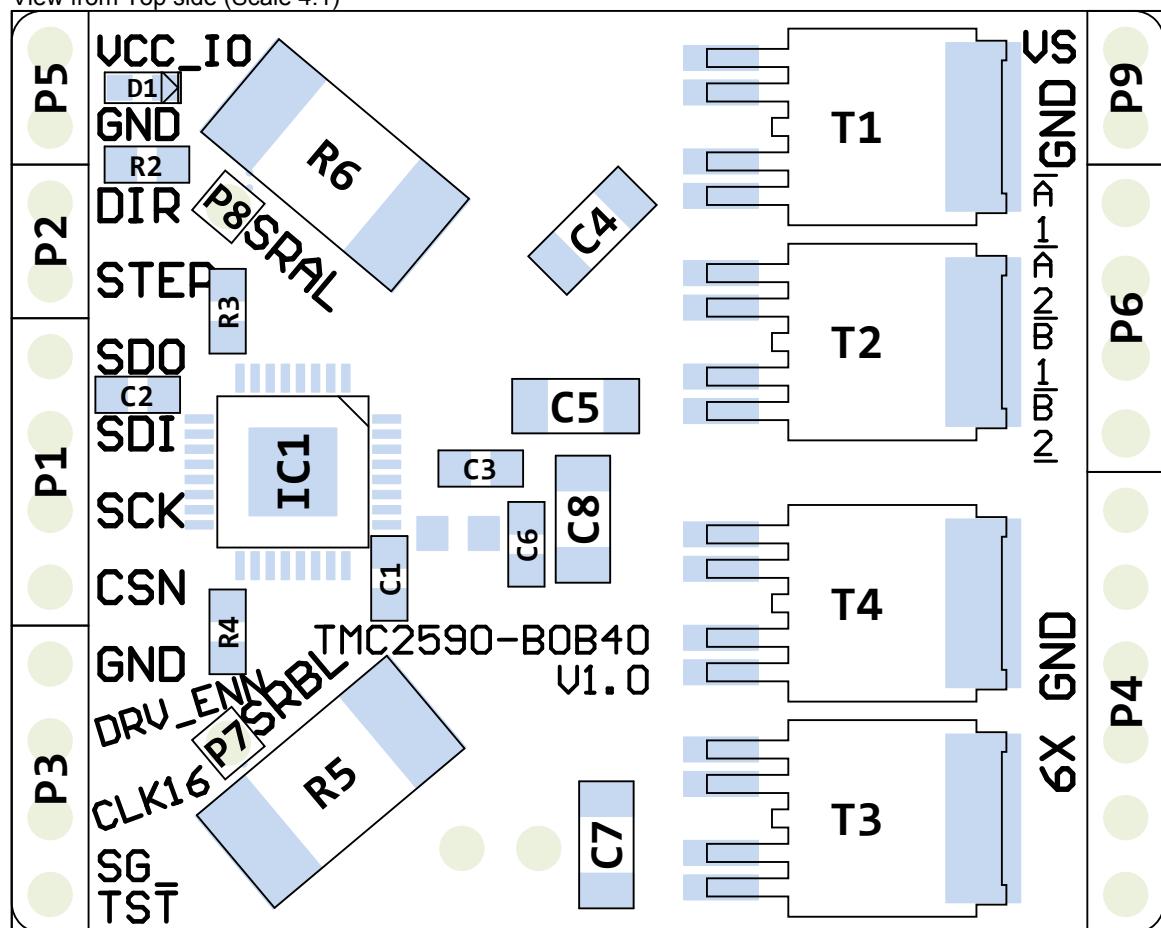
V1.0 - 20.08.2018 - Initial design

- 31.08.2018 - Changed board pinout to be same as TMC262-40-BOB
 - Placed SRAL and SRBL pins at current sense resistor GND terminals

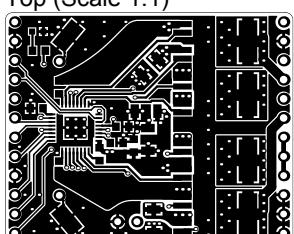
Title <i>TMC2590-BOB40</i>		
Size A4	Number	Revision 1.0
Date: 7.09.2018	Sheet	of
File: C:\Users\...\ChangeLog.SchDoc	Drawn By:	

A B C D E

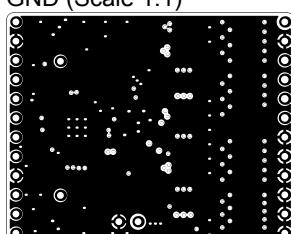
View from Top side (Scale 4:1)



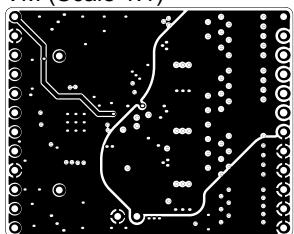
Top (Scale 1:1)



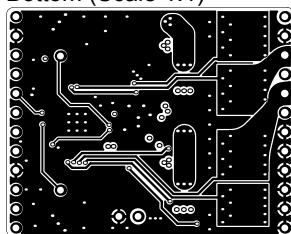
GND (Scale 1:1)



VM (Scale 1:1)



Bottom (Scale 1:1)



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Version: 1.0

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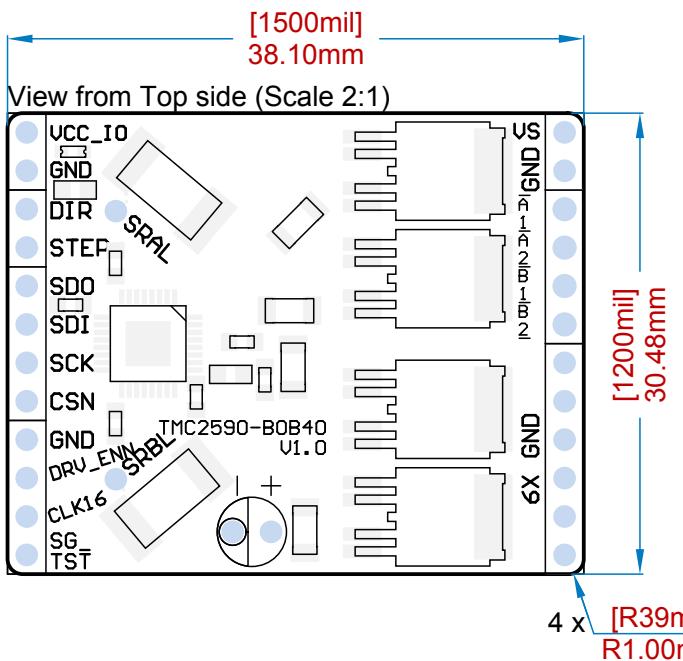
Layer Stack Legend

	Material	Layer	Thickness	Dielectric Material	Type	Gerber
1	Top Paste				Paste Mask	GTP
	Top Overlay				Legend	GTO
	Surface Material	Top Solder	0.010mm(0.400mil)	Solder Resist	Solder Mask	GTS
	Copper	Top	0.018mm(0.689mil)		Signal	GTL
	Core		0.254mm(10.000mil)	FR-4	Dielectric	
	Copper	GND	0.035mm(1.378mil)		Signal	G1
	Prepreg		1.000mm(39.370mil)	FR-4	Dielectric	
2	Copper	VM	0.035mm(1.378mil)		Signal	G2
	Core		0.254mm(10.000mil)	FR-4	Dielectric	
	Copper	Bottom	0.018mm(0.689mil)		Signal	GBL
	Surface Material	Bottom Solder	0.010mm(0.400mil)	Solder Resist	Solder Mask	GBS
		Bottom Overlay			Legend	GBO
		Bottom Paste			Paste Mask	GBP

Total thickness: 1.633mm(64.304mil)

Notes:

1. MATERIAL : FR-4-2 NATURAL EPOXY/FIBERGLASS
2. APPLY SOLDERMASK ON BOTH SIDES
COLOR: WHITE
FABRICATOR SHALL MAKE NECESSARY MODIFICATIONS TO SOLDERMASK PHOTOPLOT FILES FOR OPTIMAL SOLDERMASK COVERAGE BETWEEN FINE PITCH COMPONENT LEADS.
3. FINISH ALL EXPOSED COPPER SURFACES WITH IMMERSION GOLD.
4. HOLE SIZES APPLY AFTER PLATING.
5. APPLY SILKSCREEN TO BOTH SIDES
COLOR: BLACK
FABRICATOR SHALL MAKE NECESSARY MODIFICATIONS TO LEGEND PHOTOPLOT FILES TO ENSURE NO LEGEND INK COVERS ANY COMPONENT PAD OR VIA PAD.
6. MODIFIED PHOTOPLOT FILES ARE TO BE RETURNED BEFORE ORDER DELIVERED.
7. ALL PRINTED CIRCUITBOARD NETS SHALL BE ELECTRICALLY TESTED FOR OPENS AND SHORTS.
8. FABRICATION OF PCB TO COMPLY WITH IPC-A-600 CLASS II . CURRENT REVISION.



Title: TMC2590-BOB40

Version: 1.0

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BOM

Project: TMC2590-BOB40

Version: 1.0

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