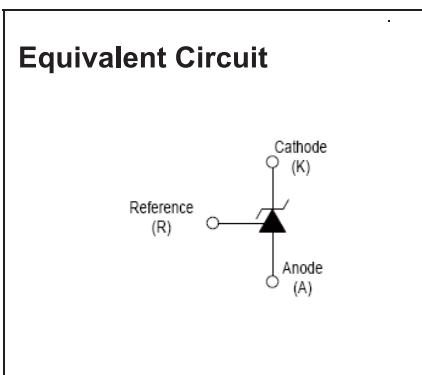
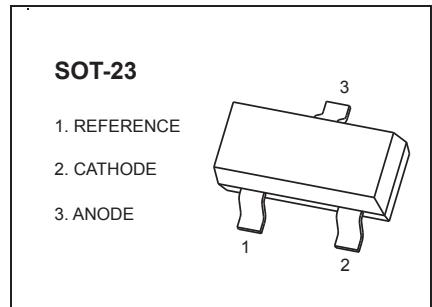


## DEVICE DESCRIPTION

The TL431 is a three-terminal adjustable shunt regulator offering excellent temperature stability. This device has a typical dynamic output impedance of  $0.2\Omega$ . The device can be used as a replacement for zener diodes in many applications.

## FEATURES

- The output voltage can be adjusted to 36V
- Low dynamic output impedance, its typical value is  $0.2\Omega$
- Trapping current capability is 1 to 100mA
- Low output noise voltage
- Fast on-state response
- The effective temperature compensation in the working range of full temperature
- The typical value of the equivalent temperature factor in the whole temperature scope is  $50 \text{ ppm}/^\circ\text{C}$



## APPLICATION

- Shunt Regulator
- High-Current Shunt Regulator
- Precision Current Limiter

## ABSOLUTE MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

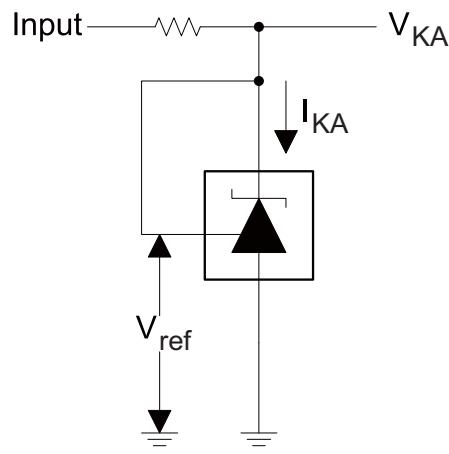
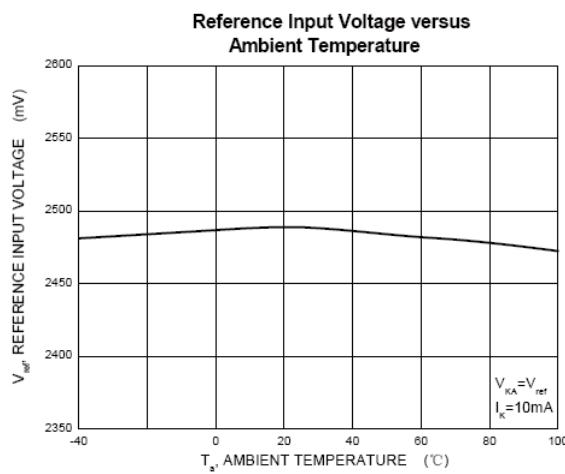
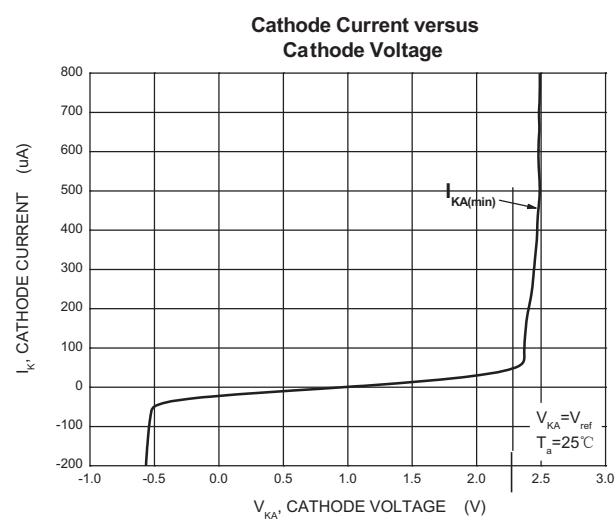
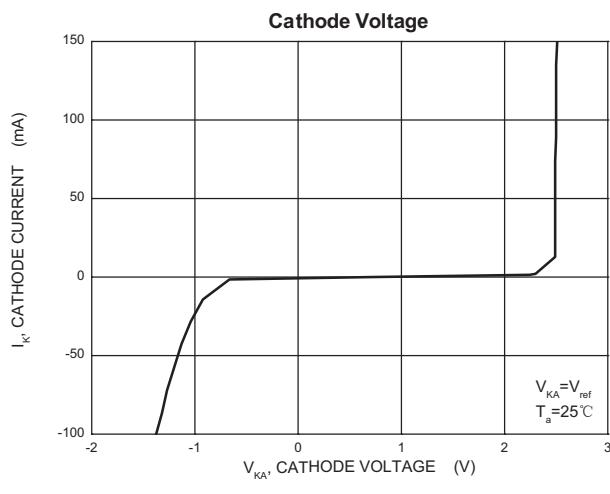
Parameter	Symbol	Value	Unit
Cathode Voltage	$V_{KA}$	36	V
Cathode Current Range (Continuous)	$I_{KA}$	-100~+150	mA
Reference Input Current Range	$I_{ref}$	0.05~+10	mA
Power Dissipation	$P_D$	300	mW
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	417	°C/W
Operating Temperature	$T_{opr}$	-25~+85	°C
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{STG}$	-65~+150	°C

## CLASSIFICATION of $V_{ref}$

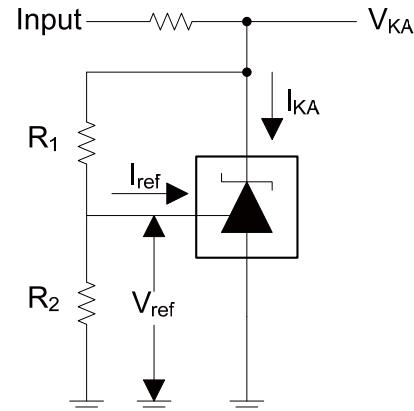
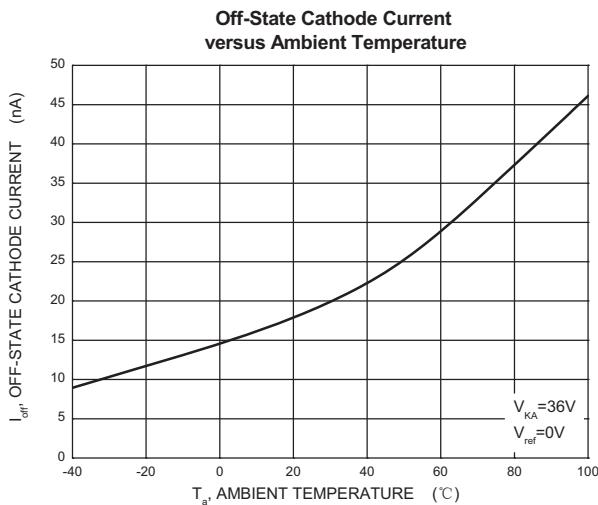
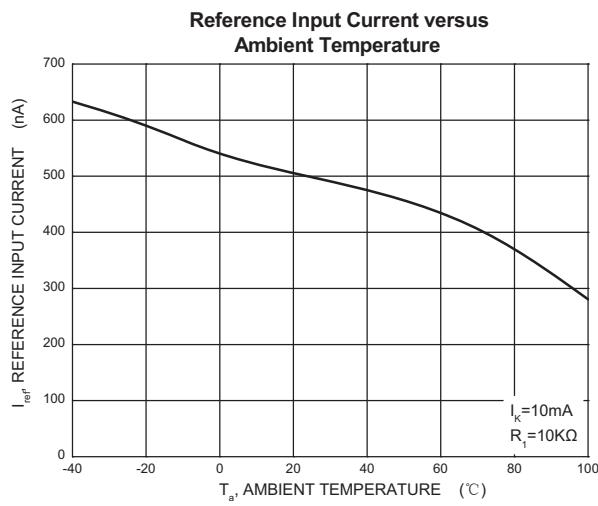
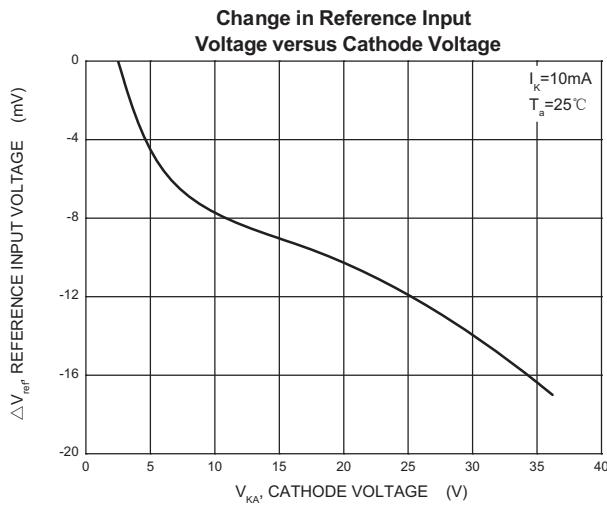
Rank	0.5%	1%
Range	2.487-2.513	2.475-2.525
MARKING		431

**ELECTRICAL CHARACTERISTICS ( $T_a=25^\circ\text{C}$  unless otherwise specified)**

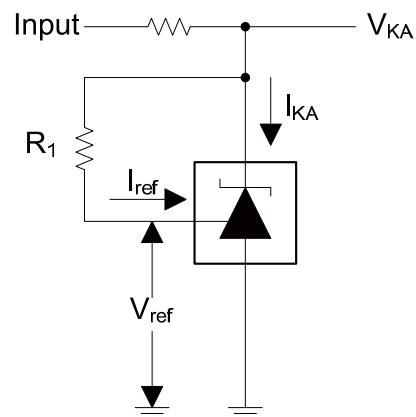
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Reference input voltage	$V_{\text{ref}}$	$V_{KA}=V_{\text{REF}}, I_{KA}=10\text{mA}$	2.475	2.5	2.525	V
Deviation of reference Input voltage over temperature (note)	$\Delta V_{\text{ref}}/\Delta T$	$V_{KA}=V_{\text{REF}}, I_{KA}=10\text{mA}$ $T_{\text{MIN}} \leq T_a \leq T_{\text{MAX}}$		4.5	17	mV
Ratio of change in reference Input voltage to the change in cathode voltage	$\Delta V_{\text{ref}}/\Delta V_{KA}$	$I_{KA}=10\text{mA}$	$\Delta V_{KA} = 10\text{V} \sim V_{\text{REF}}$		-1.0	mV/V
			$\Delta V_{KA} = 36\text{V} \sim 10\text{V}$		-0.5	mV/V
Reference input current	$I_{\text{ref}}$	$I_{KA}=10\text{mA}, R_1=10\text{k}\Omega$ $R_2=\infty$		1.5	4	$\mu\text{A}$
Deviation of reference input current over full temperature range	$\Delta I_{\text{ref}}/\Delta T$	$I_{KA}=10\text{mA}, R_1=10\text{k}\Omega$ $R_2=\infty$ $T_a=-25 \text{ to } 85^\circ\text{C}$		0.4	1.2	$\mu\text{A}$
Minimum cathode current for regulation	$I_{KA(\text{min})}$	$V_{KA}=V_{\text{REF}}$		0.45	1.0	mA
Off-state cathode current	$I_{KA(\text{OFF})}$	$V_{KA}=36\text{V}, V_{\text{REF}}=0$		0.05	1.0	$\mu\text{A}$
Dynamic impedance	$Z_{KA}$	$V_{KA}=V_{\text{REF}}, I_{KA}=1 \text{ to } 100\text{mA}$ $f \leq 1.0\text{kHz}$		0.15	0.5	$\Omega$

Note:  $T_{\text{MIN}}=-25^\circ\text{C}$ ,  $T_{\text{MAX}}=+85^\circ\text{C}$ 
**Typical Characteristics**

**Test Circuit for  $V_{KA}=V_{\text{ref}}$**

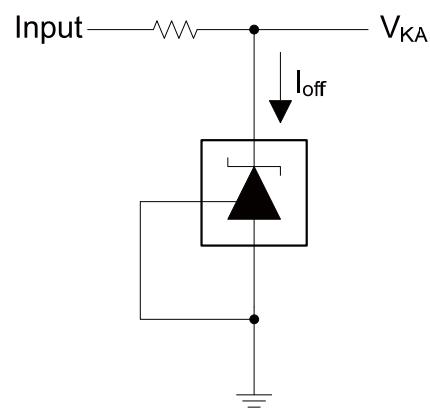
## Typical Characteristics



Test Circuit for  $V_{KA} = V_{ref}(1 + R_1/R_2) + R_1 * I_{ref}$



Test Circuit for  $I_{ref}$

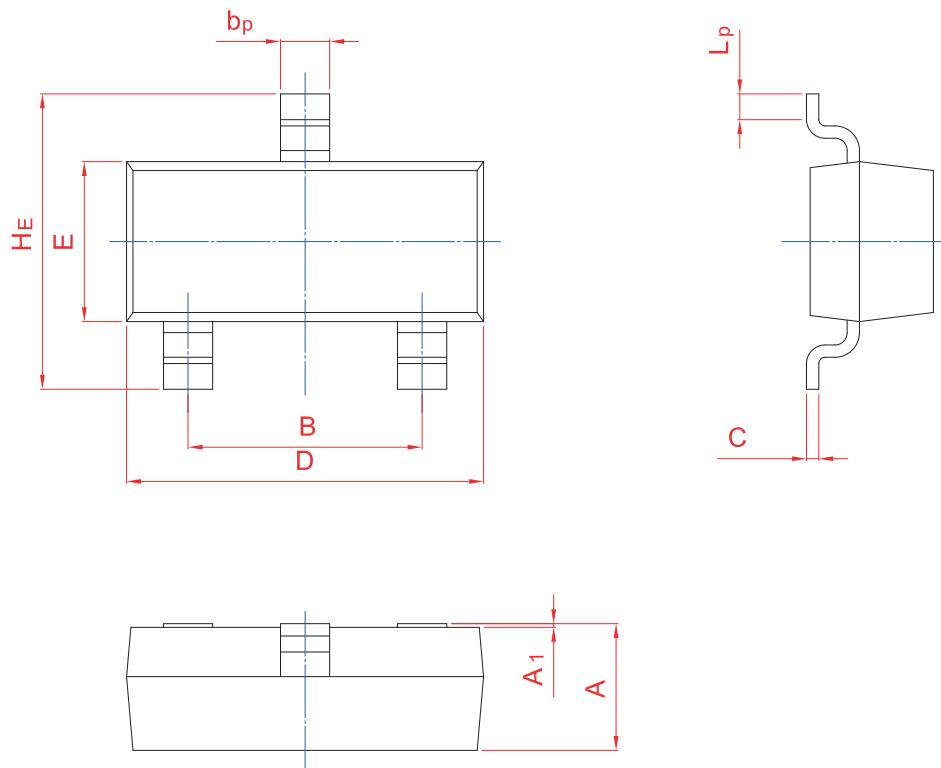
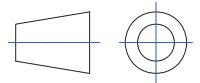


Test Circuit for  $I_{off}$

## PACKAGE OUTLINE

**SOT-23**

Plastic surface mounted package; 3 leads



UNIT	A	B	b <sub>p</sub>	C	D	E	H <sub>E</sub>	A <sub>1</sub>	L <sub>p</sub>
mm	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	0.50 0.20