

Eytan Biala  
Readme - PA4

This project is able to measure the size of the cache and the block size for a CPU by using capacity misses. When we have these hits and misses, we can measure the time with varying number of hits/misses, and then calculate the needed values.

We can try various distances to guess the cache line size, since as long as the distance is above the block size, the access will be a miss.

From my results, the caches on the iLab machines seem to range from 3 - 8 MB, depending on which machine. The cache block size is normally 256.