Introduction Le calcul par pile Intel 8087

# Microprocesseurs (MIC)

Chap. 7: Le co-processeur Intel 8087

#### Sommaire

- Introduction
- 2 Le calcul par pile
- Intel 8087
  - Pile et registres
  - Instructions

#### Les co-processeurs

- Co-processeur : circuit destiné à ajouter une fonction à un processeur classique.
  - Co-processeurs arithmétiques (calcul en virgule flottante)
  - Co-processeurs graphiques (2D, 3D)
  - Co-processeurs spécialisés dans le chiffrement
- But : augmenter les performances de la machine pour un type de calcul précis.

#### Le co-processeur Intel 8087

- Co-processeur arithmétique pour la gamme x86
- x86 : uniquement calculs de nombres entiers
  - Calculs de réels simulés avec des entiers représentant des nombres à virgule fixe implicite
  - Ex: 2.5 + 3.4
    - On représente les nombre comme 250 et 340 (si deux décimales implicites)
    - La somme donne 590 ce qui correspond à 5.9
- Le 8087 introduit des instructions de calcul de nombres « à virgule flottante »

#### Le co-processeur Intel 8087

- Disponible dès le 8086, comme circuit intégré séparé
- Versions ultérieures : 80187, 80287, 80387
- À partir du 80486DX (32 bits), le co-processeur est intégré dans la même puce que le processeur «classique»

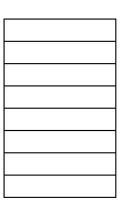


Figure: Processeur 80386 avec co-processeur 80387 séparé

#### Sommaire

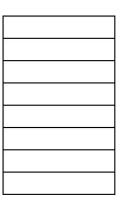
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• Calcul par pile

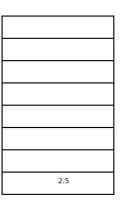


• Calcul par pile

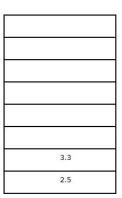
• Exemple I : 2.5 + 3.3



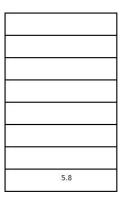
- Calcul par pile
- Exemple I : 2.5 + 3.3
  - PUSH 2.5



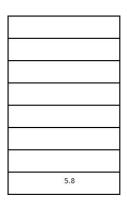
- Calcul par pile
- Exemple I : 2.5 + 3.3
  - PUSH 2.5
  - PUSH 3.3



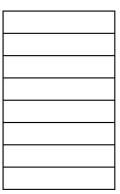
- Calcul par pile
- Exemple I : 2.5 + 3.3
  - PUSH 2.5
  - PUSH 3.3
  - SOMME



- Calcul par pile
- Exemple I : 2.5 + 3.3
  - PUSH 2.5
  - PUSH 3.3
  - SOMME
- Le résultat final est au fond de la pile



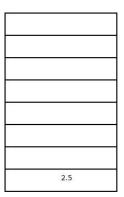
$$(2.5+3.3)\times(1.2+0.8)$$



• Exemple II:

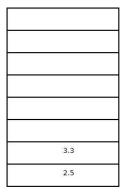
$$(2.5+3.3)\times(1.2+0.8)$$

PUSH 2.5



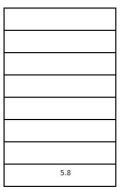
$$(2.5+3.3)\times(1.2+0.8)$$

- PUSH 2.5
- PUSH 3.3



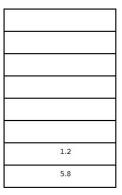
$$(2.5+3.3)\times(1.2+0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME



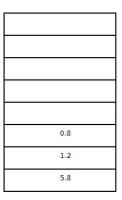
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2



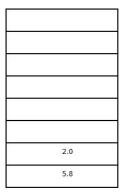
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2
- PUSH 0.8



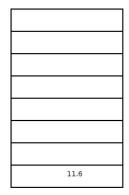
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2
- PUSH 0.8
- SOMME



$$(2.5+3.3)\times(1.2+0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2
- PUSH 0.8
- SOMME
- PRODUIT



- Principe
  - Les opérandes sont pushées sur la pile
  - Les calculs poppent les opérandes et pushent le résultat
  - Le résultat final est disponible au fond de la pile
- Technique très employée
  - Compilateurs
  - Certains processeurs et co-processeurs
  - Co-processeur Intel 8087
  - . . .

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- 3 Intel 8087
  - Pile et registres
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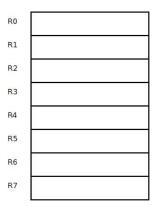
#### La pile du 8087 : registres

• Pile hardware dans le co-processeur

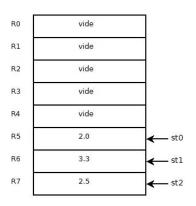
R0	
R1	;
R2	100
R3	
R4	-
R5	10
R6	
R7	30

#### La pile du 8087 : registres

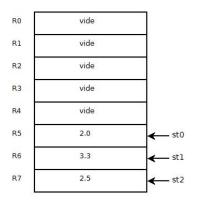
- Pile hardware dans le co-processeur
- 8 registres : R0 à R7



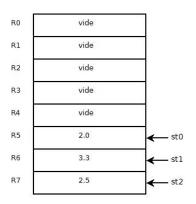
• Les registres R0 à R7 ne sont pas employés tel quels



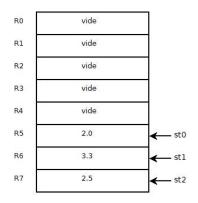
- Les registres *R0* à *R7* ne sont pas employés tel quels
- Raccourcis de notation : st0 à st7



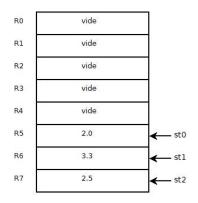
- Les registres *R0* à *R7* ne sont pas employés tel quels
- Raccourcis de notation : st0 à st7
  - st0 = sommet de pile



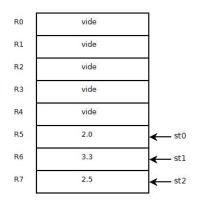
- Les registres *R0* à *R7* ne sont pas employés tel quels
- Raccourcis de notation : st0 à st7
  - st0 = sommet de pile
  - st1 = élément suivant



- Les registres *R0* à *R7* ne sont pas employés tel quels
- Raccourcis de notation : st0 à st7
  - st0 = sommet de pile
  - st1 = élément suivant
  - st2 = élément suivant



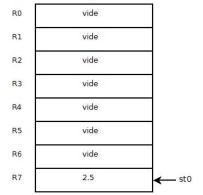
- Les registres *R0* à *R7* ne sont pas employés tel quels
- Raccourcis de notation : st0 à st7
  - st0 = sommet de pile
  - st1 = élément suivant
  - st2 = élément suivant
  - ..



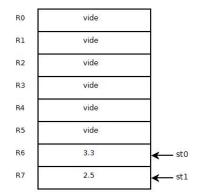
• Pile vide au départ

R0	vide
R1	vide
R2	vide
R3	vide
R4	vide
R5	vide
R6	vide
R7	vide

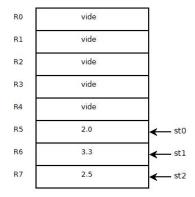
- Pile vide au départ
- PUSH 2.5



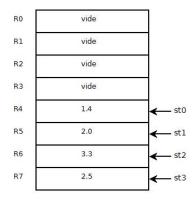
- Pile vide au départ
- PUSH 2.5
- PUSH 3.3



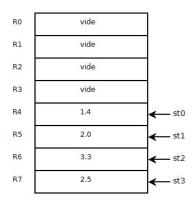
- Pile vide au départ
- PUSH 2.5
- PUSH 3.3
- PUSH 2.0



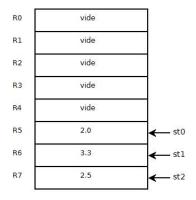
- Pile vide au départ
- PUSH 2.5
- PUSH 3.3
- PUSH 2.0
- PUSH 1.4



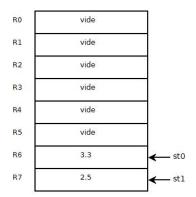
 Partons de la pile obtenue dans l'exemple précédent



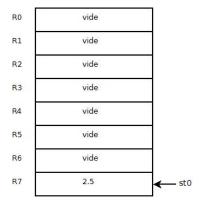
- Partons de la pile obtenue dans l'exemple précédent
- POP



- Partons de la pile obtenue dans l'exemple précédent
- POP
- POP



- Partons de la pile obtenue dans l'exemple précédent
- POP
- POP
- POP



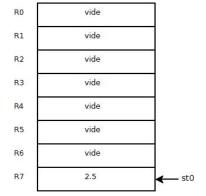
- Partons de la pile obtenue dans l'exemple précédent
- POP
- POP
- POP
- POP

R0	vide
R1	vide
R2	vide
R3	vide
R4	vide
R5	vide
R6	vide
R7	vide

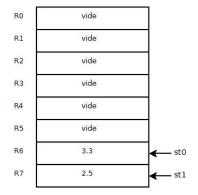
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

R0	vide
R1	vide
R2	vide
R3	vide
R4	vide
R5	vide
R6	vide
R7	vide

- $\bullet$  (2.5 + 3.3)  $\times$  (1.2 + 0.8)
- PUSH 2.5

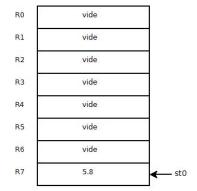


- $\bullet$  (2.5 + 3.3)  $\times$  (1.2 + 0.8)
- PUSH 2.5
- PUSH 3.3



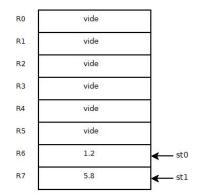
$$\bullet$$
 (2.5 + 3.3) × (1.2 + 0.8)

- PUSH 2.5
- PUSH 3.3
- SOMME



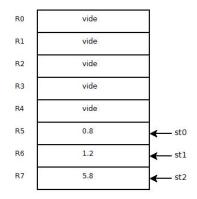
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2



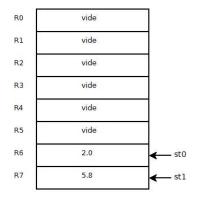
$$\bullet$$
 (2.5 + 3.3) × (1.2 + 0.8)

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2
- PUSH 0.8



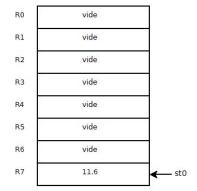
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$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
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- SOMME
- PUSH 1.2
- PUSH 0.8
- SOMME



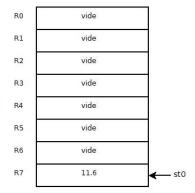
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$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2
- PUSH 0.8
- SOMME
- PRODUIT



• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- PUSH 2.5
- PUSH 3.3
- SOMME
- PUSH 1.2
- PUSH 0.8
- SOMME
- PRODUIT
- Le résultat est dans st0



### L'encodage des réels sous Intel 8087

- Les registres st0 à st7 contiennent des réels de 80 bits
- Valeurs possibles dans les registres :
  - Nombre réel (80 bits)
  - « vide »
  - « infini »
  - « NAN »
- tword = 10 octets (t = ten)
  - Même usage que byte, word, dword

### L'encodage des réels sous Intel 8087

- Encodage des réels en format « x86 Extended Precision »
  - Bit de signe s
  - Exposant e sur 15 bits (avec biais de 16383)
  - Mantisse m sur 64 bits (partie entière en bit 63, partie fractionnaire au-delà)



• Formule :

$$(-1)^s \times m \times 2^{e-16383}$$

### L'encodage des réels sous Intel 8087

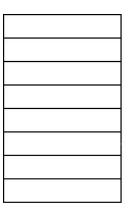
• Exemple : 4001 C0000000000000000



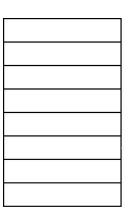
- s = 0
- $e = 1000000000001_2 = 16385_{10}$
- $m = 1.100000....0_2 = 1.5_{10}$
- Formule :

$$(-1)^s \times m \times 2^{e-16383} = (-1)^0 \times 1.5 \times 2^{16385-16383}$$
  
=  $1 \times 1.5 \times 2^2$   
=  $6$ 

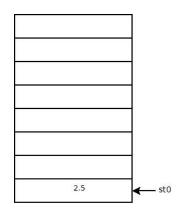
- FLD = Floating-point LoaD
- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti



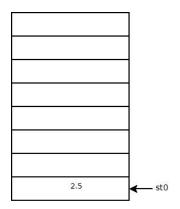
- FLD = Floating-point LoaD
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- Exemple :



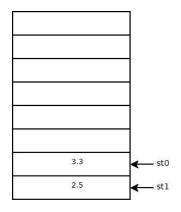
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- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti
- Exemple :
  - FLD tword 2.5



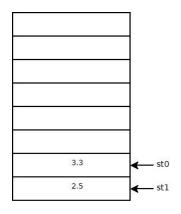
- FLD = Floating-point LoaD
- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti
- Exemple :
  - FLD tword 2.5
    - $st0 \leftarrow 2.5$



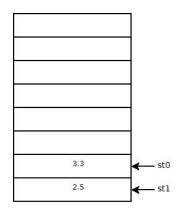
- FLD = Floating-point LoaD
- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti
- Exemple :
  - FLD tword 2.5
    - $st0 \leftarrow 2.5$
  - FLD tword 3.3



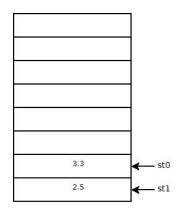
- FLD = Floating-point LoaD
- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti
- Exemple :
  - FLD tword 2.5
    - $st0 \leftarrow 2.5$
  - FLD tword 3.3
    - st1  $\leftarrow$  2.5



- FLD = Floating-point LoaD
- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti
- Exemple :
  - FLD tword 2.5
    - $st0 \leftarrow 2.5$
  - FLD tword 3.3
    - st1  $\leftarrow$  2.5
    - st0  $\leftarrow$  3.3



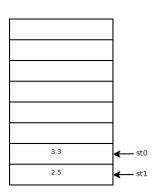
- FLD = Floating-point LoaD
- Syntaxe :
  - FLD param
  - param est pushé au sommet
  - param = immédiat, mémoire, sti
- Exemple :
  - FLD tword 2.5
    - $st0 \leftarrow 2.5$
  - FLD tword 3.3
    - st1  $\leftarrow$  2.5
    - st0  $\leftarrow$  3.3



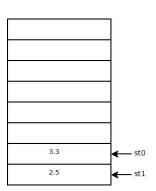
• Variantes de FLD :

FLDZ: PUSH 0.0
 FLD1: PUSH 1.0
 FLDPI: PUSH π

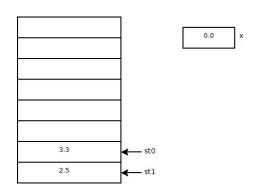
- FST = Floating-point STore
- Syntaxe: FST param
- Effet :
  - param ← sommet de pile



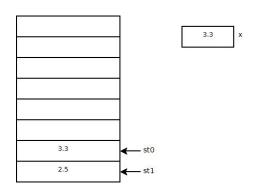
- FST = Floating-point STore
- Syntaxe: FST param
- Effet :
  - param ← sommet de pile
- Exemple :



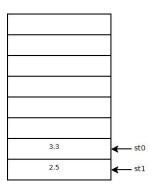
- FST = Floating-point STore
- Syntaxe : FST param
- Effet :
  - param ← sommet de pile
- Exemple :
  - x DT 0.0



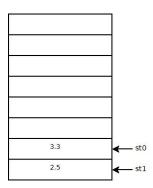
- FST = Floating-point STore
- Syntaxe: FST param
- Effet :
  - param ← sommet de pile
- Exemple :
  - x DT 0.0
  - FST tword [x]



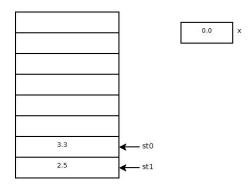
- FSTP = Floating-point STore Pop
- Syntaxe: FSTP param
- Effet :
  - param ← sommet de pile
  - POP



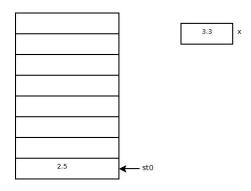
- FSTP = Floating-point STore Pop
- Syntaxe: FSTP param
- Effet :
  - param ← sommet de pile
  - POP
- Exemple :



- FSTP = Floating-point STore Pop
- Syntaxe : FSTP param
- Effet :
  - param ← sommet de pile
  - POP
- Exemple :
  - x DT 0.0



- FSTP = Floating-point STore Pop
- Syntaxe: FSTP param
- Effet :
  - param ← sommet de pile
  - POP
- Exemple :
  - x DT 0.0
  - FSTP tword [x]

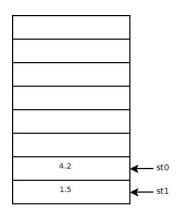


# Opérations arithmétiques du 8087

- Opérations binaires
  - Addition
  - Soustraction
  - Multiplication
  - Division
- Opérations unaires
  - Racine carrée
  - Valeur absolue
  - Changement de signe
  - Conversion réel-entier
  - Cosinus
  - . . .

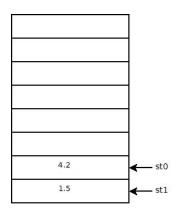
#### Addition

- FADD = Floating-point ADD
- $\bullet$  FADD sti, stj
  - $\mathsf{st}i \leftarrow \mathsf{st}i + \mathsf{st}j$



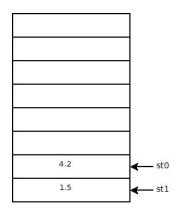
#### Addition

- FADD = Floating-point ADD
- $\bullet$  FADD sti, stj
  - $\operatorname{st} i \leftarrow \operatorname{st} i + \operatorname{st} j$
- Exemple :



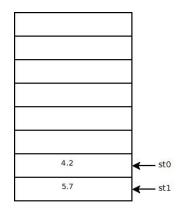
#### Addition

- FADD = Floating-point ADD
- FADD sti, stj
  - $\operatorname{st} i \leftarrow \operatorname{st} i + \operatorname{st} j$
- Exemple :
  - FADD st1, st0

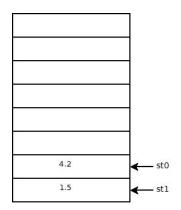


#### Addition

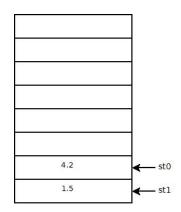
- FADD = Floating-point ADD
- FADD sti, stj
  - $\mathtt{st}i \leftarrow \mathtt{st}i + \mathtt{st}j$
- Exemple :
  - FADD st1, st0
  - $st1 \leftarrow st1 + st0$



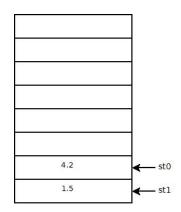
- FADDP = Floating-point ADD Pop
- FADDP sti, stj
  - $\mathsf{st}i \leftarrow \mathsf{st}i + \mathsf{st}j$
  - POP



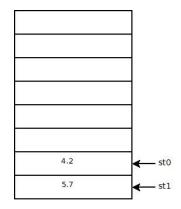
- FADDP = Floating-point ADD Pop
- FADDP sti, stj
  - $\mathsf{st}i \leftarrow \mathsf{st}i + \mathsf{st}j$
  - POP
- Exemple :



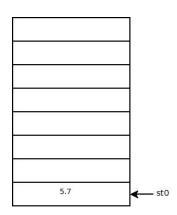
- FADDP = Floating-point ADD Pop
- FADDP sti, stj
  - $\mathsf{st}i \leftarrow \mathsf{st}i + \mathsf{st}j$
  - POP
- Exemple :
  - FADDP st1, st0



- FADDP = Floating-point ADD Pop
- FADDP sti, stj
  - $\mathsf{st}i \leftarrow \mathsf{st}i + \mathsf{st}j$
  - POP
- Exemple :
  - FADDP st1, st0
  - $st1 \leftarrow st1 + st0$



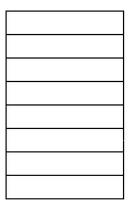
- FADDP = Floating-point ADD Pop
- FADDP sti, stj
  - $\operatorname{st} i \leftarrow \operatorname{st} i + \operatorname{st} j$
  - POP
- Exemple :
  - FADDP st1, st0
  - $st1 \leftarrow st1 + st0$
  - POP



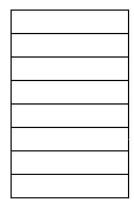
#### Addition: raccourcis

- FADDP sti
  - raccourci pour FADDP sti, st0
- FADDP
  - raccourci pour FADDP st1, st0

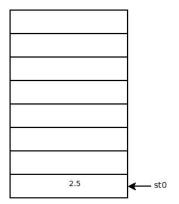
• 2.5 + 3.3



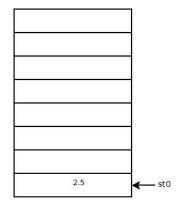
- 2.5 + 3.3
- FLD tword 2.5



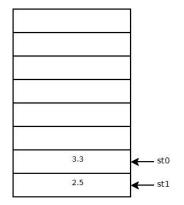
- 2.5 + 3.3
- FLD tword 2.5



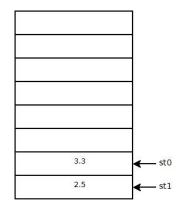
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3



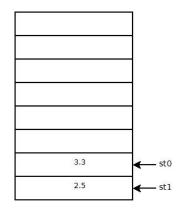
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3



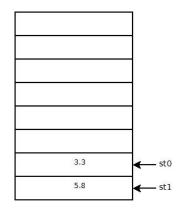
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3
- FADDP st1, st0



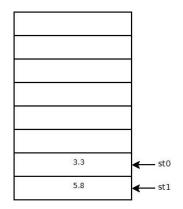
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3
- FADDP st1, st0
  - $st1 \leftarrow st1 + st0$



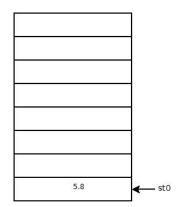
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3
- FADDP st1, st0
  - $st1 \leftarrow st1 + st0$



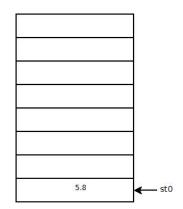
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3
- FADDP st1, st0
  - $st1 \leftarrow st1 + st0$
  - POP



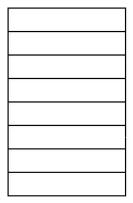
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3
- FADDP st1, st0
  - $st1 \leftarrow st1 + st0$
  - POP



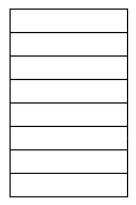
- 2.5 + 3.3
- FLD tword 2.5
- FLD tword 3.3
- FADDP st1, st0
  - $st1 \leftarrow st1 + st0$
  - POP
- Note: on abrègera désormais FADDP st1, st0 en FADDP



$$\bullet$$
 2.5 + 3.3 + 4.1

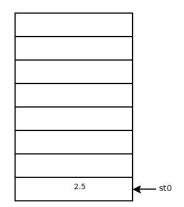


$$2.5 + 3.3 + 4.1$$



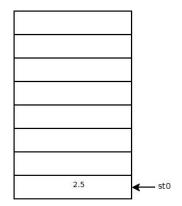
$$\bullet$$
 2.5 + 3.3 + 4.1

• FLD tword 2.5



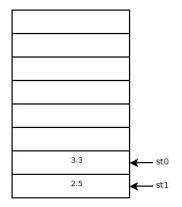
$$\bullet$$
 2.5 + 3.3 + 4.1

- FLD tword 2.5
- FLD tword 3.3



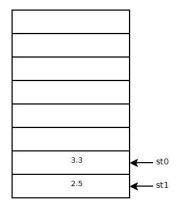
$$2.5 + 3.3 + 4.1$$

- FLD tword 2.5
- FLD tword 3.3



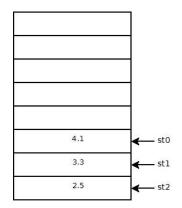
$$2.5 + 3.3 + 4.1$$

- FLD tword 2.5
- FLD tword 3.3
- FLD tword 4.1



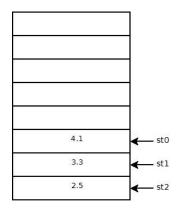
$$2.5 + 3.3 + 4.1$$

- FLD tword 2.5
- FLD tword 3.3
- FLD tword 4.1



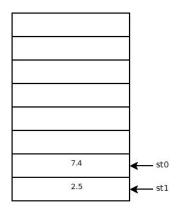
$$2.5 + 3.3 + 4.1$$

- FLD tword 2.5
- FLD tword 3.3
- FLD tword 4.1
- FADDP



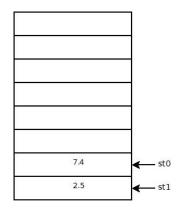
$$2.5 + 3.3 + 4.1$$

- FLD tword 2.5
- FLD tword 3.3
- FLD tword 4.1
- FADDP



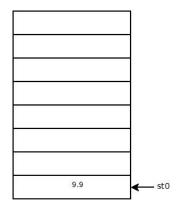
$$2.5 + 3.3 + 4.1$$

- FLD tword 2.5
- FLD tword 3.3
- FLD tword 4.1
- FADDP
- FADDP



$$2.5 + 3.3 + 4.1$$

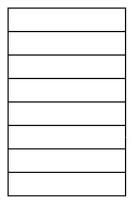
- FLD tword 2.5
- FLD tword 3.3
- FLD tword 4.1
- FADDP
- FADDP



#### Autres opérations binaires

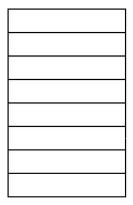
- Soustraction
  - FSUB/FSUBP (Floating-point SUBstract Pop)
- Multiplication
  - FMUL/FMULP (Floating-point MULtiply Pop)
- Division
  - FDIV/FDIVP (Floating-point DIVide Pop)
- Même syntaxe que l'addition (raccourcis inclus)

• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$



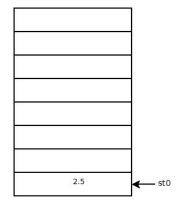
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

• FLD tword 2.5



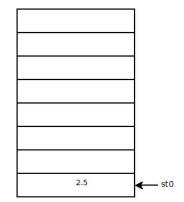
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

• FLD tword 2.5



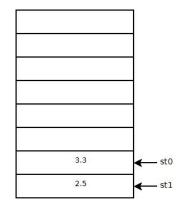
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3



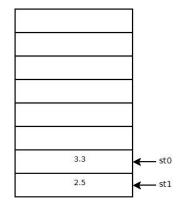
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3



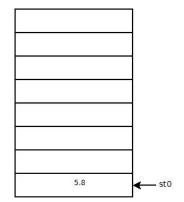
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP



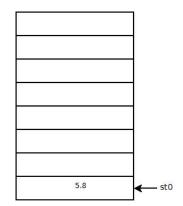
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP



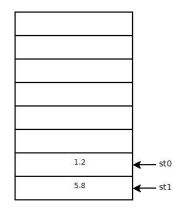
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2



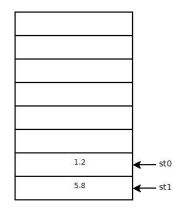
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2



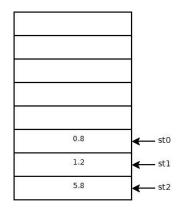
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2
- FLD tword 0.8



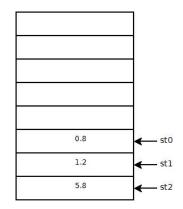
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2
- FLD tword 0.8



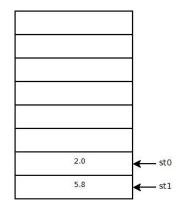
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2
- FLD tword 0.8
- FADDP



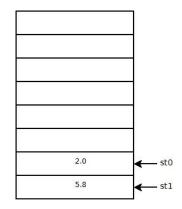
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2
- FLD tword 0.8
- FADDP



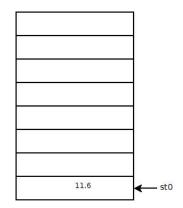
• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2
- FLD tword 0.8
- FADDP
- FMULP



• 
$$(2.5 + 3.3) \times (1.2 + 0.8)$$

- FLD tword 2.5
- FLD tword 3.3
- FADDP
- FLD tword 1.2
- FLD tword 0.8
- FADDP
- FMULP



# Opérations unaires

- Valeur absolue
  - FABS = Floating-point ABSolute value
  - st0 ← |st0|
- Racine carrée
  - FSQRT = Floating-point SQuare RooT
  - $st0 \leftarrow \sqrt{st0}$
- Cosinus
  - FCOS = Floating-point COSinus
  - $st0 \leftarrow cos(st0)$
- Etc. . .

#### Conversions réel ← entier

- Rajout d'un I après le F initial (l=Integer)
- Conversion automatique réel ← entier
- Ex: FILD eax
  - eax (entier 32 bits) converti vers réel 80 bits puis pushé
- Ex: FISTP eax
  - réel 80 bits converti vers entier 32 bits puis poppé vers eax

#### Exemple complet

```
global main
section .data
\times 1
        DT
            3.5
                    ; point1
v1
        DT 1.6
×2
        DT 25.2
                    ; point2
y2
        DT 31.3
section .bss
dist
        RESD 1
                ; distance entre les points (convertie en entier)
section .text
main:
        FINIT
                     ; initialiser la pile
        FLD
                 tword [x1]
        FLD
                 tword [x2]
        FSUBP
        FLD st0
        FMULP
        FLD
                 tword [y1]
        FI D
                 tword [y2]
        FSURP
        FLD st0
        FMULP
        FADDP
        FSQRT
        FISTP dword [dist]
```

# Exemple complet

MOV eax,1 MOV ebx,0 INT 0x80

#### Références

- Manuel Intel (chap. 8)
  - http://www.intel.com/content/dam/www/public/us/en/documents/manuals/64-ia-32-architectures-software-developer-vol-1-manual.pdf